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Source: TSG-RAN WG1
To: TSG-RAN WG2
Cc:
Title: Response to LS on UE capabilities for Low Chip Rate TDD
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TSG RAN WG1 would like to thank TSG RAN WG2 for the LS on UE capabilities for Low Chip Rate TDD.

TSG RAN WG1 has initially looked at the table in the Tdoc R2-002394: "Proposal for reference UE radio access capability combinations ", which is the input paper for the technical report TR 25.843: 1.28 Mcps TDD UE Radio Access Capabilities.

In order to do some further verification of the physical layer parameters, TSG RAN WG1 still needs more time before CR to TR 25.926 is finalised.

TSG RAN WG1 would like to draw the attention of WG2 to a typo in table 6.2.2.1, the parameter 'maximum sum of number of bits of all turbo coded transport blocks being received at an arbitrary time instant' for the 2048kbps class. Obviously, the original value (20480) that is valid for FDD and 3.84 Mcps TDD has been accidentally replaced by the value (10240) that is valid for 1.28 Mcps TDD only. TSG RAN WG1 would like to propose to replace the corresponding row in the table in the following way:

Maximum sum of number of bits of all turbo coded transport blocks being received at an arbitrary time instant	NA	3840	3840	6400	10240	20480 ⁽¹⁾ 10240 ⁽²⁾
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⁽¹⁾ FDD and 3.84 Mcps TDD

⁽²⁾ 1.28 Mcps TDD

TSG RAN WG1 suggests to include the table in the Tdoc R2-002394 in the Technical Report TR25.843 with the above modification and to add a note that

the parameters in the table still need to be verified by TSG RAN WG1 in more detail.

TSG RAN WG1 will continue the effort on the TR on 1.28 Mcps TDD UE Radio Access capabilities and will keep TSG RAN WG2 informed.