Source :   **RAN WG1 Chairman**

Title: **Revised TSG RAN WG1 time plan for Year 2000**

The following time-plan was submitted for the RAN#7 plenary in Tdoc RP-00-0173 after first round of work item descriptions was presented and as revised in Tdoc RP-00-0184. Some of the work items experienced changes that are now reflected in this work plan.

The work in year 2000 will be prioritised according to the following order:
- 1st priority: Corrections and clarifications of release 99 specifications
- 2nd Specification/feasibility study on release 00 work items

RELEASE 1999 ISSUES

For the work in year 2000, but still part of release-99, only very minor items have been identified at this point, such as out-of-synch default parameter reconfiguration by higher layers and downlink power control alignment with RAN WG3 (from the latter the issue is on the level of normative downlink inner loop power control specification in WG1/WG3)

The identified minor Release -99 issues are proposed to be solved with following schedule.
RAN WG1#12 (4/2000) is having joint Ad Hoc with WG3 on 11th of April 2000 on downlink power control and to clarify the necessary changes.

Also minor issue on out of synch parameterisation is to be solved at RAN WG1#12

Related CRs to be presented to TSG RAN#8.

RELEASE 2000 ISSUES

The following schedule is proposed for the Release 2000 work items with respect to RAN WG1. The names have been updated according to TSG RAN decisions

- TDD 1.28 Mchips functionality.
    RAN#8 Decisions whether to reflect the issue in the existing TDD (Rel.00) specifications or whether to create 1.28 Mchips/s specific specifications.
    RAN#9 & RAN 10 CRs for approval with main part of the CRs for RAN#9. New specifications (if any) under CR procedure from RAN#9 onwards (i.e. version 4.0.0 after RAN#9)

- Terminal power saving features (Techniques like DPCCH gating & Paging channel configuration change indicator have been raised earilier in TSG RAN WG1)
    Principles agreed and presented to RAN#8 for co-ordination with other RAN WGs
    CRs presented for approval for RAN#9

- Improvement of interfrequency and intersystem measurement
    Summary of the methods for RAN#8 for co-ordination with other RAN WGs.

CRs for RAN#9 for approval.

- High speed downlink packet access (study item)
  Results of the feasibility study from WG1 presented for RAN#10 approval for Rel.01 work. Progress will be reported earlier (RAN#9 and RAN#8 if progress is made)

- Hybrid ARQ II/III
  Results for RAN#8 for co-ordination with other RAN WGs
  CRs RAN#9

- Radio link performance enhancements (Study Item)
  (including TX diversity enhancements or power control improvements with DCH or DSCH)
  CRs for approval in RAN#9

- Feasibility Study for Improved Common DL Channel for Cell FACH State (Study Item)
  Results for TSG RAN#10

- Support of Location Services in UTRA (FDD&TDD)
  Work items were agreed separately for both FDD & TDD
  Results for TSG RAN#9

- NodeB Synchronisation for UTRA TDD
  - Results for TSG RAN#9

- Uplink Synchronous Transmission (Study Item)
  Results for TSG RAN#9

REFERENCES (WI descriptions)

TDoc RP-00-0032 Downlink packet data
TDoc RP-00-0053, Support of Location Services in UTRA TDD
TDoc RP-00-0054 Hybrid ARQ II/III
TDoc RP-00-0055 NodeB Synchronisation for UTRA TDD
TDoc RP-00-0190 Feasibility Study for Improved Common DL Channel for Cell-FACH State
TDoc RP-00-0057 Low Chip Rate TDD Option (Update in Tdoc RP-00-0191)
TDoc RP-00-0189 Terminal power saving features
TDoc RP-00-0180 Compressed mode enhancements (note WI name change to Improvement of interfrequency and intersystem measurement)
TDoc RP-00-0181 Radio link performance enhancements
TDoc RP-00-0139 USTS (approved as study item)