TSG-RAN Working Group 1 meeting #9   *TSGR1#9(99)L35*
Dresden, Germany
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To: RAN WG2
Source: NTT DoCoMo
Title: Draft LS on parity bit attachment to 0 bit transport block

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In WG1#9, the followings are approved to measure BLER during DTX for the outer-loop power control:

1) Parity bits can be attached to 0 bit-transport block to measure the quality for the outer loop TPC.
2) The parity bit pattern is the same as CRC parity bit pattern of transport block size =0, i.e. all parity bit equals to 0.
3) Necessity of the parity bits attachment is designated via TFS. The number of transport blocks =0 of TFS designates that the parity bits need not to be attached to 0 bit- transport block. Transport block size =0 bit of TFS designates that the parity bits shall be attached to 0 bit- transport block.

CR for TS25.212 according to the above is approved in WG1.

In the above 3), transport block size = 0 is used for control of parity bit attachment. However, WG1 noted that transport block size = 0 is not included in the range of transport block size in WG2 specifications.

In order to avoid inconsistency between WG1 and WG2, WG1 would like WG2 to add transport block size = 0 bit to the range of transport block.