3GPP TSG RAN WG1 Meeting #107-e R1-2110964

**e-Meeting, Nov. 11th – 19th, 2021**

**Source: Moderator (ZTE)**

Title: FL summary #1 on SRS enhancements

Agenda Item: 8.1.3

Document for: Discussion and Decision

# Introduction

In RAN#86, the Rel-17 WID of further enhancements on MIMO for NR is approved [1]. In the approved WID, a particular point is about SRS enhancements in terms of flexibility, coverage and capacity, targeting both FR1 and FR2. The detailed scope of the SRS enhancement is given as follows.

*3. Enhancement on SRS, targeting both FR1 and FR2:*

* 1. *Identify and specify enhancements on aperiodic SRS triggering to facilitate more flexible triggering and/or DCI overhead/usage reduction*
  2. *Specify SRS switching for up to 8 antennas (e.g., xTyR, x = {1, 2, 4} and y = {6, 8})*
  3. *Evaluate and, if needed, specify the following mechanism(s) to enhance SRS capacity and/or coverage: SRS time bundling, increased SRS repetition, partial sounding across frequency*

Previous RAN1 agreements on these SRS enhancements are given in Section 6.1.

In this contribution, we summarize companies’ views on the above SRS enhancements submitted to RAN1#107-e [2]-[22].

# Flexibility enhancements

## SRS triggering offset

### 2.1.1. Collision handling

One FFS point from RAN1#104e’s agreement on available slot definition is “rules to handle the case of multiple SRS resource sets with overlapping symbols and/or triggered by a same DCI”. Companies’ detailed views are given in the table below.

Table 2-1

|  |  |  |
| --- | --- | --- |
| **Collision handling** | | |
| Views | Companies | Priority rules |
| Introduce dropping rule when collision happens among aperiodic SRS resource sets | Intel, Xiaomi (UE optional), CMCC, Apple (UE optional), Nokia/NSB, Qualcomm, ZTE, Huawei/HiSilicon, Futurewei, Lenovo/MotM, Ericsson, vivo, Spreadtrum, CATT | * Rule 1 – Based on usage: Intel, CMCC, Nokia/NSB, Qualcomm, ZTE, Ericsson, vivo * Rule 2 – Based on set ID and CC ID: Intel, CMCC, ZTE, Huawei/HiSilicon, Ericsson, vivo, Spreadtrum, CATT * Rule 3 – Based on order of the triggering DCI: Lenovo/MotM, vivo * Rule 4 – Based on type of the aperiodic SRS and the UL channel/signaling: Futurewei |
| Do not introduce new dropping rule | Samsung, OPPO, LG |  |

The majority of companies are positive to have dropping rule defined to handle this collision. Among all the proposed rules, the first two rules (usage and CC ID/set ID) attract the majority view. Based on majority view, the following proposal is recommended by FL.

***FL Proposal 2-1:*** *Introduce dropping rule when collision happens among multiple aperiodic SRS resource sets in a same CC or different CCs.*

* *Adopt the following priority rules (with priority level from high to low)*
  + *Usage > CC ID > Set ID*
    - *For usages, priority order is AS > CB > NCB > BM*
    - *For CC ID/set ID, lower ID has higher priority than higher ID*
* *The new dropping rule is a UE optional feature*

Companies’ further views are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| InterDigital | We are on the fence for this issue. We see some value by considering some dropping rules, however at the same time we understand the position of companies who think this is not necessary and can be avoided by gNB. |
| Futurewei | RAN1 had the following agreement before:  **Agreement**  *Confirm the following working assumption with modifications*  *An “available slot” is a slot satisfying there are UL or flexible symbol(s) for the time-domain location(s) for all the SRS resources in the resource set and it satisfies UE capability on the minimum timing requirement between triggering PDCCH and all the SRS resources in the resource set.*   * *From the first symbol carrying the SRS request DCI and the last symbol of the triggered SRS resource set, UE does not expect to receive SFI indication, UL cancellation indication or dynamic scheduling of DL channel/signal(s) on flexible symbol(s) that may change the determination of “available slot”.* * *Note: Collision handling between the triggered SRS and any other UL channel/signal is performed after the determination of available slot.* * *FFS: Rules to handle the case of multiple SRS resource sets with overlapping symbols and/or triggered by a same DCI*   So the group already agreed that collision handling is needed for SRS using the available slot mechanism, unless a collision never happens for such a SRS. If a collision never happens, the only implication is that the gNB is not allowed to overwrite any previous decisions, even when the gNB needs to do so to respond to some new events. Therefore, we think the group should stick with the previous agreement and provide the gNB with the flexibility via collision handling.  We also understand there is limited time for this WI. Based on the previous agreement, we do not have to design collision handling for general SRS transmissions, but only for cases involving SRS using the available slot mechanism.  So our suggestion is:  *Introduce dropping rule when collision happens among an aperiodic SRS resource set configured with available slot offset and other transmission(s) in a same CC or different CCs.*   * *The new dropping rule is a UE optional feature*   Further details can be discussed. |
| LGE | We don’t think that dropping rule is necessary. It is purely up to gNB scheduling.  Regarding the collision between Rel-17 SRS and the other transmission(s), legacy rule should be fine. |
| NEC | We think priority rule based on CC/set ID is enough.  While if majority supports the proposal, we are fine. |
| vivo | Support the FL proposal |
| Samsung | We think gNB can handle the collision. Hence we don’t think this is not needed. |
| Huawei, HiSilicon | Not support. One unified dropping rule based on usage is not reasonable since the priority of different usage depend on the scenario, such as BM has higher priority in FR2 transmission, but DL CSI acquisition has higher priority in some cases for FR1. So, the dropping rule only based on CC ID/set ID is sufficient, and gNB can flexibly arrange the priority for usages by configuring CC ID/set ID. |
| Ericsson | Support first bullet, however, we need some clarification how this can be an optional feature? What is the behaviour when collisions occurs for a UE that do not support collisions? |
| QC | Don’t support.  We are only motivated for handling the collision by same DCI. If there is no consensus among companies and given this is the last RAN1 meeting, this issue should deprioritized as it can be handled by proper gNB scheduling. |
| Lenovo/MotM | We support to introduce a dropping rule at least for the collision among multiple SRS resource sets triggered by a same DCI.  However, we think CB and NCB should have the same priority since they can not be configured simultaneously.  ***FL Proposal 2-1:*** *Introduce dropping rule when collision happens among multiple aperiodic SRS resource sets in a same CC or different CCs.*   * *Adopt the following priority rules (with priority level from high to low)*   + *Usage > CC ID > Set ID*     - *For usages, priority order is AS > CB/NCB > BM*     - *For CC ID/set ID, lower ID has higher priority than higher ID* * *The new dropping rule is a UE optional feature* |
| DOCOMO | We support the FL proposal. |
| Intel | Support FL proposal |
| OPPO | Not support since there is no clear motivation and benefits. Moreover, the proponents of new dropping rules have quiet diverging options for the solution. Thus, this issue should be down-prioritized as we are approaching the deadline of R17 completion. |
| CATT | Since available slot is determined for each SRS resource set separately, two SRS resource sets may be collided even when they are configured with different *slotOffset* and *t* values. Therefore it is difficult for gNB to avoid collision by scheduling and a collision handling rule is needed in Rel-17.  For the dropping rule, the motivation of combining rule 1 and rule 2 is not clear. We prefer to determine the priority of SRS resource sets according to CC ID first, and then by set ID. That is, for two SRS resource sets in different CCs, the SRS resource set in the CC with lower ID is of higher priority, and for two SRS resource sets in the same CC, the SRS resource set with lower set ID is of higher priority. Then if gNB expects an SRS resource set for a typical usage in a CC to be of higher priority, gNB can set the SRS resource set with lower set ID. |
| Spreadtrum | We think dropping based on either usage of CC ID/Set ID is enough. No need to introduce a hybrid rule. |
| Xiaomi | We prefer this depriorized. |
| Nokia/NSB | Support FL proposal. |
| CMCC | As commented in the previous meeting, we do not think “*in a same CC*” should be the high priority issue compared with multiple CCs, and even for a single/same DCI triggered collied SRS transmissions.  We could be more open for the priority rule. The gNB could configure different SRS sets with different usages, and determine the priority according to the SRS set id which is linked with certain usage. Then different priority of usage could be realized through the configuration of SRS set with different id, and in addition provides more flexibilities.  We are also a bit confused that whether this ‘optional’ is needed here, since the whole Rel-17 feature is optional. Please clarify, thanks. |

### 2.1.2 Remaining issues of the Rel-17 mechanism in CA case

The remaining issues to complete the Rel-17 mechanism of triggering offset determination in CA case includes the bit width of the SOI field when multiple CCs/BWPs are configured and the definition of reference slot when *ca-SlotOffset* is configured.

Table 2-2

|  |  |  |
| --- | --- | --- |
| **Bit width of SOI when multiple CCs/BWPs are configured** | | |
| Alternatives | Companies | Further details |
| Alt 1: SOI bit width depends on the maximum number of “t” values configured for any of the aperiodic SRS resource sets across all BWPs within one CC | Qualcomm, CATT |  |
| Alt 2: SOI bit width depends on the maximum number of t values configured for the resource sets in the BWP where the DCI is received | ZTE, OPPO | * ZTE: Use padding or truncation approach when the maximum number of t values configured for resource sets in the indicated BWP does not equal to the DCI BWP * OPPO: if the number (X) of configured “t” values is less than the number (Y) that can be indicated by this new DCI field, when one of the largest (Y-X) codepoints is indicated by the new DCI field, the slot for the transmission of this triggered SRS resource is determined by Rel-15/16 mechanism |
| Alt 3: SOI bit width depends on the maximum number of t values configured for all the resource sets across all BWPs in all CCs. | Huawei/HiSilicon, Futurewei, vivo |  |
| **Reference slot when *ca-SlotOffset* is configured** | | |
| Views | Companies | |
| When *ca-SlotOffset* is configured, reference slot to use the Rel-17 mechanism to determine the SRS offset is slot, where , , and are determined by *ca-SlotOffset* configurations of the PDCCH carrier and SRS carrier. | ZTE, Huawei/HiSilicon, Futurewei | |

For the first issue, FL believes a simple solution is sufficient to handle this case. Hence the following is recommended.

***FL Proposal 2-2:*** *SOI bit width depends on the maximum number of t values configured for all the resource sets across all configured BWPs in all configured CCs in the bands where each has at least one t value configured.*

* *For the bands without any t value configured, follow Rel-15/16 mechanism to determine the SRS slot offset*

For the second issue, the specification needs a solution to support the Rel-17 mechanism when *ca-SlotOffset* is configured. Hence FL suggests the following proposal.

***FL Proposal 2-3:*** *When ca-SlotOffset is configured, reference slot to use the Rel-17 mechanism for determining the SRS offset is slot , otherwise reference slot is, where , , and are determined by ca-SlotOffset configurations of the PDCCH carrier and SRS carrier.*

Companies’ further views are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| InterDigital | Support Proposal 2-2. |
| Futurewei | Support both proposals. |
| LGE | For Proposal 2-2, we suggest to add “within a cell group” at the end of the sentence. And, we have one clarification question on the proposal. The maximum number of configured t values (within a CC/BWP) can be different across different CCs/BWPs?  ***FL Proposal 2-2:*** *SOI bit width depends on the maximum number of t values configured for all the resource sets across all configured BWPs in all configured CCs within a cell group.*  For Proposal 2-3, the parameter name should be revised as “*ca-SlotOffset*” |
| NEC | We are fine with the proposals. |
| vivo | Support the FL proposal |
| Samsung | Support the FL proposal. |
| Huawei, HiSilicon | Support both proposals. |
| Ericsson | Support both proposals |
| QC | On FL Proposal 2-2, we have one concern on how to handle the case where UE doesn’t support rel-17 AvailableSlot on a certain band (e.g. unlicensed band or FDD band) and network not configuring the parameter ‘availableSlot’ for the SRS sets for the CCs within that band. The UE should follow Rel-15 triggering based on SlotOffset, however proposal 2-2 says something different. The current proposal is confusing to us as it means the Available slot mechanism is expected at all other CCs/bands whether UE support this feature or not on that CC/band. **This needs to be clarified first.**  Support FL Proposal 2-3 as it is an extension for the concept of reference slot. Also, we need a RAN1 agreement of reference slot for cross-CC SRS triggering as the reference slot the reference slot is (n+k) per follow agreement.  **Agreement**  Support Opt. 2: Reference slot is the slot indicated by the legacy triggering offset.   * If DCI is transmitted in slot n, and k is the legacy triggering offset, reference slot is slot n+k. * Note: the legacy triggering offset can be 0, if slotOffset is absent.   Suggest the following edit:  ***FL Proposal 2-3:*** *When ca-SlotOffset is configured, reference slot to use the Rel-17 mechanism for determining the SRS offset is slot , otherwise reference slot is , where , , and are determined by ca-SlotOffset configurations of the PDCCH carrier and SRS carrier.* |
| *FL* | The two proposals are updated based on the comment from LG and QC.  @LG,  For the first comment, I’m not sure whether it is needed to constrain it within a CG. SRS can be triggered in CCs outside the CG. It seems your revision cannot solve this issue.  The proposal is updated based on your second comment (correcting the RRC parameter name). Thanks for spotting this.  @QC,  2-2: The proposal is updated per your comment. It is limited to the CCs/BWPs in the bands that support this Rel-17 feature, and it is clarified that for the bands that do not support this feature, Rel-15/16 mechanism will be used.  2-3: The proposal is updated based on your comment. |
| Lenovo/MotM | For proposal 2-2, we have the same concern with QC. If all the SRS resources in a CC are not configured with t values, Rel-15 triggering mechanism shall be used. However, the current proposal 2-2 seems to have different intensions.  We are fine with proposal 2-3. |
| DOCOMO | For Proposal 2-2, we agree Lenovo/MotM’s concern. For example, if a UE reports its support of Rel-17 availableSlot in Band#A and Band#B, and if Band#A has SRS resource set(s) with t value configuration while Band#B has NO SRS resource set(s), a triggering DCI, even if it is in Band#B, has to include SOI field based on maximum number of t values in both Band#A and Band#B. When the DCI triggers A-SRS in the same band (Band#B, which has no SRS resources with t value configuration), there should be no SOI field in our view. Thus we propose the following  ***FL Proposal 2-2:*** *SOI bit width depends on the maximum number of t values configured for all the resource sets across all configured BWPs in all configured CCs in the bands that is configured with at least one value of t in at least one SRS resource set in any of BWP in a CC in the band~~support the Rel-17 feature of SRS triggering offset enhancement~~.*   * *For the bands that is configured with at least one value of t in at least one SRS resource set in any of BWP in a CC~~do not support this Rel-17 feature~~, follow Rel-15/16 mechanism to determine the SRS slot offset*   For Proposal 2-3, again, we agree with QC’s edit, while the direction is generally ok. |
| Intel | For proposal 2-2, we don’t understand why the SOI bit width depends on the max number of t values for all the SRS resource sets across all the BWPs in all CCs.  One example is the number of SRS ports could be different for different BWP/CC, and correspondingly the ports for TPMI could be different for different BWP/CC. But the TPMI field size in DCI just consider the active BWP.  So, we think the SOI bit width should depends on the max number of t values for the SRS resource sets in the active BWP in current CC.  For proposal 2-3, we are fine. |
| OPPO | Support Proposal 2-3  Regarding Proposal 2-3, our first preference is per-BWP, but we can keep open to other alternatives. However, there seems some ambiguity on the current version. For “*in the bands that support the Rel-17 feature of SRS triggering offset enhancement*”, there may be two different interpretations   * Alt.1: the band on which UE reports to support the R17 feature * Alt.2: the band on which UE is configured with R17 feature on some CCs   In order to avoid the ambiguity and address the concerns of QC/Lenovo, we suggest the following modifications  ***FL Proposal 2-2:*** *For a BWP/CC configured with Rel-17 feature of SRS triggering, SOI bit width depends on the maximum number of t values configured for all the resource sets across all configured BWPs in all configured CCs ~~in the bands that support the Rel-17 feature of SRS triggering offset enhancement~~.*   * *~~For the bands that do not support this Rel-17 feature, follow Rel-15/16 mechanism to determine the SRS slot offset~~* * Note: Whether UE is configured with Rel-17 feature of SRS triggering or not in a BWP/ CC depends on UE capability and gNB configuration |
| CATT | **For Proposal 2-2**: We prefer to determine SOI bit width based on the maximum number of t values configured for all the resource sets across all configured BWPs in a CC. The reason is as follows: If the resource sets are resource sets across all CCs, unnecessary DCI overhead is wasted for CCs that not configured with t values or with less t values needed. If the resource sets are resource sets in a BWP, the bit width of SOI filed and SRS triggering scheme (Rel-17 vs. Rel-15) can be changed in respond to BWP switching. It would increases UE’s complexity.  **For Proposal 2-3**: Support. |
| *FL* | Proposal 2-2 is updated based on OPPO and NTT DOCOMO’s comment.  @Intel, @CATT,  I understand your preference. But as this solution (to make SOI bit width depend on all the CCs/BWPs) is the simplest solution which does not require any further work, and we are at the last meeting of this release, I’m afraid the current proposal is the only way to go. |
| Xiaomi | For proposal 2-2, we still confused and clarifications needed. We are open to an optimized solution anyway.  For proposal 2-3, fine with this. |
| Nokia/NSB | Support both FL proposals. |
| Huawei, HiSilicon2 | To make it clear, we need to clarify the bands is for SRS transmission. Then, SOI bit-width is 0 for the bands without any t value configured. So, propose the following revision:  ***FL Proposal 2-2:*** *SOI bit width depends on the maximum number of t values configured for all the resource sets across all configured BWPs in all configured CCs in the bands for SRS transmission where each has at least one t value configured.*   * *For the bands without any t value configured, follow Rel-15/16 mechanism to determine the SRS slot offset, where SOI bit-width is 0* |
| CMCC | For proposal 2-2, our preference is to determine the SOI considering all BWP for one CC for the efficient use of the bit field in DCI. |

## Flexible DCI format

**Re-purpose**

Based on the agreement of using DCI 0\_1/0\_2 to trigger SRS without data and without CSI request, companies propose the following schemes to repurpose unused fields in these DCI fields to indicate SRS parameters dynamically.

Table 2-3

|  |  |  |
| --- | --- | --- |
| **Repurpose unused fields in DCI format 0\_1/0\_2 without data and without CSI** | | |
| Categories | Detailed alternatives | Companies |
| CAT A (Time-domain parameters)   * Supported by 4 companies * 1 company has concern | A-1: Indication of available slot position, i.e., the t values | Xiaomi, NTT DOCOMO |
| A-2: Indication of slot offset | vivo |
| A-3: Indication of SRS symbol-level offset | Futurewei |
| A-4: Indication of time-domain behavior for SRS transmission over multiple OFDM symbols, e.g., repetition, hopping, and/or splitting | Futurewei |
| Do not support this category | Intel |
| CAT B (Frequency-domain parameters)   * Supported by 3 companies | B-1: Indication of a group of CCs for SRS transmission | Xiaomi, Futurewei |
| B-2: Indication of frequency domain resource in a BWP for SRS transmission |  |
| B-3: Indication of whether DL/UL BWP is applied for SRS transmission | Intel |
| Do not support this category |  |
| CAT C (Power control parameters)   * Supported by 2 companies * 2 companies have concern | C-1: Re-purpose ‘TPC command for PUSCH’ as ‘TPC command for SRS’ | NTT DOCOMO, Futurewei |
| C-2: Indication of open loop power control parameter e.g., p0. |  |
| Do not support this category | CMCC, vivo |
| CAT D (Spatial-domain parameters, i.e., indication of SRS port and beamforming) | Re-purpose CSI-RS/TPMI indication to indicate SRS spatial-domain parameters |  |
| Do not support this category | CMCC |
| CAT E (Extend the number of DCI codepoints for aperiodic SRS trigger states)   * Supported by 5 companies | Extend the number of DCI codepoints for aperiodic SRS trigger states | Intel, Xiaomi, NTT DCM, Nokia/NSB, Futurewei |
| No or deprioritize | - | Samsung, Apple, Qualcomm, ZTE, OPPO |

It seems it is hard converge on this issue. Since we have discussed this issue for long time costing a lot of meeting resources, and companies’ interest on this has cooled down, the following conclusion is recommended by FL.

***FL Proposal 2-4:*** *No consensus to support repurpose of DCI field(s) for SRS parameter indication in Rel-17.*

Companies’ further views are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| InterDigital | Support FL proposal |
| Futurewei | We still think this can be beneficial. For the remaining of the WI, we hope to discuss repurposing/reusing as much as time allows, such as the TPC field discussed below. |
| NEC | Support the proposal. |
| Huawei, HiSilicon | Fine for the proposal. |
| QC | Support. |
| Lenovo/MotM | Support FL proposal. |
| DOCOMO | We share Futurewei’s view above. At least just to reuse the existing PUSCH related fields for the triggered SES can be supported with quite limited efforts. |
| Intel | Similar view as Futurewei and DoCoMo. |
| CATT | Support FL’s proposal. |
| Xiaomi | Support FL proposal |
| Nokia/NSB | Support the FL proposal. |
| CMCC | Support FL’s proposal. |

**Group-common DCI**

Another remaining issue is whether to enhance group-common DCI in addition. Companies’ views are summarized as follows.

Table 2-4

|  |  |
| --- | --- |
| **Whether group-common DCI enhancement is supported additionally** | |
| Alternatives | Companies |
| Yes | Samsung, Qualcomm, vivo, Futurewei, Intel |
| No or deprioritize | LG, Huawei/HiSilicon, CATT |

***FL Proposal 2-5:*** *TBD*

Companies’ further views are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| Futurewei | Added our support in above table. We also support GC DCI enhancement. At least it is straightforward to introduce available slot offset for GC DCI. |
| LGE | We are not sure it is really needed at this late stage. |
| Huawei, HiSilicon | Not necessary. Group-common DCI is generally used for group common related service. For aperiodic SRS triggering, it is difficult to find the scenarios where group common triggering is needed. |
| QC | Similar views as Futurewei, as least introduce Rel-17 available slot for GC DCI. |
| Intel | Similar view as QC and Futurewei. Support to apply available slot operation for DCI 2\_3. Otherwise, we need to support mixed Rel-17 operation and Rel-15 operation. |
| CATT | Not to enhance group-common DCI at this stage. |

**TPC command and BWP indication**

Companies would like to clarify or enhance the interpretation of TPC command and BWP indicator in DCI 0\_1/0\_2 triggering SRS without data and without CSI.

Table 2-5

|  |  |
| --- | --- |
| **Interpretation of TPC command and BWP indicator in DCI 0\_1/0\_2 triggering SRS without data and without CSI** | |
| Views | Companies |
| * For SRS triggered by DCI format 0\_1/0\_2 without scheduling PUSCH and without CSI Request, the existing TPC command carried by the DCI is used for the triggered SRS transmission. * When SRS is triggered by DCI format 0\_1/0\_2 without scheduling PUSCH and without CSI Request, the existing BWP indicator field carried by the DCI could be used to switch the BWP for the triggered aperiodic SRS transmission. | Intel, Futurewei |

***FL proposal 2-6:*** *TBD*

Companies’ further views are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| Futurewei | We think CIF and BWP indicator fields should apply to the SRS, which is covered in existing spec:  TS 38.213  If a UE is configured with *CrossCarrierSchedulingConfig* for a serving cell the carrier indicator field value corresponds to the value indicated by *CrossCarrierSchedulingConfig.*  *…*  If a bandwidth part indicator field is configured in a DCI format, the bandwidth part indicator field value indicates the active DL BWP, from the configured DL BWP set, for DL receptions as described in [5, TS 38.212]. If a bandwidth part indicator field is configured in a DCI format, the bandwidth part indicator field value indicates the active UL BWP, from the configured UL BWP set, for UL transmissions as described in [5, TS 38.212]. If a bandwidth part indicator field is configured in a DCI format and indicates an UL BWP or a DL BWP different from the active UL BWP or DL BWP, respectively, the UE shall  … - set the active UL BWP or DL BWP to the UL BWP or DL BWP indicated by the bandwidth part indicator in the DCI format  For the TPC command field, it is not covered in existing spec but this enhancement is simple and beneficial. We support to use the TPC command field for SRS.  We also support to reuse the FDRA field for SRS. |
| Samsung | Do not support. We failed to see what the difference between this proposal and Re-purpose above. |
| Huawei, HiSilicon | Agree with Samsung. The discussion is the same as Proposal 2-4. |
| QC | Don’t support. |
| DOCOMO | We support to discuss this issue. |
| Intel | Support to discuss these issues since it is related with the completion of the agreed feature to trigger aperiodic SRS via non-scheduling DCI.  For BWP indicator field, we think it should be used to change the BWP for the triggered SRS. Otherwise, the UE behavior is not clear regarding this field.  For TPC command, it should be used for the triggered SRS to adjust the SRS Tx power timely and properly.  Questions to the group:   1. For SRS triggered by DCI 0\_1/0\_2 without scheduling data, what should be the UE behavior if BWP indicator field indicates a different BWP? 2. For SRS triggered by DCI 0\_1/0\_2 without scheduling data, how to determine the Tx power for the triggered SRS? |
| OPPO | Share the same view with Samsung |
| CATT | Don’t support.  The power control adjustment state for a SRS resource may follow PUSCH’s power control adjustment state or be updated according to the TPC field in DCI format 2\_3. If the TPC filed in DCI format 0\_1/0\_2 without PUSCH scheduling and CSI request is repurposed, some issues have to be considered, e.g. the scenarios for TPC field repurposing, how to repurpose the TPC fields when two TPC fields are configured for M-TRP PUSCH transmission, etc. Since this is the last meeting for Rel-17, we prefer not to repurpose the TPC field in DCI format 0\_1/0\_2 for SRS at this stage.  The BWP indicator field in DCI is used to indicate active BWP change. According to current specs, the overhead of DCI is determined by the active BWP. BWP change may cause bit width change for many DCI fields that related to parameters configured per BWP (e.g. SRI filed, TPMI filed, etc.). In order not to impact the performance of PUSCH, DCI for aperiodic SRS triggering only should not change active BWP. |
| Nokia/NSB | Do not support, agree with Samsung’s comments. |
| CMCC | As pointed by Samsung, this is the same issue as repurposing. Do not support. |

## Usage/overhead reduction

One remaining issue is whether to support specification enhancement on using SRS resources configured in SRS resource set with usage = “antennaSwitching” for codebook based UL transmission. Table 2-5 summarizes companies’ views.

Table 2-6

|  |  |
| --- | --- |
| **Whether to support specification enhancement on using SRS resources configured in SRS resource set with usage = “antennaSwitching” for codebook based UL transmission** | |
|  | Companies |
| Action 1: Add a UE capability to ensure same virtualization if SRS resource(s) for antenna switching also belong to a set for codebook | Apple, NTT DOCOMO |
| Action 2: Add a RRC parameter to turn on/off the UE behavior in Action 1 | Apple, NTT DOCOMO |
| Action 3: Clarify same virtualization is used if SRS resource(s) for antenna switching also belong to a set for codebook |  |
| None of the above actions is needed | Futurewei, Huawei/HiSilicon |

***FL proposal 2-7:*** *TBD*

Companies’ further views are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| Futurewei | Not needed and Rel-15 works fine. |
| Huawei, HiSilicon | Not necessary. SRS resource sharing is already supported in Rel-15. |
| Ericsson | Support Action 1+2 to introduce SRS resource sharing in NR. |
| DOCOMO | Either of the three actions should be taken in this release in our view, since the proper virtualization may not be achieved in the current specification, while using SRS resource configured in a set with usage=”antennaSwtching” for codebook based UL transmission itself can be performed. |
| CATT | We are supportive to support SRS usage sharing in Rel-17. One possible solution is to specify that for an SRS resource set configured in both SRS resource set for “codebook” and SRS resource set for “antennaSwitching”, when the SRS resource is transmitted in the SRS resource set for “antennaSwitching”, UE shall assume that it is used for both “codebook” and “antennaSwitching”. |

## Flexible antenna switching

Multiple companies discuss the issue of indicating the number of antennas to support more flexible antenna switching in dynamic signaling. Their views are summarized in the following table.

Table 2-7

|  |  |  |
| --- | --- | --- |
| **Update Tx/Rx antennas for SRS antenna switch in dynamic signaling** | | |
| Views | Companies | Further details |
| Clarify that it changes the number of SRS ports dynamically but does not change the real number of Tx/Rx antennas | Futurewei, OPPO |  |
| Support indicating the number of Tx/Rx antennas for SRS antenna switching via MAC CE or DCI | Intel, Xiaomi, Samsung, Nokia/NSB, Qualcomm, Futurewei, Lenovo/MotM, Ericsson, vivo, Spreadtrum, CATT, OPPO | MAC CE:   * Xiaomi, Samsung, Nokia/NSB, Qualcomm, Ericsson, vivo (with new activation timing), Spreadtrum, OPPO   DCI:   * CATT, Intel |
| Support UE reporting of the preferred antenna switching configuration | Yes: Xiaomi (MAC CE), Apple  No: Intel, Futurewei |  |

The following proposal is given based on majority view.

***FL proposal 2-8:*** *Support gNB indicating the used SRS resources from the configured SRS resources in SRS resource set(s) for antenna switching via MAC CE.*

* *Support UE reporting of one preferred antenna switching configuration in MAC CE*
* *The gNB indicated or UE reported antenna switching configuration belongs to the supported antenna switching reported by UE capability signaling*
* *A new application timing of the MAC CE activation is introduced for this purpose*
* *Note: Any change on the configured number of Tx antennas in each SRS resource is precluded in either the gNB indication or UE reporting*

Companies’ further views are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| InterDigital | Support the FL proposal without the note. It almost contradicts the first bullet. The main point of such flexibility is to also allow UE to indicate preferred TX dimension, obviously gNB would not be obliged to respect it. |
| Futurewei | We think “without changing the numbers of Tx/Rx antennas” needs to be added to the proposal.  We are not convinced that the UE should report a preferred antenna switching configuration. Some companies argued that this is similar to UE CQI reporting, in which UE reports its preference but the gNB does not have to follow the report for its MCS determination. However, CQI/MCS have a large number of potential values and hence higher uncertainty, so the UE reporting of CQI would lead to significant uncertain reduction and performance gains. For antenna switching configurations, however, there is only a very small set of antenna switching configurations for each UE. Therefore, we doubt the reporting will lead to any meaningful uncertain reduction and performance gains. |
| vivo | Support the FL proposal |
| Samsung | Support FL proposal. Regarding 3rd bullet with the application timing, we think the same MAC-CE activation time is enough. |
| Huawei, HiSilicon | Generally is fine for us. But we cannot accept the change of Tx number which impacts chain switching that needs RAN4 discussion. So, we are not fine to remove the note. And also prefer to restrict on only periodic and semi-persistent SRS cases, while AP-SRS is not the use case for resource and power saving. |
| Ericsson | Support although “*Support UE reporting of one preferred antenna switching configuration in MAC CE”* is obsolete*,* This information will not be used by gNB. |
| QC | Support in principles. We think there is no need to introduce new application timing of the MAC CE. |
| Lenovo/MotM | Support in principle. But fail to see the motivation of introducing new application timing of the MAC CE. |
| DOCOMO | We are fine with the proposal. |
| Intel | Do not support the FL proposal.  1. For aperiodic SRS, the DCI based solution should be supported, which is more important. Introducing MAC-CE to indicate some resources just introduce additional signaling overhead and is not be sufficient to satisfy the timing of aperiodic SRS.  The switching between xTyR could be achieved by associating different trigger state with the aperiodic SRS resource sets for corresponding xTyR. For example, trigger state #1 could be associated with SRS set #A for 2T4R, and trigger state #2 could be associated with SRS set #B for 1T2R.  The spec impact is much less and there is no need to introduce new DCI field.  2. For periodic SRS, we don’t see the need to have MAC-CE based solution at all. The RRC reconfiguration is sufficient.  3. Regarding semi-persistent SRS, it has been agreed that two semi-persistent SRS resource sets could be supported. Therefore, the existing MAC-CE to activate/deactivate semi-persistent SRS can be used to enable flexible switching between xTyR.  For example, the UE could be configured with one semi-persistent resource set for 2T4R and another semi-persistent SRS resource set for 1T2R. In this way, the gNB can use the existing MAC-CE to activate corresponding semi-persistent SRS resource set for 1T2R or 2T4R operation.  Comparing with introducing new MAC-CE, re-using the existing MAC-CE is the simplest way and has minimum spec impact. In addition, re-using the existing MAC-CE can also support changing the number of ports of SRS.  4. Regarding the UE reporting of preferred antenna switching configuration via MAC-CE, what’s the condition to trigger the reporting and how often to report it?  Therefore, we have the following proposal:  ***Proposal:***   * *For antenna switching with aperiodic SRS, DCI is used to switch between different xTyR*   + *The aperiodic SRS resource sets for different xTyR are associated with different trigger state*   + *No new DCI field is needed* |
| OPPO | Not sure what does “*A new application timing of the MAC CE activation*” mean. The application timing of MAC CE signaling are the same for various cases. Does this proposal intend to introduce a new value of the application timing of MAC CE signaling?  As there are different understanding on the impact on the Rx antennas for DL reception in previous meetings, we suggest to add a note as below:   * Note: This feature is not related to the Rx antennas for DL reception. |
| CATT | More clarification is needed for the proposal:  1. Whether gNB indicates SRS resources by selecting one xTyR schemes from multiple schemes, or gNB indicates SRS resources from multiple SRS resources configured for the same xTyR scheme?  2. Whether UE reports one preferred xTyR scheme or the number of Rx ports only?  3. What’s the trigger condition for the MAC-CE reporting for preferred antenna switching?  4. What’s the motivation of new application timing of the MAC CE activation? What’s the candidate value?  5. Is the MAC CE used to update SRS configurations for AP-SRS, SP-SRS or P-SRS?  Compared to updating SRS configuration for AS via MAC CE, flexible SRS triggering via DCI is preferred, since it has less spec efforts. |
| Spreadtrum | Support |
| Xiaomi | Support the FL proposal in principle. In our view, current DCI codepoints need to be extended to make this feature useful, espectially when we are discussing 6/8Rx in R17, MAC-CE should be enough to achieve a faster than RRC mechanism. UE reporting of one preferred xTyR configuration but xT is fixed currently. It is no harm to reserve some flexibility for future use. We are fine with Oppo’s suggestion that a Note can be added for clarification. |
| Nokia/NSB | Support the FL proposal. |
| CMCC | Do not see the motivation to change the application time of MAC CE. |

## Implicit determination of SRS parameters from data channel

Some companies propose to associate aperiodic SRS parameters (e.g., bandwidth) with scheduled data channel (e.g., PUSCH/PDSCH). The following summarizes companies’ views on this issue.

Table 2-8

|  |  |
| --- | --- |
| **Implicit determination of SRS parameters from data channel** | |
|  | Companies |
| The FDRA field in a DCI can apply to the triggered aperiodic SRS resource set*.* | LGE, Futurewei |

***FL proposal 2-9:*** *TBD*

Companies’ further views are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| Futurewei | Support. We have shown high performance gains and answered other companies’ questions before. This seems to be a simple mechanism with good performance benefit and should be considered.  We can make this more focused, e.g.:  *The FDRA field in a DCI can apply to the triggered aperiodic SRS resource set.* |
| LGE | We share the view as Futurewei. We also fine with Futurewei’s suggestion. |
| Huawei, HiSilicon | Seems the same discussion in proposal 2-4. |
| Ericsson | Support |
| QC | Deprioritize. As commented by other companies, it is the same as proposal 2-4 to *repurpose of DCI bitfields.* |
| Intel | Low priority |
| OPPO | Same view as Huawei/QC |
| Nokia/NSB | Low priority |

## Update of the association between trigger states and resource sets

Several companies discuss the issue of using MAC CE to update the association between SRS trigger states and SRS resource sets. Companies’ views are summarized in the following table

Table 2-9

|  |  |
| --- | --- |
| **Update of the association between trigger states and resource sets** | |
|  | Companies |
| Support to update the association between SRS trigger states and SRS resource sets via MAC CE | NTT DCM, Lenovo/MotM, Ericsson |

***FL proposal 2-10:*** *TBD*

Companies’ further views are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| Huawei, HiSilicon | Not necessary to introduce additional MAC-CE. |
| Ericsson | Support |
| Lenovo/MotM | Support.  This feature has been used for aperiodic CSI-RS triggering and more aperiodic SRS resource sets may be configured for a UE. This feature can improve the SRS triggering flexibility. |
| DOCOMO | We support using MAC CE to update the association between SRS trigger state and SRS resource sets. Generally such update via MAC CE is beneficial from flexibility point of view. |
| OPPO | The benefit is not clear. |
| Nokia/NSB | Support |
| CMCC | The motivation should be clarified and justified first. |

## Others

The following issues are discussed by one company.

|  |  |
| --- | --- |
| Extend the mechanism of indicating t for available slot to SRS triggered by group common DCI 2\_3 | Intel |
| Support single scheduling DCI to trigger simultaneous A-SRS transmission across multiple component carriers | Qualcomm |
| Support to trigger aperiodic SRS by non-scheduled DCI format 1-1 and 1-2. | vivo |
| Support DCI format 0\_1 and 0\_2 to trigger aperiodic SRS without data but with a non-zero "CSI request" where the associated "reportQuantity" in CSI-ReportConfig set to "none" for all CSI report(s) triggered by "CSI request" in this DCI format 0\_1 or 0\_2. | CATT |

Companies’ further views on the above issues are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| Futurewei | Support Intel’s proposal.  Ok with vivo’s proposal, but we’d like to see more discussions.  Support CATT’s proposal. |
| LGE | We support vivo’s proposal. |
| Ericsson | Support vivo |
| Intel | We think the available slot operation should be applied to DCI 2\_3. |
| CATT | AP-SRS triggering with DCI format 0\_1/0\_2 without PUSCH scheduling and without CSI request is supported. It is natural to support AP-SRS triggering with DCI format 0\_1/0\_2 without data but with a non-zero "CSI request" where the associated "reportQuantity" in *CSI-ReportConfig* set to "none" for all CSI report(s) triggered by "CSI request" in this DCI format 0\_1 or 0\_2. |

# Antenna switching up to 8Rx

## Guard period

**Presence of GP**

Companies discuss possible enhancements on the presence of guard symbols for antenna switching SRS. The proposed alternatives and companies’ positions are summarized as follows.

Table 3-1

|  |  |
| --- | --- |
| **Presence of guard symbols** | |
| Alternatives | Companies |
| Alt 1-0: Guard symbols are always-on, which is same as Rel-15 | Intel, Xiaomi, Qualcomm, Huawei/HiSilicon, OPPO, MediaTek |
| Alt 1-1: Guard symbols are configurable subject to UE capability | Nokia/NSB, ZTE, CMCC, Samsung, NTT DCM, vivo, CATT, LG, Ericsson, InterDigital |

***FL Proposal 3-1:*** *TBD*

Companies’ further views are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| InterDigital | Support Alt 1-1, it makes more sense. If a UE has such capability, why it should be prevented from using it. It enhances overall system spectrum efficiency, |
| LGE | We share the view with InterDigital. If there is an enhanced UE which is capable of fast antenna switching, the configurability of guard symbol based on the UE capability has clear benefit to save unnecessary guard symbol. |
| vivo | Support Alt 1-1 |
| Samsung | Support Alt 1-1. |
| Ericsson | Support Alt.1-1 |
| QC | Strongly support Alt 1-0. Guard period should always be present similar to Rel-15. |
| Lenovo/MotM | Support Alt.1-1 |
| MediaTek | Support Alt.1-0 |
| DOCOMO | We support Alt 1-1. Allowing high-capability UE to have no GP can improve resource efficiency a lot. |
| Intel | Support Alt 1-0 since it’s less spec change. |
| OPPO | Support Alt 1-0 which is aligned with RAN4 LS. Without new input from RAN4, RAN1 should stick to the existing design. |
| CATT | Support Alt 1-1. For a UE supports quick antenna switching that takes less time than CP, allowing the UE to transmit SRS resources without guard period would be helpful to reduce the overhead and transmission latency of the SRS. |
| Xiaomi | Support Alt.1-0. Lack of evidence for the support of Alt.1-1 |
| Nokia/NSB | Support Alt 1-1 |
| CMCC | Support Alt 1-1. Reducing the guard symbols could improve the network efficiency and resource utilizations. |

**Remaining issue of inter-set GP**

One FFS point for inter-set GP is how/whether to handle the case where the interval between SRS resource sets is larger than Y.

Table 3-2

|  |  |
| --- | --- |
| **How/whether to handle the case where the interval between SRS resource sets is larger than Y** | |
| Alternatives | Companies |
| Alt 1: UL/DL signals are allowed to be transmitted in the interval between SRS resource sets for antenna switching when the interval is larger than Y symbols, i.e., no scheduling restriction | Huawei/HiSilicon, NTT DOCOMO |
| Alt 2: If the interval between two SRS resource sets for antenna switching is larger than Y symbols, the position of guard period for scheduling restriction is the last Y symbols of the interval. | CATT |
| Alt 3: If the interval between two SRS resource sets for antenna switching is larger than Y symbols, the position of guard period for scheduling restriction is the first Y symbols of the interval | OPPO |
| No need to handle this case | CMCC, NTT DOCOMO, Qualcomm |

***FL Proposal 3-2:*** *TBD*

Companies’ further views are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| NEC | Support to discuss. And we also think uplink signals are allowed to be transmitted at least in remaining symbols excluding Y symbols. |
| Huawei, HiSilicon | Support Alt.1.  Question to DCM for no need to handle this case: if two AP-SRS resource sets are configured in two consecutive slots, there are 10 symbols between the two SRS sets, then whether the PUSCH is allowed for the 10 symbols?  We prefer no any restriction for PUSCH transmission if the gap between two SRS resource sets are large than Y as Alt.1 mentioned. But we can also live with Alt.2 or 3. |
| DOCOMO | Support Alt 1 (the summary above is revised as such). Our intention of “no handling” was no scheduling restriction. |
| Intel | No need to handle this case. |
| OPPO | We support to discuss this issue and is open to the final solution. |
| CATT | For UEs not support SRS starting at any symbol in a slot, the interval between two SRS resource sets for antenna switching would be much larger than Y. If the whole interval is considered to be guard period, no signal can be transmitted in the whole interval. Then all the resources in the interval are wasted. In order not to waste resources in the interval, UL/DL signal transmission in the interval between SRS resource sets for antenna switching should be allowed when the interval is larger than Y symbols. We prefer to predefine the position of guard period. Then the symbols in the interval other than the guard period can be used to transmit other DL/UL signals. |
| Xiaomi | Fine with alt.1. |
| Nokia/NSB | Open to discuss further |
| Huawei, HiSilicon2 | For this issue, there is an ambiguity whether data can be scheduled when the interval between SRS resource sets is large than Y. Actually, from companies’ replies, it seems no handling this issue means “no scheduling restriction”. If we are on the same page, we need a conclusion on this issue to avoid ambiguity in specs:  **Conclusion**: If the interval between SRS resource sets is larger than Y, there is no scheduling restriction. |
| CMCC | According to the discussion of Alt1, it is more like an issue of the guard symbols between one SRS resource set and other channels, not about the guard symbols for the SRS sets.  Our original thinking and according to the agreements, if the symbols in-between the two SRS transmission is larger than the guard symbol numbers, which fulfil the required there is no need to further handling anything for the SRS transmission.  But if the topic is more related to the SRS and other channels’ transmission, it is the same situation for two uplink slots only support last 6 symbols to transmit the SRS resources. Current spec does not have any limitation for the 8 symbols OFDMs in between.  if the interval between two SRS transmissions is Y symbols, we do not think any DL or UL transmission could happen as the RF chain is switching. And if the in-between symbols are larger than Y, we are glad to hear more views. |

## 4T6R configurations

It has been agreed to support 4T6R antenna switching in Rel-17. Companies’ views on the detailed 4T6R configuration are summarized as follows.

Table 3-3

|  |  |  |
| --- | --- | --- |
| **4T6R SRS antenna switching configurations** | | |
| Alternatives | Companies | Further details |
| Alt 1: 4 + 2 | Intel, Xiaomi, CMCC (2nd), NEC, Samsung, NTT DCM, Qualcomm, ZTE, CATT, OPPO, LG | Supported number of aperiodic resource sets:   * 1 or 2: Intel, ZTE, CATT   Enhance the transmit power determination of 4T6R SRS to ensure a constant ratio of the transmit power for the 2-port SRS resource and the transmit power for the 4-port SRS resource   * CATT |
| Alt 2-1: 2 + 2 + 2   * No guard symbols exist between the 1st and the 2nd transmission. Y guard symbol(s) exist between 2nd and 3rd transmission, where Y is same as the value defined in the current specification for different SCSs | vivo |  |
| Alt 2-2: 2+2+2   * For SCS=15, 30 and 60KHz: No guard symbols exist * For SCS=120 KHz: No guard symbols exist between the 1st and the 2nd transmission, and 1 guard symbol exists between the 2nd and 3rd transmission | CMCC (1st), Nokia/NSB, InterDigital, Huawei/HiSilicon, Ericsson, Spreadtrum |  |

Given the majority view is to Alt 1, and this is a necessary component to complete 4T6R, the following is suggested by FL.

***FL Proposal 3-3:*** *For 4T6R configuration, support two SRS resources with 4 ports in one resource and 2 ports in another resource.*

* *The two resources are distributed in 1 or 2 sets for aperiodic SRS*

Companies’ further views are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| InterDigital | We don’t support FL proposal. As it has been discussed in the previous meeting, there are performance issues resulted from Alt 1. |
| LGE | Support the FL proposal, which is the simplest way. |
| NEC | Support the proposal. |
| vivo | Support Alt 2-1 |
| Samsung | Support the FL proposal. |
| Huawei, HiSilicon | Do not support.  We have following concerns on 4+2:  One is channel estimation quality imbalance. Since the channel estimation SINR of 4-port SRS resource is always 3dB less than that of 2-port SRS resource, the channel estimation quality is imbalanced and the whole channel estimation quality is limited by the 4-port SRS resource. Please note that the 3dB loss is due to the transmission power restriction, which can not be compensated by receiver side (i.e., gNB).  Another is power imbalance. For power class-3, we only define 3dB for power imbalance tolerance. If there already exist 3dB power difference between 4-port resource and 2-port resource because of the antenna switching structure, then how can guarantee there is no insertion loss between antennas?  Please note that due to the introduction of 4+2 antenna switching structure, the channel estimation quality imbalance and power imbalance always exist under any antenna architectures. Here it is meaningless to discuss some corner cases under special antenna architectures in which the UE is lack of full power transmission capability. |
| Ericsson | Do not support, Alt. 2-2 is simpler. |
| QC | Support. |
| Lenovo/MotM | Support. |
| MediaTek | Support FL proposal |
| DOCOMO | We support the proposal. Whether Alt 2 could be beneficial or not seems much dependent on RAN4 discussion. If Alt 2 result in Alt 2-1, more symbols are needed for 4T6R antenna switching, which we want to avoid. |
| Intel | Support FL proposal.  As commented in previous meeting, the power imbalance also exists for Alt-2 if the UE PA architecture is [23 23 23 ~~20~~17] dBm. The maximum output power for the three 2-port SRS resources would be 23, 20, 23 dBm respectively (assuming 1st SRS connects to 1st and 2nd PA, 2nd SRS connects to 3rd and 4th PA, 3rd SRS connects to 1st and 2nd PA).  As for the actual Tx power after power control, the Tx power could be different for different SRS resource for antenna switching with xTyR according to current spec, especially when multiple SRS resource sets are configured and distributed over different slots. The SRS Tx power will change since the pathloss may be different.  In addition, for Alt-2 configuration, how to differentiate between 4T6R and 2T6R? |
| OPPO | Support |
| CATT | Support FL’s proposal.  We think the issue of power imbalance may also present for 2+2+2. For example, for a PC 3 UE with PAs of 17dBm+17dBm+20dBm+20dBm, although the UE is expected to transmit all SRS resources with same power, it is possible that 1 SRS resources are transmitted at 23dBm (with PAs of 20dBm+20dBm), and the other two SRS resources are transmitted at 20dBm, since the PAs of these SRS resources (17dBm+17dBm) cannot achieve 23dBm.  The problem of power imbalance for 4+2 can be solved by many solutions, some candidate solutions are as follows:   * + Alt 1: The transmit power of each SRS port in a SRS resource for 4T6R is determined as if there are 4 ports in the SRS resource, i.e. for any SRS resource for 4T6R, each SRS port uses a quarter of the calculated transmit power(i.e. in TS 38.213).   + Alt 2: The transmit power of each SRS resource is calculated according to the power control mechanism in Rel-15/Rel-16 first. If the calculated transmit power for the 2-port SRS resource is scaled, the calculated transmit power for the 4-port SRS resource is scaled accordingly to make sure the ratio of the transmit power for the 2-port SRS resource and the transmit power for the 4-port SRS resource is constant.   + Alt 3: gNB indicates the transmit power determination scheme for 4T6R to the UE,     - The candidate determination schemes can include one of Alt 1 and Alt 2, and the legacy power control strategy in Rel-15/Rel-16;     - Whether the legacy power control strategy is supported by a UE is subject to UE capability. |
| Xiaomi | Support FL proposal |
| Nokia/NSB | Do not support, agree with Ericsson that Alt 2-2 is simpler. |
| Intel | Correct some typo in previous comment. The mentioned PA architecture should be [23 23 23 17] dBm. |
| CMCC | Our thinking is to include both Alt 1 and Alt 2-2 for the support of 4T6R. As the Alt 2-2 has more requirements for PA and UE implementations, then Alt 2-2 could be an UE capability dependent feature. Compared with Alt 1, Alt 2-2 are more efficient with less overheads and higher powers. Then we proposal to include both alternatives in the proposal.  For the sub-bullet, we are quite confused why should the two SRS resources distributed into two sets. If it is the similar case as the agreements for <=4Rx, then two SRS resources set should be an optional feature with same spirit. |

## Insertion loss compensation

Some companies discussed possible enhancements to compensate the insertion loss cause by antenna switching, especially when the number of switches is large. Companies’ views are summarized as follows.

Table 3-4

|  |  |
| --- | --- |
| **Insertion loss compensation** | |
| Views | Companies |
| Support UE capability reporting of power offset across antenna ports in different SRS resources for insertion loss compensation in DL CSI acquisition | Qualcomm, InterDigital |
| Ericsson proposes to enhance this from a different angle: Support to report ∆TRxSRS = 0 dB as a UE capability (in RAN4) | Ericsson |

***FL Proposal 3-4:*** *TBD*

Companies’ further views are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| InterDigital | We share the same view as Qualcomm. This problem is not about just passing UE tests, it is performance-related and it cannot be addressed with report ∆TRxSRS = 0 dB as a UE capability (in RAN4). |
| Huawei, HiSilicon | Not necessary. We do not see the cases for further enhancement. |
| Ericsson | Support. This is an important issue since reciprocity based MU-MIMO performance can be bad for some UEs in the field, due to this SRS antenna power imbalance. TDD based reciprocity fails. |
| QC | Support. gNB awareness of the power offset between UL/DL ports due to insertion loss or PA power mismatch is very essential to reciprocity-based beamforming. |
| DOCOMO | Questions to understand the issue better:   * Why is such report, which is NOT supported in Rel-15/16, necessary here in Rel-17? Although we read QC tdoc, we are still struggling to understand why introductions of new Tx-Rx combination results in this proposal.   What is the impact in RAN1 specification by having this report? |
| OPPO | Not support as the insertion loss is RAN4 issue |
| Nokia/NSB | We are open to discuss this further. |
| CMCC | We are not sure we have enough time for this issue. And this is more related to UE implementations. |

## Others

The following issues are discussed by one or two companies.

|  |  |
| --- | --- |
| A 6Rx can report a capability of two, four or six layers of maximum number of DL MMO layers. And 8Rx UE can report a capability of two, four, six or eight layers of maximum number of DL MMO layer. | Qualcomm |
| Consider multi-panel UEs for antenna switching. | vivo |
| For antenna switching across multiple slots, restrict that the slots are contiguous or within a given period | LG |
| Support antenna switching configuration for mTRP   * Two periodic/semi-persistent SRS resource sets for antenna switching in multi-TRP * The number of aperiodic SRS resource sets in single TRP is K, then number of aperiodic SRS resource sets for xTyR in multi-TRP should be 2\*K | Intel, vivo |
| Clarify how UE should handle OFDM symbols including potential guard period(s) associated with UL SRS antenna switching configuration between non-consecutive UL SRS symbols | Nokia/NSB |
| Support simple indication (e.g. RRC) in Rel-17 whether antenna correspondence holds or not between UL SRS transmission and DL DMRS reception | Nokia/NSB |

Companies’ further views on the above issues are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| CATT | Since N=1 SRS resource set for aperiodic SRS configuration is supported for 1T6R and 1T8R, it is natural to support N = 1for 1T4R. |
|  |  |
|  |  |

# Coverage and capacity enhancements

## RB-level partial frequency sounding (RPFS)

This section summarizes companies’ views on remaining issues for RPFS.

### 4.1.1 PF values

Companies discuss details about additional values for PF. Companies’ views are summarized as follows.

Table 4-1

|  |  |
| --- | --- |
| **Additional PF values** | |
| Values | Companies |
| Support additional PF values | * vivo: Support {3, 8, 12} * Futurewei: 3, 8, 12, 16, and fractional numbers * Huawei/HiSilicon: Support 3 if is a multiple of 3 |
| Do not support additional PF values | Intel, CMCC, OPPO |

For PF values, given there is no consensus on whether and how to support PF values other than {2, 4}, FL recommends the following.

***FL Proposal 4-1:*** *No consensus to support PF values other than {2, 4} for RPFS in Rel-17.*

Companies’ further views are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| Futurewei | More PF values should be supported, to allow this design to be useful for both capacity enhancement and coverage enhancement with higher flexibility. |
| LGE | Support the FL proposal. |
| NEC | Fine with the proposal. |
| Samsung | Support the FL proposal. |
| Huawei, HiSilicon | Support to introduce 3, since many SRS bandwidth is multiple times of 3. |
| Ericsson | Support |
| QC | Support |
| Lenovo/MotM | Fine |
| DOCOMO | We share Futurewei’s view. If the issue is to have SRS BW other than the existing ones, we can avoid it by having a restriction in section 4.1.4. |
| Intel | Support FL proposal. |
| OPPO | Support |
| CATT | Support the FL proposal. |
| Spreadtrum | Support |
| Xiaomi | Support |
| Nokia/NSB | Support the FL proposal. |
| CMCC | Support. |

### 4.1.2 Start RB location hopping

The remaining issues of start RB location hopping includes three aspects

* Whether to extend start RB location hopping to aperiodic SRS
* Whether to support start RB location hopping within a legacy FH period

**Whether to extend start RB location hopping to aperiodic SRS**

Companies’ views on this aspect are summarized as follows.

Table 4-2

|  |  |
| --- | --- |
| **Whether to extend start RB location hopping to aperiodic SRS** | |
| Views | Companies |
| For aperiodic SRS, support same start RB location hopping approach as for P/SP SRS | ZTE, Huawei/HiSilicon, Ericsson, Futurewei, LGE, NEC, Qualcomm, MediaTek |
| For aperiodic SRS, support start RB location hopping across repetition symbols for R>1 | CATT, MediaTek |
| Start RB location hopping is not applicable on aperiodic SRS | Intel, OPPO |
|  |  |

***FL Proposal 4-2:*** *For aperiodic SRS, support same start RB location hopping approach as for P/SP SRS.*

Companies’ further views are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| Futurewei | Support this as it provides higher flexibility. |
| LGE | We are open to support start RB location hopping for aperiodic SRS, but not to support repetition symbols for R>1 case. R>1 case has own purpose to achieve coverage gain. |
| NEC | Support start RB location hopping for aperiodic SRS. |
| vivo | No need to introduce start RB location hopping into aperiodic SRS. |
| Huawei, HiSilicon | Support to extend start RB location hopping to aperiodic SRS, since it can be useful when there is more than one FH period for aperiodic SRS. |
| Ericsson | We continue to support the 1st option. |
| QC | Support to extend start RB location hopping to aperiodic SRS when more than one legacy FH exits within a slot. It is very beneficial as UE can keep phase coherency within a slot and this helps gNB do coherent interpolation. |
| *FL* | The proposal is updated based on the comments received so far. |
| Lenovo/MotM | Support to extend start RB location hopping to aperiodic SRS. |
| MediaTek | We support start RB hopping for aperiodic SRS.  As for R>1 case, this can be merged to next topic discussion for within a legacy FH period. This will be useful for better frequency coverage in some cases, e.g., when N\_symbol = R. |
| DOCOMO | While not sure how much beneficial it is for A-SRS, we are okay with unlocking this functionality for A-SRS as well, with the understanding that it could be turned off per RRC configuration. |
| Intel | We don’t see the necessity for aperiodic SRS. |
| OPPO | The aperiodic SRS is one-shot transmission. Thus, the benefit is doubtable. |
| CATT | We support to extend start RB location hopping to aperiodic SRS, and prefer start RB location hopping across repetition symbols for R>1. Since both R>1 and RPFS can be used to enhance SRS coverage, it is not necessary to keep both features at the same time. In addition, the entire bandwidth can be sounded for aperiodic SRS to support start RB location hopping across repetition symbols. |
| Xiaomi | Support the FL proposal |
| CMCC | Support to extend the start RB location for aperiodic SRS. |

**Whether to support start RB location hopping within a legacy FH period**

Companies’ views on this aspect are summarized as follows.

Table 4-3

|  |  |  |
| --- | --- | --- |
| **Whether to support start RB location hopping within a legacy FH period** | | |
| Views | | Companies |
| Yes | Start RB location hopping is performed across repetition symbols in one SRS resource when R>1 | MediaTek, Spreadtrum |
| Start RB location hopping is performed across SRS occasions in one legacy FH period | CATT |
| No or deprioritize | | NTT DCM, Huawei/HiSilicon, vivo, OPPO, LGE |

***FL Proposal 4-3:*** *TBD*

Companies’ further views are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| LGE | Prefer to deprioritize. |
| Huawei, HiSilicon | Not necessary. |
| MediaTek | Support hopping across repetition symbols within a legacy FH period when R>1. And we think hopping of intra-FH period and inter-FH period can be formulated in two separate terms, such as , so can also be controlled independently. |
| Intel | Some clarification on the benefit is needed. |
| OPPO | Not needed |
| CATT | Support start location hopping within a frequency hopping period. It can reduce the latency of sounding the whole bandwidth and improve the channel estimation accuracy without requiring additional interpolation calculation. |

### 4.1.3 Applicable cases

Some companies discuss whether to restrict RPFS applicable to FH enabled case only. Companies’ views are summarized as follows.

Table 4-4

|  |  |
| --- | --- |
| **Whether to restrict the applicable cases for RPFS** | |
| Views | Companies |
| Applicable for frequency hopping case only | Intel, CMCC, Qualcomm, OPPO, vivo |
| Applicable for both frequency hopping and non-frequency hopping cases | NEC, ZTE, Futurewei, CATT, LGE, Spreadtrum, Ericsson, Huawei/HiSilicon, Lenovo/MotM, NTT DCM, |

***FL Proposal 4-4:*** *TBD*

Companies’ further views are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| Futurewei | Support both cases, which will be especially useful when dynamic indication of PF and kF values is supported. |
| LGE | We share the view with Futurewei. |
| NEC | Support both cases. |
| vivo | Support applicable for frequency hopping case only |
| Huawei, HiSilicon | It’s not necessary to add the restriction. Partial sounding is beneficial for both case in terms of power boosting and capacity increase. |
| Ericsson | Support both cases |
| QC | Support only for frequency hopping. |
| Lenovo/MotM | Support to both cases. |
| MediaTek | Support both cases. |
| DOCOMO | We are fine to support both cases. |
| Intel | We think the partial sounding should be applied for frequency hopping only. |
| OPPO | Support for frequency hopping case only. For non-hopping cases, the current spec can achieve the same purpose. |
| CATT | Support both cases |
| Spreadtrum | Support both cases |
| Xiaomi | Support for frequency hopping case only. |
| Nokia/NSB | Support both cases |
| CMCC | If we support the non-frequency hopping cases, the partial frequency sounding with start location changed in every SRS transmission occasion. It is just another way of frequency hopping. Why do we need a duplicated enhancement ?  Also we are not sure if we need a dynamic indication of Pf and Kf. |

### 4.1.4 Further restriction on the number of RBs

One remaining issue is whether to further restrict the number of RBs for SRS transmission in RPFS. Companies’ views are summarized as follows.

Table 4-5

|  |  |
| --- | --- |
| **Further restriction on the number of RBs for RPFS** | |
| Alternatives | Companies |
| Alt 1: is an integer value | NEC, ZTE, Futurewei, Ericsson, Huawei/HiSilicon, MediaTek |
| Alt 2: is an integer value with minimum value 4 | NEC, NTT DCM, Nokia/NSB, MediaTek, Lenovo/MotM |
| Alt 3: is a multiple of 4 | Intel, Samsung, Apple, Nokia/NSB, Qualcomm, vivo, OPPO, LG |
| Alt 4: Round to a multiple of 4 in case of Alt 1 or Alt 2 | NEC, Qualcomm, vivo, CATT |

***FL Proposal 4-5:*** *TBD*

Companies’ further views are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| Futurewei | Support Alt 1.  Both Alt 1 and Alt 2 are generally acceptable. The only difference between them is about how to handle a resulting bandwidth less than 4 PRBs. We note that the actual limitation is not directly on the number of PRBs but on the minimum sequence length. So it seems Alt 1 together with the restriction that “SRS sequence shorter than the minimum length supported in the current specification is not pursued” is the best choice.  Alt 3 is very limiting, significantly restricting the potential PF values and usable SRS bandwidth configurations. Many of the SRS bandwidth values supported in the current standards cannot meet this requirement. Therefore, this is not preferred.  Alt 4 assumes that UE should transmit SRS with bandwidth values as multiples of 4 only. It is unclear why this restriction has to be imposed. This is not preferred at least from a futureproof or SRS flexibility perspective. |
| LGE | Support Alt 3 considering multiplexing between Rel-17 SRSs and between Rel-17 SRS and legacy SRS without complexity. |
| NEC | Support alternatives except Alt 3, as Alt 3 is too restrict and almost all cases are already supported by legacy configuration, the benefit of the feature is limited and the effort for discussion is wasted.  This has been discussed for several meetings, maybe we can firstly decide whether to restrict to be a multiple of 4 or not.  Proposal:   * Alt A: Support to be an integer value not restricted as a multiple of 4   + FFS: the details on the values. * Alt B: Restrict to be a multiple of 4 |
| vivo | Support Alt 3 or Alt 4. |
| Samsung | Support Alt3. Since this issue has been discussed so many meetings, it seems there is no consensus. Then, the default option can be Alt3 which is a current specification. |
| Huawei, HiSilicon | The further restriction is not necessary, since we already agreed that there is no new sequence length, i.e., length is 6 or multiple of 6, which is equivalent with Alt.1. For Alt 3, it’s too restricted which will limit the use case of partial sounding, which requires the bandwidth of SRS configuration is multiple of 8 or 16. And for Alt 4, the rounding will cause RB wasting. |
| Ericsson | As shown in our contribution, Alt.3 makes the whole feature useless since all but a handful of RPFS schemes (highlighted in green) can already be configured with existing equivalent SRS configurations. Neither of Alt.2,3,4 increase the SRS capacity. Hence, to make this feature useful and save our reputation, Alt.1 must be chosen. |
| QC | We have many conernes on Alt 1:   1. Orthogonality of SRS sequences when SRS sequence is not integer number of maxCS for the PF=2,4. 2. Multiplexing with legacy UEs. 3. MPR issues for 1,2,3 RBs especially at edge of the band.   Alt 4 makes best sence. |
| Lenovo/MotM | We support Alt 2.  We have the concern on orthogonality of SRS sequences if we go with Alt 1.  For example, for , if the configured SRS bandwidth is PRBs and the partial-sounding factor is , the resulting sequence length is , which is smaller than the maximum number of cyclic shifts for comb 4 (i.e., 12 CSs). For , if the configured SRS bandwidth is PRBs and the partial-sounding factor is , the resulting sequence length is , which is smaller than the maximum number of cyclic shifts for comb 4 (i.e., 8 CSs). |
| MediaTek | To make the feature useful, we can support Alt.1 or Alt.2. As the sequence length become more dynamic (also because comb-8 is supported), the orthogonality between CSs need to be handled properly. |
| DOCOMO | Support Alt 2. |
| Intel | Support Alt 3. |
| OPPO | Alt.3 for the better support of mulitplexing with legacy UEs and improve the efficency of SRS resource. |
| CATT | We have similar concerns with QC. Alt 4 includes Alt 3. They can be meraged. Alt 4 is a current specification, which does not have the issues on orthogonality of SRS sequences, multplexing with legacy UEs and MPR. |
| Xiaomi | Support Alt 3 or Alt 4. |
| Ericsson | Question to @Qualcomm, @Xiaomi, @CATT, @OPPO, @Intel, @Samsung, @vivo and others....  What is the benefit of the feature? Why would UE and NW make the effort to implement something that can be achieved already with legacy release? Could someone please explain the motivation for this? |
| Nokia/NSB | Support Alt-3 or Alt-2. |
| CMCC | Do not support the Alt 3, as it puts a strong limitation for the use of partial frequency souding. |

### 4.1.5 Dynamic signaling to determine PF and kF

It has been agreed that RRC signaling is used to indicate PF and kF. The following is to discuss whether more dynamic signaling, e.g., MAC CE or DCI can also be used to update these two values.

Table 4-6

|  |  |
| --- | --- |
| **Signaling to determine PF and kF** | |
| Alternatives | Companies |
| Use MAC CE to update P\_F and/or k\_F | CMCC, NTT DCM, Lenovo/MotM, CATT, Futurewei |
| Use DCI to indicate P\_F and/or k\_F | Lenovo/MotM, CATT, LG, Futurewei, LGE |
| Do not support to use MAC CE or DCI | Samsung, Nokia/NSB, Qualcomm, vivo, Spreadtrum, Ericsson, OPPO, Intel |

***FL Proposal 4-6:*** *TBD*

Companies’ further views are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| Futurewei | Added our support in above table.  Support dynamic indication via DCI for aperiodic SRS. If DCI indication is not supported, using MAC CE is also acceptable to us. |
| LGE | We support dynamic indication of P\_F and/or k\_F for aperiodic SRS for flexibility. |
| Samsung | Do not support to use MAC CE or DCI to update |
| Huawei, HiSilicon | Not necessary. RRC is enough for the signaling. |
| Ericsson | RRC is enough. |
| QC | No need to support MAC-CE or DCI. |
| DOCOMO | We support to use MAC CE to update P\_F and/or k\_F especially for P-/SP-SRS. For A-SRS, as both P\_F and k\_F are the ones to be configured per SRS-Resource, “dynamic indication” itself is already possible per SRS Request field in DCI. |
| Intel | RRC configuration is sufficient |
| OPPO | No need to support MAC-CE or DCI |
| CATT | Support MAC CE or DCI to indicate P\_F and/or k\_F |
| Spreadtrum | Do not support MAC-CE or DCI. |
| Xiaomi | No need to support MAC-CE or DCI |
| Nokia/NSB | RRC is sufficient. |
| CMCC | Do not support DCI based indication. |

## Comb-8

The remaining issues for Comb 8 includes

* For the supported Max CS = 6, how 4 ports are supported.
* Whether to support Max CS = 12

### 4.2.1 How to support 4 ports when Max CS = 6

Companies’ views on this aspect are summarized as follows.

Table 4-7

|  |  |  |
| --- | --- | --- |
| **How to support 4 ports when Max CS = 6** | | |
| Alternatives | Companies | Further details |
| Alt 1: Use two comb offsets to support 4 ports | Samsung, ZTE, vivo, Huawei/HiSilicon, Lenovo/MotM, MediaTek, NTT DOCOMO, Intel, OPPO | ZTE: Configure two comb offset values and two CS values  vivo: Revise the CS and comb offset allocation formulas as following |
| Alt 2: Allow 4 CSs for each comb offset to support 4 ports | Ericsson | Ericsson: Revise the CS allocation formula as following |

FL believes a simple solution is sufficient to address this issue. Hence FL recommends the following.

***FL Proposal 4-7:*** *To support 4 ports with Max CS = 6,*

* *Port 0 and Port 2 locate in n\_CS and (n\_CS+3) mod 6 in comb offset k\_TC, respectively.*
* *Port 1 and Port 3 locate in n\_CS and (n\_CS+3) mod 6 in comb offset (k\_TC + 4) mod 8, respectively.*
* *Note: n\_CS and k\_TC are the configured CS and comb offset values.*

Companies’ further views are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| vivo | Support the FL proposal |
| Samsung | We are fine with the FL proposal. |
| Huawei, HiSilicon | Support the proposal. |
| Ericsson | We believe Alt 1 can work IF partial sounding in < 4 RBs is NOT agreed and it will work for both maxCS = 6 and maxCS = 12.  If, however, partial sounding in < 4 RBs is agreed then one would have to update formula also there as sequence length will not always be multiple of maxCS. With Alt.2, there is no such dependencies on other agreement since it is more general.  So either we settle other agreements first or we select Alt.2  We also think port 0 and 2 should be together and 1 and 3 should be together as in 2 and 4 port cases.. |
| QC | Shouldn’t this be discussed after deciding on maxCS = 12? |
| *FL* | The proposal is updated based on the comment from Ericsson.  @Ericsson,  The grouping of ports is updated per your comment.  For the dependency issue you mentioned, my understanding is the issue of <4RB RPFS is not same. For <4RB RPFS, the issue is the sequence length is not a multiple of max CS. The CS allocation formula still works. Hence for <4RB RPFS, the issue can be solved by gNB configuring a proper CS value, or even a rule to limit the configurable CS values as you proposed previously.  @QC,  We have agreed that Max CS = 6. So my understanding is this issue needs to be solved at least for the case that Max CS = 6. Max CS = 12 is still FFS. It shouldn’t delay the issue to be solved for Max CS = 6. |
| Lenovo/MotM | Support the updated proposal. |
| MediaTek | Fine with the proposal |
| DOCOMO | Fine with the proposal |
| Intel | Fine with FL proposal.  But one question is if we agree to support Max CS=12 for Comb-8, do we still need this proposal? |
| OPPO | Support the proposal |

### 4.2.2 Whether to support Max CS = 12

Companies’ views on this aspect are summarized as follows.

Table 4-8

|  |  |
| --- | --- |
| **Whether to support Max CS = 12** | |
| Alternatives | Companies |
| Yes | NEC (when the sequence length is 12), NTT DCM, Nokia/NSB, Qualcomm, MediaTek, Lenovo/MotM, Ericsson, CATT, Intel |
| No | Samsung, Huawei/HiSilicon, Spreadtrum, Futurewei, vivo, OPPO, Spreadtrum |

***FL Proposal 4-8:*** *TBD*

Companies’ further views are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| Futurewei | No. For the maximum number of cyclic shifts for comb 8, if 12 is supported, then on the same resources, up to 8 x 12 = 96 SRS resources are multiplexed. It is questionable whether these many multiplexed resources are practical. |
| vivo | No |
| Samsung | Not support 12. |
| Huawei, HiSilicon | Not support 12 CSs.  Agree with Futurewei, in the practical case as we analyzed in our Tdoc (R1-2110786), we do not see the use cases to support 12 CSs for Comb-8.  In practical network, the number of available CSs is restricted by the channel delay, TA error and PDP leakage. In the case of 30K SCS, Comb-8 and 12 CSs, the tolerable delay corresponding to each cyclic shifts is about 343.73ns. However, even for the indoor case (CDL-A with 39ns RMS delay spread, the maximum is 376.68ns), the maximum delay is more than it.  Moreover, to ensure the orthogonality when the TA error exists, the delay gap between two CSs should be larger than double of TA error. However, the adjustment indicated by TA command is a multiple of 260.6ns for 30K SCS in current spec which means the TA error could be ≥260ns. So, how to ensure the orthogonality in the case of 12 CSs? |
| Ericsson | Support 12 CS. Whether MUX is possible depends on delay spread and implmentation. For indoor office where DS can be as low as 30 ns, it is certainly possible to enjoy this high SRS capacity. In Qualcomm contribution there are also evaluation showing the feasibility. |
| QC | Support 12 CS. It was decided in the last meeting whether to support maxCS = 12 based on further analysis/evaluation.  Based on our evaluation for CDL-C with 100 ns, very small performance degradation is observed between maxCS =12 mas CS =6 or 8. Please note that for this evaluation, the UE ports are assigned to consecutive CSs (e.g., CS0, CS1, CS2 and CS4) to stress the effect of port orthogonality. |
| Lenovo/MotM | Support 12 CS to increase the SRS capacity which is one of the important motivations of R17 SRS enhancement. |
| MediaTek | Support 12 CSs |
| DOCOMO | Yes, max. 12 CSs should be unlocked. Comb8 with max. 6 CS does not provide NR with additional multiplexing capacity on top of the existing specification. |
| Intel | Fine to support Max CS=12. |
| OPPO | Not support 12 CS |
| CATT | Support Max CS=12. |
| Spreadtrum | Do not support 12 CSs |
| Xiaomi | Fine to support CS=12 |
| Nokia/NSB | Support 12 CSs |
| CMCC | We have concerns for 12 CSs as it may cover too limited maximum delay spread and cannot work in the field. If additional rule is used to improve the orthogonality, it is almost the same as 6 CSs. |

## Others

The following issue is discussed by one companies.

|  |  |
| --- | --- |
| Support different repetition factors/SRS bandwidths for different symbols within one SRS resource | Nokia/NSB |
| Support to use RRC, MAC CE and DCI to indicate the Comb number and offset | Futurewei |

Companies’ further views are collected as follows.

|  |  |
| --- | --- |
| Companies | Views |
| Futurewei | Dynamic indication of comb and offset provides higher flexibility for SRS transmissions, which can enhance coverage/capacity and reduce collision. |
|  |  |
|  |  |

# Conclusion

The following proposals are recommended for further online and offline discussion.

# Appendix

## Previous agreements

Table 6-1

|  |
| --- |
| **RAN1#102e**  **Agreement**  Enhance the determination of aperiodic SRS triggering offset, with at least one of the following alternatives   * + Alt 1: Delay the SRS transmission to an available slot later than the triggering offset defined in current specification, including possible re-definition of the triggering offset   + Alt 2: Indicate triggering offset in DCI explicitly or implicitly   + Alt 3: Update triggering offset in MAC CE   + Further consideration aspects may include the cost v.s. the total combinations PDCCH and SRS locations for gNB to choose, DCI overhead, multi-UE SRS multiplexing, CA aspect, whether to have multiple opportunities to transmit SRS, etc.   **Agreement**  Study the following two alternatives in the scope to enhance at least one DCI format for aperiodic SRS triggering   * + Alt 1: Use UE-specific DCI, e.g., extending DCI 0\_1 without uplink data and without CSI   + Alt 2: Use group-common DCI, e.g., extending DCI 2\_3 for cases other than carrier switching   + Further consideration aspects may include simultaneous or CC-specific SRS triggering for multiple CCs, dynamic indication of SRS frequency resources, etc..   **Agreement**  For SRS overhead reduction, study reusing same resources among multiple usages, at least for “codebook” and “antenna switching”. Study aspects include   * + Whether implementation approach based on legacy SRS configuration is sufficient     - If not, and if there are benefits other than RRC overhead reduction, study further on the case that antenna switching and PUSCH have different number of Tx antennas, whether UL BWP for different SRS usages is the same or different, whether and how to ensure UE to use same virtualization, the set of applicable usages, UE implementation complexity and overhead, etc..   **Agreement**  For SRS antenna switching up to 8Rx, study the configuration of {1T6R, 1T8R, 2T6R, 2T8R, 4T6R, 4T8R}.   * + Study points may include CSI latency, performance considering aspects like insertion loss, use cases, antenna structure, UE power saving, SRS resource configuration, etc..   **Agreement**  For SRS coverage/capacity enhancements, evaluate and, if needed, specify one or more from three categories based on the following definition.   * + Class 1 (Time bundling): Utilize relationship among two or more occasions of one or more SRS resources in one or more slots to enable joint processing within time domain.     - Study aspects include the issue of phase discontinuity, interruption of SRS transmission by other UL signals, etc..   + Class 2 (Increase repetition): Change the legacy SRS pattern in one resource and one occasion from time domain by increasing SRS symbols for repetition.     - Study aspects include to use TD-OCC to compensate the negative impact on SRS capacity, inter-cell interference randomization, whether these SRS symbols are in one slot or consecutive slots, etc..   + Class 3 (Partial frequency sounding): Support more flexibility on SRS frequency resources to allow SRS transmission on partial frequency resources within the legacy SRS frequency resources.     - Study aspects include the partial frequency resources are with RB level or subcarrier level (e.g., larger comb, partial bandwidth), PAPR issue, etc..   **RAN1#103e**  **Agreement**  A given aperiodic SRS resource set is transmitted in the (t+1)-th available slot counting from a reference slot, where t is indicated from DCI, or RRC (if only one value of t is configured in RRC), and the candidate values of t at least include 0. Adopt at least one of the following options for the reference slot.   * Opt. 1: Reference slot is the slot with the triggering DCI. * Opt. 2: Reference slot is the slot indicated by the legacy triggering offset. * FFS the detailed definition of “available slot” considering UE processing complexity and timeline to determine available slot, potential co-existence with collision handling, etc., e.g.,   + Based on only RRC configuration, “available slot” is the slot satisfying: there are UL or flexible symbol(s) for the time-domain location(s) for all the SRS resources in the resource set and it satisfies the minimum timing requirement between triggering PDCCH and all the SRS resources in the resource set * FFS explicit or implicit indication of t * FFS whether updating candidate triggering offsets in MAC CE may be beneficial   **Agreement**  Support at least DCI 0\_1 and 0\_2 to trigger aperiodic SRS without data and without CSI.   * FFS whether/how to re-purpose the unused fields, e.g., the triggering offset(s) and the frequency resources for triggering A-SRS on one or more component carriers, SFI-index, etc. * FFS UL/DL DCI with data for aperiodic SRS * FFS group common DCI   **Agreement**  In Rel-17 SRS coverage and capacity enhancement, support at least one scheme from Class 2 and Class 3, and deprioritize Class 1.   * Note: Extensions of Rel-15/16 frequency hopping are included in Classes 2 and 3, e.g. where UE hops once per symbol within a Rel-17 SRS resource.   **Agreement**  Candidate schemes for Class 2:   * Scheme 2-0: Increase the number of repetition symbols in one slot * Scheme 2-1: Inter-slot repetition on consecutive symbols or non-consecutive symbols across slots * Scheme 2-2: Repetition with TD-OCC * Scheme 2-3: Repetition with CS hopping   Candidate schemes for Class 3:   * Scheme 3-1: RB-level partial frequency sounding * Scheme 3-2: Subcarrier-level partial frequency sounding * Scheme 3-3: Subband-level partial frequency sounding * Scheme 3-4: Partial-frequency sounding schemes assisted with CSI-RS, where SRS is transmitted in a subset of RBs of the original SRS frequency resource * Scheme 3-5: Dynamic change of SRS bandwidth with RB-level subband size scaling * Note: Consider issues like gNB receiver complexity, PAPR, etc., with above schemes * Note: Joint operation between Class 2 and Class 3 schemes can be considered   **Agreement**  For antenna switching up to 8Rx, support SRS resource configurations for {1T6R, 1T8R, 2T6R, 2T8R, [4T6R], 4T8R}.  **RAN1#104e**  **Agreement**  For Rel-17 SRS capacity and coverage enhancement, support the following   * Increase the maximum number of repetition symbols in one slot and one SRS resource to S   + Support at least one S value from {8, 10, 12, 14}     - FFS other candidate values * Support to transmit SRS only in  contiguous RBs in one OFDM symbol, where  indicates the number of RBs configured by BSRS and CSRS   + Support at least one PF value from {2, [3], 4, 8}     - FFS other candidate values, e.g., non-integer values for PF   + Note: SRS sequence shorter than the minimum length supported in the current specification is not pursued.   + No new sequence including length is introduced   + FFS it is applicable to frequency hopping and non-frequency hopping   + FFS detailed signaling mechanism to determine PF and the location of the  RBs * Support Comb 8   + Note: SRS sequence shorter than the minimum length supported in the current specification is not pursued. * FFS whether and if needed, how to use harmonized approach to define the three supported schemes * Note: other schemes for SRS capacity and coverage enhancements are not supported in Rel-17.   **Agreement**   * For aperiodic antenna switching SRS, support to configure N <=N\_max resource sets, where totally K resources are distributed in the N resource sets flexibly based on RRC configuration.   + For 1T6R, K=6, N\_max = [4], and each resource has 1 port.   + For 1T8R, K=8, N\_max = [4], and each resource has 1 port.   + For 2T6R, K=3, N\_max = [3], and each resource has 2 ports.   + For 2T8R, K=4, N\_max = [4], and each resource has 2 ports.   + (Working Assumption) For 4T8R, K=2, N\_max = [2], and each resource has 4 ports.   + FFS the number of supported candidate values of N for each xTyR. * FFS extension to increase N\_max for 1T4R, 2T4R, T=R and 1T2R cases for aperiodic, periodic and semi-persistent SRS resources * FFS the number of resources and resource sets for semi-persistent and periodic antenna switching SRS * Note: SRS could be transmitted over the last 6 OFDM symbols, or over any OFDM symbols within the slot subject to UE capability.   **Agreement**  Further study whether and if needed, how to achieve further enhancements on aperiodic SRS triggering and resource management based on repurposing unused fields in DCI format 0\_1/0\_2 without data and without CSI. Consider the following examples   * CAT A: Time-domain parameters   + A-1: Indication of available slot position, i.e., the t values   + A-2: Indication of slot offset   + A-3: Indication of SRS symbol-level offset   + A-4: Indication of time-domain behavior for SRS transmission over multiple OFDM symbols, e.g., repetition, hopping, and/or splitting * CAT B: Frequency-domain parameters   + B-1: Indication of a group of CCs for SRS transmission   + B-2: Indication of frequency domain resource in a BWP for SRS transmission   + B-3: Indication of whether DL/UL BWP is applied for SRS transmission * CAT C: Power control parameters   + C-1: Re-purpose ‘TPC command for PUSCH’ as ‘TPC command for SRS’     - FFS impact on power control, impact from triggering a group of CCs for SRS   + C-2: Indication of open loop power control parameter e.g., p0. * CAT D: Spatial-domain parameters, i.e., indication of SRS port and beamforming * CAT E: Extend the number of DCI codepoints for aperiodic SRS trigger states * Other examples are not precluded   **Agreement**  A list of t values is configured in RRC for each SRS resource set. Adopt at least one of the following for DCI indication of t.   * In DCI format 0\_1/0\_2 without data and without CSI request,   + Alt 1-1: Reuse the same scheme used for DCI format 0\_1/0\_2/1-1/1-2 that schedules a PDSCH or PUSCH   + Alt 1-2: Re-purpose unused DCI field to indicate t   + Alt 1-3: t is indicated by a configurable DCI field, where the DCI field may contain bits from unused fields and additional bits configured by gNB     - FFS design details with other potential field(s)   + FFS: whether t can be slot offset * In DCI format 0\_1/0\_2/1-1/1-2 that schedules a PDSCH or PUSCH   + Alt 2-1: t is indicated by adding a new configurable DCI field   + Alt 2-2: t is indicated without adding DCI payload * Note: The size of DCI payload does not change dynamically * Note: RAN1 should strive for unified solution for different DCI formats. * FFS: The number of RRC configured t values per SRS resource set and DCI bit field size.   **Agreement**  Confirm the following working assumption with modifications  An “available slot” is a slot satisfying there are UL or flexible symbol(s) for the time-domain location(s) for all the SRS resources in the resource set and it satisfies UE capability on the minimum timing requirement between triggering PDCCH and all the SRS resources in the resource set.   * From the first symbol carrying the SRS request DCI and the last symbol of the triggered SRS resource set, UE does not expect to receive SFI indication, UL cancellation indication or dynamic scheduling of DL channel/signal(s) on flexible symbol(s) that may change the determination of “available slot”. * Note: Collision handling between the triggered SRS and any other UL channel/signal is performed after the determination of available slot. * FFS: Rules to handle the case of multiple SRS resource sets with overlapping symbols and/or triggered by a same DCI   **RAN1#104bis-e**  **Agreement**  For increased repetition in Rel-17, support the following N\_symbol (number of OFDM symbols in one SRS resource) and R (repetition factor) values   * N\_symbol = 8, R = {1, 2, 4, 8} * N\_symbol = 12, R = {1, 2, [3], 4, 6, 12} * FFS the following configurations   + N\_symbol = 10, R = {1, 2, 5, 10}   + N\_symbol = 14, R = {1, 2, 7, 14} * FFS options to reduce SRS BW for R>1   **Agreement**  On aperiodic SRS configuration for antenna switching with > 4Rx, support the following N\_max values   * 1T6R: N\_max = 3 * 1T8R: N\_max = 4 * 2T6R: N\_max = 3 * 2T8R: N\_max = 4 * [4T8R: N\_max = 2] * The support of N\_max value does not imply the support of N value that is smaller than N\_max. This is FFS. * FFS whether further enhancement for single-DCI or multi-DCI based MTRP is needed   **Agreement**  For RB-level partial frequency sounding (RPFS) in Rel-17   * The start RB index of the RBs in the RBs is , where kF = {0, …, PF-1}   + FFS support start RB location (Noffset) hopping in different SRS occasions, symbols or frequency hopping periods, and if supported, detailed hopping pattern * Support to determine PF and Noffset at least via RRC configuration per SRS resource.   + FFS whether to introduce DCI and/or MAC CE in addition   **Working Assumption**  For DCI indication of “t” in Rel-17 SRS triggering offset enhancement   * For both DCI that schedules a PDSCH/PUSCH and DCI 0\_1/0\_2 without data and without CSI request   + t is indicated by adding a new configurable DCI field (up to 2 bits)     - Applies only when there are multiple candidate values of t configured   + No further enhancement to indicate “t” for DCI 0\_1/0\_2 without data and without CSI request at least when the new DCI field is configured   **Agreement**  On supported values of N for Rel-17 aperiodic SRS antenna switching with >4Rx, down-select at least one of the following alternatives in RAN1#105e   * Alt 1: All the non-zero integer values <= N\_max are supported for N * Alt 2: Support N=N\_max only * Alt 3: Support specific N values <= N\_max * FFS whether different alternatives may be selected for the same xTyR configuration subject to the UE capability on maximum number of symbols that can be used for SRS in a slot * FFS: whether different alternatives may be selected for different xTyR configuration   **Agreement**  Study the maximum number of cyclic shifts for Comb-8 in Rel-17, with the following alternatives as starting points   * Alt 1: The maximum number of CSs for Comb-8 is 6 * Alt 2: The maximum number of CSs for Comb-8 is 12, and introduce a rule to restrict applicable CSs when SRS sequence is shorter than the maximum number of CSs   **Agreement**   * Up to 4 “t” values can be configured per SRS resource set.   **Agreement**   * For RPFS in Rel-17, support PF = {2, 4}. * FFS 3, 8, 12, 16 or fractional numbers * Support at least one of the following alternatives (to be decided in RAN1#105-e)   + Alt 1: is an integer value   + Alt 2: is an integer value with minimum value 4   + Alt 3: is a multiple of 4   + Alt 4: Round to a multiple of 4 in case of Alt 1 or Alt 2   **Agreement**  On aperiodic SRS configuration for antenna switching with 4T8R, support N\_max = 2  **Agreement**  For RPFS SRS in Rel-17, adopt one of the following alternatives for sequence generation, where no new sequence length other than the ones supported in the current spec is introduced (to be decided in RAN1#105-e)   * Alt 1: Generate length- ZC sequence * Alt 2: Truncate from legacy length- sequence according to the location of RPFS SRS   **Agreement**  For antenna switching, support one of the following   * Alt 1: Support maximum one SRS resource set for periodic SRS and maximum one SRS resource set for semi-persistent SRS * Alt 2: Support up to two semi-persistent SRS resource sets in addition to a periodic SRS resource set   + Note: the two SP-SRS resource sets are not activated at the same time. * FFS whether further enhancement for single-DCI or multi-DCI based MTRP is needed * FFS whether configurations on SRS repetitions have impact * FFS relevant UE capability design   **RAN1#106-e**  **Agreement**  Confirm the following WA:  For DCI indication of “t” in Rel-17 SRS triggering offset enhancement   * For both DCI that schedules a PDSCH/PUSCH and DCI 0\_1/0\_2 without data and without CSI request   + t is indicated by adding a new configurable DCI field (up to 2 bits)     - Applies only when there are multiple   candidate values of t configured   + No further enhancement to indicate “t” for DCI 0\_1/0\_2 without data and without CSI request at least when the new DCI field is configured   **Agreement**  Support start RB location (Noffset) hopping in different SRS frequency hopping periods for RPFS and at least periodic/semi-persistent SRS, where Noffset is the start RB index of the RBs in the RBs.   * For a given SRS transmission occasion, , where khopping is same for all SRS occasions within a legacy FH period but changes across legacy FH periods, kF and PF are at least configured by RRC signaling (kF = {0, 1, …, PF-1}). * Support at least one pattern for khopping in time domain, FFS detailed pattern * Note: the legacy FH period is the period to sound the full SRS hopping bandwidth across the different subbands of RBs each. * This start RB location hopping is enabled or disabled by RRC signaling. * FFS whether MAC CE or DCI can be additionally used * When this start RB location hopping is disabled, khopping is fixed to be 0 for all SRS symbols * This start RB location hopping is UE optional. * FFS whether start RB location hopping is also applicable on SRS occasion(s) within one FH period (e.g., when R>1) and/or on aperiodic SRS, if so, how   **Agreement**  For aperiodic xTyR antenna switching SRS, where xTyR is from {1T6R, 1T8R, 2T6R, 2T8R, 4T8R}, support all the non-zero integer values N<=N\_max except N=1 for 1T8R   * For each xTyR configuration, UE does not expect multiple SRS resource sets are configured or triggered in one slot * UE does not expect that the OFDM symbols contained in one SRS resource set exceed UE capability on which OFDM symbols can be used for SRS taking guard period into account   **Agreement**  Support Opt. 2: Reference slot is the slot indicated by the legacy triggering offset.   * If DCI is transmitted in slot n, and k is the legacy triggering offset, reference slot is slot n+k. * Note: the legacy triggering offset can be 0, if slotOffset is absent.   **Conclusion**  MAC CE for t value update in Rel-17 is not supported.  **Agreement**  For antenna switching SRS, support maximum one SRS resource set for periodic SRS and maximum 2 SRS resource sets for semi-persistent SRS.   * Note: the two SP-SRS resource sets are not activated at the same time * For xTyR where y>4, if UE does NOT support this feature, support maximum one SRS resource set for periodic SRS and maximum one SRS resource set for semi-persistent SRS * Applies for all supported xTyR where y<=8 * For each xTyR antenna switching (except for 4T6R if supported), each periodic or semi-persistent resource set contains y/x resources.   This feature is UE optional: For UEs that do not support this feature, follow Rel-15 on the number of resource sets for periodic and semi-persistent SRS  **Agreement**   * Support 4T6R SRS antenna switching in Rel-17.   **Agreement**  For RPFS SRS sequence generation, support   * Alt 1: Generate length- ZC sequence.   **Agreement**  For SRS increased repetitions in Rel-17, support the following configurations, and no other values are supported.   * (N\_symbol, R) = {(8, 1), (8, 2), (8, 4), (8, 8), (12, 1), (12, 2), (12, 3), (12, 4), (12, 6), (12, 12), (10, 1), (10, 2), (10, 5), (10,10), (14, 1), (14, 2), (14, 7), (14, 14)} * Note: N\_symbol SRS symbols are adjacent in a slot.   **Agreement**   * On the presence of guard symbols in Rel-17 for SRS antenna switching, down-select one of the following   + Alt 1-0: Guard symbols are always-on, which is same as Rel-15   + Alt 1-1: Guard symbols are configurable subject to UE capability * On whether to introduce guard symbols between SRS resource sets for antenna switching, down-select one of the following   + Alt 2-0: Do not introduce guard symbols between SRS resource sets, i.e., guard symbols only appears between SRS resources in a resource set   + Alt 2-1: Introduce guard symbols between two sets mapped to consecutive slots * Note: Rel-15 guard period symbols are supported if none of the above enhancements is agreed   **Agreement**  For Comb-8 SRS in Rel-17, down-select one of the following in RAN1#106bis-e   * Alt 1: The maximum number of CSs for Comb-8 is 6 * Alt 2: The maximum number of CSs for Comb-8 is 12, and introduce a rule to restrict applicable CSs when SRS sequence is shorter than the maximum number of CSs   **RAN1#106bis-e**  **Agreement**  For two SRS resource sets of an xTyR antenna switching located in two consecutive slots, if UE is capable of transmitting SRS in all symbols in one slot, a minimum gap period of Y symbols exists between the last OFDM symbol occupied by the SRS resource set in the first slot and the first OFDM symbol occupied by the SRS resource set in the second slot   * The value of Y is same as the inter-resource GP defined in Rel-15 * FFS: Whether or not the minimum GP exists can be RRC configurable subject to UE capability * Whether this inter-set GP is needed for 4T6R can be discussed later per the decision on 4T6R configuration. * FFS: How/Whether to handle the case where the interval between SRS resource sets is larger than Y   **Agreement**  For the detailed pattern of when start RB location hopping across legacy FH periods is enabled, support the following   * For PF = 2, = {0, 1} * For PF = 4, = {0, 2, 1, 3} * Note: means for the (n+1)-th legacy FH period, where n = {0, 1, 2, 3, …}   **Agreement**  Bit width of SOI depends on the maximum number of “t” values configured for any of the aperiodic SRS resource sets (FFS: across all CCs or across a CC/BWP)   * The SOI field is 0 bit if the maximum number of ‘t’ values is one * If at least one resource set has “t” configured   + For the resource sets with “t” value configured, each of them is configured with K values of “t”, where 1<=K<=4   + t=0 applies for the resource set(s) without “t” configured in RRC * If none of the resource sets is configured with “t” values, follow Rel-15 approach to determine slot offset   **Agreement**  For comb-8 SRS in Rel-17, the maximum number of CSs is 6.   * FFS: Whether a maximum number of 12 CSs is supported   **Agreement**  For extension of aperiodic antenna switching SRS configurations for <=4Rx, support N=4 for 1T4R and N=2 for 1T2R/2T4R.   * The above extension is UE optional   **Agreement**  On SRS configuration for 4T6R, select at least one from the following three alternatives in RAN1#107e   * Alt 1: 4 + 2 * Alt 2: 2+2+2   + Alt 2-1:     - No guard symbols exist between the 1st and the 2nd transmission. Y guard symbol(s) exist between 2nd and 3rd transmission, where Y is same as the value defined in the current specification for different SCSs   + Alt 2-2:     - For SCS=15, 30 and 60KHz: No guard symbols exist     - For SCS=120 KHz: No guard symbols exist between the 1st  and the 2nd transmission, and 1 guard symbol exists between the 2nd and 3rd transmission * Clarification on the notation: means totally K resources are needed, where the k-th resource contains ports, 1<=k<=K |

# References

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