**3GPP TSG-RAN WG1 Meeting #102-eR1-200xxxx**

**e-Meeting, August 17th – 28th, 2020**

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| *CR-Form-v12.0* | | | | | | | | |
| **DRAFT CHANGE REQUEST** | | | | | | | | |
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|  | **38.212** | **CR** |  | **rev** |  | **Current version:** | **16.2.0** |  |
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| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* | | | | | | | | |
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| ***Proposed change affects:*** | UICC apps |  | ME | **x** | Radio Access Network | **x** | Core Network |  |

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| ***Title:*** | Corrections for Rel-16 NR-U | | | | | | | | | |
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| ***Source to WG:*** | Huawei | | | | | | | | | |
| ***Source to TSG:*** | R1 | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Work item code:*** | NR\_unlic-Core | | | | |  | ***Date:*** | | | 2020-08-31 |
|  |  | | | |  | |  | | |  |
| ***Category:*** | F |  | | | | | ***Release:*** | | | Rel-16 |
|  | *Use one of the following categories:* ***F*** *(correction)* ***A*** *(mirror corresponding to a change in an earlier release)* ***B*** *(addition of feature),* ***C*** *(functional modification of feature)* ***D*** *(editorial modification)*  Detailed explanations of the above categories can be found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | | | | | | | | *Use one of the following releases: Rel-8 (Release 8) Rel-9 (Release 9) Rel-10 (Release 10) Rel-11 (Release 11) Rel-12 (Release 12)* *Rel-13 (Release 13) Rel-14 (Release 14) Rel-15 (Release 15) Rel-16 (Release 16)* | |
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| ***Reason for change:*** | | 1. The parameter names in the description of DCI format 2\_0 are not aligned with corresponding names in 38.331. 2. Editorial corrections, and clarification to avoid confusion on the definiton of Y bits in DCI 0\_0 and DCI 0\_1 3. In DCI format 0\_0, the “UL/SUL indicator” is meant to be located in the last bit position of DCI format 0\_0. However, in current spec, the “ChannelAccess-CPext” is added after the “UL/SUL indicator 4. Align DCI Format 1\_0 bit length for all RNTIs and reflect RAN2 agreement on condition for inclusion of SFN LSB bits. 5. There is ambiguity on the TDRA table for DCI format 0\_1 when multiple PUSCH scheduling is configured to the UE. | | | | | | | | |
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| ***Summary of change:*** | | 1. [102-e-NR-unlic-NRU-DL\_Signals\_and\_Channels-03] TP#A in Section 2.1 of R1-2007357: Change *availableRB-SetPerCell-r16* to *availableRB-SetsToAddModList-r16.* Change *CO-DurationPerCell-r16* to *co-DurationsPerCellToAddModList-r16.* Change *searchSpaceSwitchTrigger-r16* to *searchSpaceSwitchTriggerToAddModList-r16* 2. [102-e-NR-unlic-NRU-ULSignalsChannels] TP#8 of R1-2007108: More compact wording to avoid double definition of the variable Y. Clarification that the frequency domain resource assignment applies to the active UL BWP 3. [102-e-NR-unlic-NRU-ChAcc-02] TP in Section 7 of R1-2007396: The “ChannelAccess-CPext” field in DCI 0\_0 is moved to the front of “UL/SUL indicator” 4. [102-e-NR-unlic-NRU-InitAccessProc-01] TP#5a in Section 2.3 of R1-2007113: Clarify conditions for inclusion of SFN LSB bits. Modify reserved bits to obtain same DCI Format bit length when scrambled with all RNTIs including msgB-RNTI and RA-RNTI and with and without operation in shared spectrum. 5. [102-e-NR-unlic-NRU-HARQ-02] TP in R1-2007102: Correction to include *pusch-TimeDomainAllocationListForMultiPUSCH* in the applicable PUSCH time domain resource allocation for DCI format 0\_1 in UE specific search space, and clarification of the TDRA field in DCI format 0\_1. | | | | | | | | |
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| ***Consequences if not approved:*** | | 1. Unaligned parameter names in 38.212 and 38.331. 2. Specification might be not clear 3. Ambiguous UE behaviour with SUL 4. Unclear specification when SFN LSB bits should be included in DCI Format 1\_0, and bit lengths are not aligned for all RNTIs 5. The UE applies an incorrect TDRA table when it is configured with *pusch-TimeDomainAllocationListForMultiPUSCH* | | | | | | | | |
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| ***Clauses affected:*** | | 7.3.1.3.1; 7.3.1.1.1; 7.3.1.1.2; 7.3.1.2.1; | | | | | | | | |
|  | |  | | | | | | | | |
|  | | **Y** | **N** |  | | | |  | | |
| ***Other specs*** | |  | **X** | Other core specifications | | | |  | | |
| ***affected:*** | |  | **X** | Test specifications | | | |  | | |
| ***(show related CRs)*** | |  | **X** | O&M Specifications | | | |  | | |
|  | |  | | | | | | | | |
| ***Other comments:*** | |  | | | | | | | | |
|  | |  | | | | | | | | |
| ***This CR's revision history:*** | |  | | | | | | | | |

7.3.1.1.1 Format 0\_0

DCI format 0\_0 is used for the scheduling of PUSCH in one cell.

The following information is transmitted by means of the DCI format 0\_0 with CRC scrambled by C-RNTI or CS-RNTI or MCS-C-RNTI:

- Identifier for DCI formats – 1 bit

- The value of this bit field is always set to 0, indicating an UL DCI format

- Frequency domain resource assignment –  bits if neither of the higher layer parameters *useInterlacePUCCH-PUSCH* in *BWP-UplinkCommon* and *useInterlacePUCCH-PUSCH* in *BWP-UplinkDedicated* is configured, where  is defined in clause 7.3.1.0

- For PUSCH hopping with resource allocation type 1:

-  MSB bits are used to indicate the frequency offset according to Clause 6.3 of [6, TS 38.214], where  if the higher layer parameter *frequencyHoppingOffsetLists* contains two offset values and  if the higher layer parameter *frequencyHoppingOffsetLists* contains four offset values

-  bits provides the frequency domain resource allocation according to Clause 6.1.2.2.2 of [6, TS 38.214]

- For non-PUSCH hopping with resource allocation type 1:

-  bits provides the frequency domain resource allocation according to Clause 6.1.2.2.2 of [6, TS 38.214]

- if any of the higher layer parameters *useInterlacePUCCH-PUSCH* in *BWP-UplinkCommon* and *useInterlacePUCCH-PUSCH* in *BWP-UplinkDedicated* is configured

- 5+Y bits provide the frequency domain resource allocation according to Clause 6.1.2.2.3 of [6, TS 38.214] if the subcarrier spacing for the active UL bandwidth part is 30 kHz.

- 6+Y bits provide the frequency domain resource allocation according to Clause 6.1.2.2.3 of [6, TS 38.214] if the subcarrier spacing for the active UL bandwidth part is 15 kHz.

If the DCI format 0\_0 is monitored in a UE-specific search space, the value of Y is determined by where is the number of RB sets contained in the UL BWP as defined in clause 7 of [6, TS38.214]. If the DCI 0\_0 is monitored in a common search space Y = 0.

\*\*\* Unchanged text omitted \*\*\*

- ChannelAccess-CPext – 2 bits indicating combinations of channel access type and CP extension as defined in Table 7.3.1.1.1-4 for operation in a cell with shared spectrum channel access; 0 bit otherwise.

- Padding bits, if required.

- UL/SUL indicator – 1 bit for UEs configured with *supplementaryUplink* in *ServingCellConfig* in the cell as defined in Table 7.3.1.1.1-1 and the number of bits for DCI format 1\_0 before padding is larger than the number of bits for DCI format 0\_0 before padding; 0 bit otherwise. The UL/SUL indicator, if present, locates in the last bit position of DCI format 0\_0, after the padding bit(s).

- If the UL/SUL indicator is present in DCI format 0\_0 and the higher layer parameter *pusch-Config* is not configured on both UL and SUL the UE ignores the UL/SUL indicator field in DCI format 0\_0, and the corresponding PUSCH scheduled by the DCI format 0\_0 is for the UL or SUL for which high layer parameter *pucch-Config* is configured;

- If the UL/SUL indicator is not present in DCI format 0\_0 and *pucch-Config* is configured, the corresponding PUSCH scheduled by the DCI format 0\_0 is for the UL or SUL for which high layer parameter *pucch-Config* is configured.

- If the UL/SUL indicator is not present in DCI format 0\_0 and *pucch-Config* is not configured, the corresponding PUSCH scheduled by the DCI format 0\_0 is for the uplink on which the latest PRACH is transmitted.

The following information is transmitted by means of the DCI format 0\_0 with CRC scrambled by TC-RNTI:

- Identifier for DCI formats – 1 bit

- The value of this bit field is always set to 0, indicating an UL DCI format

- Frequency domain resource assignment – number of bits determined by the following:

- bits if the higher layer parameter *useInterlacePUCCH-PUSCH* in *BWP-UplinkCommon* is not configured, where

-  is the size of the initial UL bandwidth part.

- For PUSCH hopping with resource allocation type 1:

-  MSB bits are used to indicate the frequency offset according to Table 8.3-1 in Clause 8.3 of [5, TS 38.213], where  if  and  otherwise

-  bits provides the frequency domain resource allocation according to Clause 6.1.2.2.2 of [6, TS 38.214]

- For non-PUSCH hopping with resource allocation type 1:

-  bits provides the frequency domain resource allocation according to Clause 6.1.2.2.2 of [6, TS 38.214]

- if the higher layer parameter *useInterlacePUCCH-PUSCH* in *BWP-UplinkCommon* is configured

- 5 bits provide the frequency domain resource allocation according to Clause 6.1.2.2.3 of [6, TS 38.214] if the subcarrier spacing for the active UL bandwidth part is 30 kHz

- 6 bits provide the frequency domain resource allocation according to Clause 6.1.2.2.3 of [6, TS 38.214] if the subcarrier spacing for the active UL bandwidth part is 15 kHz

- Time domain resource assignment – 4 bits as defined in Clause 6.1.2.1 of [6, TS 38.214]

- Frequency hopping flag – 1 bit according to Table 7.3.1.1.1-3, as defined in Clause 6.3 of [6, TS 38.214]

- Modulation and coding scheme – 5 bits as defined in Clause 6.1.4.1 of [6, TS 38.214]

- New data indicator – 1 bit, reserved

- Redundancy version – 2 bits as defined in Table 7.3.1.1.1-2

- HARQ process number – 4 bits, reserved

- TPC command for scheduled PUSCH – 2 bits as defined in Clause 7.1.1 of [5, TS 38.213]

- ChannelAccess-CPext – 2 bits indicating combinations of channel access type and CP extension as defined in Table 7.3.1.1.1-4 for operation in a cell with shared spectrum channel access; 0 bit otherwise

- Padding bits, if required.

- UL/SUL indicator – 1 bit if the cell has two ULs and the number of bits for DCI format 1\_0 before padding is larger than the number of bits for DCI format 0\_0 before padding; 0 bit otherwise. The UL/SUL indicator, if present, locates in the last bit position of DCI format 0\_0, after the padding bit(s).

- If 1 bit, reserved, and the corresponding PUSCH is always on the same UL carrier as the previous transmission of the same TB

7.3.1.1.2 Format 0\_1

DCI format 0\_1 is used for the scheduling of one or multiple PUSCH in one cell, or indicating CG downlink feedback information (CG-DFI) to a UE.

The following information is transmitted by means of the DCI format 0\_1 with CRC scrambled by C-RNTI or CS-RNTI or SP-CSI-RNTI or MCS-C-RNTI:

\*\*\* Unchanged text omitted \*\*\*

- Frequency domain resource assignment – number of bits determined by the following, where  is the size of the active UL bandwidth part:

\*\*\* Unchanged text omitted \*\*\*

- If the higher layer parameter *useInterlacePUCCH-PUSCH* in *BWP-UplinkDedicated* is configured

- 5 + Y bits provide the frequency domain resource allocation according to Clause 6.1.2.2.3 of [6, TS 38.214] if the subcarrier spacing for the active UL bandwidth part is 30 kHz. The 5 MSBs provide the interlace allocation and the Y LSBs provide the RB set allocation.

- 6 + Y bits provide the frequency domain resource allocation according to Clause 6.1.2.2.3 of [6, TS 38.214] if the subcarrier spacing for the active UL bandwidth part is 15 kHz. The 6 MSBs provide the interlace allocation and the Y LSBs provide the RB set allocation.

The value of Y is determined by where is the number of RB sets contained in the active UL BWP as defined in clause 7 of [6, TS38.214].

If "Bandwidth part indicator" field indicates a bandwidth part other than the active bandwidth part and if both resource allocation type 0 and 1 are configured for the indicated bandwidth part, the UE assumes resource allocation type 0 for the indicated bandwidth part if the bitwidth of the "Frequency domain resource assignment" field of the active bandwidth part is smaller than the bitwidth of the "Frequency domain resource assignment" field of the indicated bandwidth part.

- Time domain resource assignment – 0, 1, 2, 3, 4, 5, or 6 bits

- If the higher layer parameter *PUSCH-TimeDomainResourceAllocationList-ForDCIformat0\_1* is not configured and if the higher layer parameter *pusch-TimeDomainAllocationListForMultiPUSCH* is not configured and if the higher layer parameter *pusch-TimeDomainAllocationList* is configured, 0, 1, 2, 3, or 4 bits as defined in Clause 6.1.2.1 of [6, TS38.214]. The bitwidth for this field is determined as bits, where *I* is the number of entries in the higher layer parameter *pusch-TimeDomainAllocationList*;

- If the higher layer parameter *PUSCH-TimeDomainResourceAllocationList-ForDCIformat0\_1* is configured or if the higher layer parameter *pusch-TimeDomainAllocationListForMultiPUSCH is configured*, 0, 1, 2, 3, 4, 5 or 6 bits as defined in Clause 6.1.2.1 of [6, TS38.214]. The bitwidth for this field is determined as bits, where *I* is the number of entries in the higher layer parameter *PUSCH-TimeDomainResourceAllocationList-ForDCIformat0\_1* or *pusch-TimeDomainAllocationListForMultiPUSCH*;

- otherwise the bitwidth for this field is determined as bits, where *I* is the number of entries in the default table*.*

- Frequency hopping flag – 0 or 1 bit:

- 0 bit if only resource allocation type 0 is configured, or if the higher layer parameter *frequencyHopping* is not configured and the higher layer parameter pusch-RepTypeIndicatorForDCI-Format0-1 is not configured to *pusch-RepTypeB*, or if the higher layer parameter frequencyHoppingForDCI-Format0-1 is not configured and pusch-RepTypeIndicatorForDCI-Format0-1 is configured to *pusch-RepTypeB*, or if only resource allocation type 2 is configured;

- 1 bit according to Table 7.3.1.1.1-3 otherwise, only applicable to resource allocation type 1, as defined in Clause 6.3 of [6, TS 38.214].

- Modulation and coding scheme – 5 bits as defined in Clause 6.1.4.1 of [6, TS 38.214]

- New data indicator – 1 bit if the number of scheduled PUSCH indicated by the Time domain resource assignment field is 1; otherwise 2, 3, 4, 5, 6, 7 or 8 bits determined based on the maximum number of schedulable PUSCH among all entries in the higher layer parameter *pusch-TimeDomainAllocationListForMultiPUSCH*, where each bit corresponds to one scheduled PUSCH as defined in clause 6.1.4 in [6, TS 38.214].

- Redundancy version – – number of bits determined by the following:

- 2 bits as defined in Table 7.3.1.1.1-2 if the number of scheduled PUSCH indicated by the Time domain resource assignment field is 1;

- otherwise 2, 3, 4, 5, 6, 7 or 8 bits determined by the maximum number of schedulable PUSCHs among all entries in the higher layer parameter *pusch-TimeDomainAllocationListForMultiPUSCH*, where each bit corresponds to one scheduled PUSCH as defined in clause 6.1.4 in [6, TS 38.214] and redundancy version is determined according to Table 7.3.1.1.2-34.

\*\*\* Unchanged text omitted \*\*\*

7.3.1.2.1 Format 1\_0

\*\*\* Unchanged text omitted \*\*\*

The following information is transmitted by means of the DCI format 1\_0 with CRC scrambled by RA-RNTI or MsgB-RNTI:

- Frequency domain resource assignment – bits

-  is the size of CORESET 0 if CORESET 0 is configured for the cell and  is the size of initial DL bandwidth part if CORESET 0 is not configured for the cell

- Time domain resource assignment – 4 bits as defined in Clause 5.1.2.1 of [6, TS38.214]

- VRB-to-PRB mapping – 1 bit according to Table 7.3.1.2.2-5

- Modulation and coding scheme – 5 bits as defined in Clause 5.1.3 of [6, TS38.214], using Table 5.1.3.1-1

- TB scaling – 2 bits as defined in Clause 5.1.3.2 of [6, TS38.214]

- LSBs of SFN – 2 bits for the DCI format 1\_0 with CRC scrambled by MsgB-RNTI as defined in Clause 8.2A of [5, TS 38.213] if *msgB-responseWindow-r16* is configured to be larger than 10 ms; or 2 bits for the DCI format 1\_0 with CRC scrambled by RA-RNTI as defined in Clause 8.2 of [5, TS 38.213] for operation in a cell with shared spectrum channel access if *ra-ResponseWindow or ra-ResponseWindow-v1610* is configured to be larger than 10 ms; 0 bit otherwise

- Reserved bits – (16 – *A*) bits for operation in a cell without shared spectrum access, (18 – *A*) for operation in a cell with shared spectrum access, where the value of *A* is the number of bits for the field of ‘LSBs of SFN’ as defined above.

\*\*\* Unchanged text omitted \*\*\*

##### 7.3.1.3.1 Format 2\_0

DCI format 2\_0 is used for notifying the slot format, COT duration, available RB set, and search space set group switching.

The following information is transmitted by means of the DCI format 2\_0 with CRC scrambled by SFI-RNTI:

- If the higher layer parameter *slotFormatCombToAddModList* is configured,

- Slot format indicator 1, Slot format indicator 2, …, Slot format indicator *N*,

- If the higher layer parameter *availableRB-SetsToAddModList-r16* is configured,

- Available RB set Indicator 1, Available RB set Indicator 2, …, Available RB set Indicator *N1*,

- If the higher layer parameter *co-DurationsPerCellToAddModList-r16* is configured

- COT duration indicator 1, COT duration indicator 2, …, COT duration indicator *N2.*

- If the higher layer parameter *searchSpaceSwitchTriggerToAddModList-r16* is configured

- Search space set group switching flag 1, Search space set group switching flag 2, …, Search space set group switching flag *M.*

The size of DCI format 2\_0 is configurable by higher layers up to 128 bits, according to Clause 11.1.1 of [5, TS 38.213].