**[100b-e-NR-5G\_V2X\_NRSL-SYNC-01]**

**Email discussion/approval related to PSBCH contents - indication of TDD configuration**

[100b-e-NR-5G\_V2X\_NRSL-SYNC-01] Email discussion/approval related to PSBCH contents - Indication of TDD configuration

(a,k.a. issue 1-1) by 4/24, with potential TPs by 4/29 (CATT, Teng)

**Issue 1-1 Indication of TDD configuration**

***Proposal 1: Among the 12 bits for indication of TDD configuration:***

* ***X=1 bit indicates the number of patterns.***
* ***Y=4 bits indicate the periodicity information.***
* ***Z=7 bits indicate the UL slots.***

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| **Company** | **Views** |
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