

**TSG-RAN Meeting #20**  
**Hämeenlinna, Finland, 03-06 June 2003**

**RP-030297**

**Title:** CRs (Rel-4 and Rel-5 category A) to TS 25.322  
**Source:** TSG-RAN WG2  
**Agenda item:** 7.2.4

Spec	CR	Rev	Phase	Subject	Cat	Version-Current	Version-New	Doc-2nd-Level	Workitem
25.322	221	-	Rel-4	Receiver behaviour when detecting an AMD PDU duplicate	F	4.8.0	4.9.0	R2-031355	TEI4
25.322	222	-	Rel-5	Receiver behaviour when detecting an AMD PDU duplicate	A	5.4.0	5.5.0	R2-031356	TEI4
25.322	226	-	Rel-4	RLC window size reconfigurations	F	4.8.0	4.9.0	R2-031469	TEI4
25.322	227	-	Rel-5	RLC window size reconfigurations	A	5.4.0	5.5.0	R2-031470	TEI4

## CHANGE REQUEST

⌘ **25.322 CR 221** ⌘ rev **-** ⌘ Current version: **4.8.0** ⌘

For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.

**Proposed change affects:** UICC apps  ME  Radio Access Network  Core Network

<b>Title:</b>	⌘ Receiver behaviour when detecting an AMD PDU duplicate		
<b>Source:</b>	⌘ RAN WG2		
<b>Work item code:</b>	⌘ TEI4	<b>Date:</b>	⌘ 10/04/2003
<b>Category:</b>	⌘ <b>F</b>	<b>Release:</b>	⌘ Rel-4
	Use <u>one</u> of the following categories:		Use <u>one</u> of the following releases:
	<b>F</b> (correction)	<b>2</b> (GSM Phase 2)	
	<b>A</b> (corresponds to a correction in an earlier release)	<b>R96</b> (Release 1996)	
	<b>B</b> (addition of feature),	<b>R97</b> (Release 1997)	
	<b>C</b> (functional modification of feature)	<b>R98</b> (Release 1998)	
	<b>D</b> (editorial modification)	<b>R99</b> (Release 1999)	
	Detailed explanations of the above categories can be found in 3GPP <a href="#">TR 21.900</a> .	<b>Rel-4</b> (Release 4)	
		<b>Rel-5</b> (Release 5)	
		<b>Rel-6</b> (Release 6)	

<b>Reason for change:</b>	⌘ Duplicate detection is one function of AM RLC according to section 5. However, no details are given in the section 11.3 on "Acknowledged mode data transfer procedure", how the receiver behaves in case it detects a duplicate.
<b>Summary of change:</b>	⌘ A section is added, which gives details on how the AM RLC Receiver behaves, if it receives a AMD PDU duplicate
<b>Consequences if not approved:</b>	⌘ Incomplete specification

<b>Clauses affected:</b>	⌘ 11.3.4.8 (NEW)						
<b>Other specs affected:</b>	<table border="1" style="display: inline-table; border-collapse: collapse;"> <tr> <td style="width: 20px; text-align: center;">Y</td> <td style="width: 20px; text-align: center;">N</td> </tr> <tr> <td style="text-align: center;">⌘</td> <td style="text-align: center;">X</td> </tr> </table>	Y	N	⌘	X	Other core specifications	⌘
Y	N						
⌘	X						
		Test specifications					
		O&M Specifications					
<b>Other comments:</b>	⌘						

### How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at <http://www.3gpp.org/specs/CR.htm>. Below is a brief summary:

- 1) Fill out the above form. The symbols above marked ⌘ contain pop-up help information about the field that they are closest to.
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under <ftp://ftp.3gpp.org/specs/>. For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.

- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

[ ... ]

### 11.3.3 Reception of AMD PDU by the Receiver

Upon reception of an AMD PDU, the Receiver shall:

- update VR(R), VR(H) and VR(MR) state variables for each received AMD PDU (see clause 9.4);
- if a received AMD PDU includes a "Polling bit" set to "1", or "Missing PDU Indicator" is configured and the Receiver detects that a PDU is missing:
  - initiate the STATUS PDU transfer procedure;
- reassemble the received AMD PDUs into RLC SDUs;
- if "In-Sequence Delivery" is configured:
  - deliver the RLC SDUs in-sequence (i.e. in the same order as the RLC SDUs were originally transmitted by the peer entity) to upper layers through the AM-SAP.
- otherwise:
  - deliver the RLC SDUs in arbitrary order to upper layers through the AM-SAP.

#### 11.3.3a Reached maximum number of attempts

If  $VT(DAT) = MaxDAT$ , the Sender shall:

- if "No\_discard after MaxDAT number of transmissions" is configured:
  - initiate the RLC reset procedure, see subclause 11.4.
- if "SDU discard after MaxDAT number of transmissions" is configured:
  - initiate the "SDU discard with explicit signalling" procedure for the corresponding SDU, see subclause 11.6.

### 11.3.4 Abnormal cases

#### 11.3.4.1 Void

#### 11.3.4.2 Receiving an AMD PDU outside the reception window

Upon reception of an AMD PDU with "Sequence Number" outside the interval  $VR(R) \leq SN < VR(MR)$ , the Receiver shall:

- discard the AMD PDU;
- if the "polling bit" in the discarded AMD PDU is set to "1":
  - initiate the STATUS PDU transfer procedure.

#### 11.3.4.3 Timer\_Discard timeout

##### 11.3.4.3.1 SDU discard with explicit signalling

Upon expiry of the timer Timer\_Discard, the Sender shall:

- initiate the SDU discard with explicit signalling procedure, see subclause 11.6.2.

In the case where the TFC selection exchange has been initiated by sending the RLC Entity Info parameter to MAC, the UE may wait until after it provides MAC with the requested set of PDUs before discarding the afore-mentioned SDUs.

#### 11.3.4.4 Void

#### 11.3.4.5 Invalid length indicator value

If the "Length Indicator" of an AMD PDU has a value that is larger than the PDU size – RLC header size and is not one of the predefined values listed in the table of subclause 9.2.2.8, the Receiver shall:

- ignore that AMD PDU.

#### 11.3.4.6 Length Indicator value reserved for AMD PDU

Upon delivery by the lower layer of an AMD PDU that contains a "Length Indicator" value specified to be reserved for AMD PDUs in this version of the protocol, the Receiver shall:

- ignore that AMD PDU.

#### 11.3.4.7 Void

#### 11.3.4.8 Receiving an AMD PDU within the reception window more than once (Handling of Duplicates)

Upon reception of an AMD PDU with a "Sequence Number" within the interval  $VR(R) \leq SN < VR(MR)$ , for which "Sequence Number" an AMD PDU has already been received, the Receiver shall:

- discard the AMD PDU;
- consider the AMD PDU with this "Sequence Number" as having been correctly received in the next status report to be transmitted.
- if the "polling bit" in the discarded AMD PDU is set to "1":
  - initiate the STATUS PDU transfer procedure;

## 11.4 RLC reset procedure

[ ... ]

## CHANGE REQUEST

⌘ **25.322 CR 222** ⌘ rev **-** ⌘ Current version: **5.4.0** ⌘

For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.

**Proposed change affects:** UICC apps  ME  Radio Access Network  Core Network

<b>Title:</b>	⌘ Receiver behaviour when detecting an AMD PDU duplicate		
<b>Source:</b>	⌘ RAN WG2		
<b>Work item code:</b>	⌘ TEI4	<b>Date:</b>	⌘ 10/04/2003
<b>Category:</b>	⌘ <b>A</b>	<b>Release:</b>	⌘ Rel-5
	Use <u>one</u> of the following categories:		Use <u>one</u> of the following releases:
	<b>F</b> (correction)	2	(GSM Phase 2)
	<b>A</b> (corresponds to a correction in an earlier release)	R96	(Release 1996)
	<b>B</b> (addition of feature),	R97	(Release 1997)
	<b>C</b> (functional modification of feature)	R98	(Release 1998)
	<b>D</b> (editorial modification)	R99	(Release 1999)
	Detailed explanations of the above categories can be found in 3GPP <a href="#">TR 21.900</a> .	Rel-4	(Release 4)
		Rel-5	(Release 5)
		Rel-6	(Release 6)

<b>Reason for change:</b>	⌘ Duplicate detection is one function of AM RLC according to section 5. However, no details are given in the section 11.3 on "Acknowledged mode data transfer procedure", how the receiver behaves in case it detects a duplicate.
<b>Summary of change:</b>	⌘ A section is added, which gives details on how the AM RLC Receiver behaves, if it receives a AMD PDU duplicate
<b>Consequences if not approved:</b>	⌘ Incomplete specification

<b>Clauses affected:</b>	⌘ 11.3.4.8 (NEW)										
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Y	N										
⌘	X										
⌘	⌘										
⌘	⌘										
		Test specifications									
		O&M Specifications									
<b>Other comments:</b>	⌘										

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- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

[ ... ]

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### 11.3.4 Abnormal cases

#### 11.3.4.1 Void

#### 11.3.4.2 Receiving an AMD PDU outside the reception window

Upon reception of an AMD PDU with "Sequence Number" outside the interval  $VR(R) \leq SN < VR(MR)$ , the Receiver shall:

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##### 11.3.4.3.1 SDU discard with explicit signalling

Upon expiry of the timer Timer\_Discard, the Sender shall:

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In the case where the TFC selection exchange has been initiated by sending the RLC Entity Info parameter to MAC, the UE may wait until after it provides MAC with the requested set of PDUs before discarding the afore-mentioned SDUs.

#### 11.3.4.4 Void

#### 11.3.4.5 Invalid length indicator value

If the "Length Indicator" of an AMD PDU has a value that is larger than the PDU size – RLC header size and is not one of the predefined values listed in the table of subclause 9.2.2.8, the Receiver shall:

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#### 11.3.4.6 Length Indicator value reserved for AMD PDU

Upon delivery by the lower layer of an AMD PDU that contains a "Length Indicator" value specified to be reserved for AMD PDUs in this version of the protocol, the Receiver shall:

- ignore that AMD PDU.

#### 11.3.4.7 Void

#### 11.3.4.8 Receiving an AMD PDU within the reception window more than once (Handling of Duplicates)

Upon reception of an AMD PDU with a "Sequence Number" within the interval  $VR(R) \leq SN < VR(MR)$ , for which "Sequence Number" an AMD PDU has already been received, the Receiver shall:

- discard the AMD PDU;
- consider the AMD PDU with this "Sequence Number" as having been correctly received in the next status report to be transmitted.
- if the "polling bit" in the discarded AMD PDU is set to "1":
  - initiate the STATUS PDU transfer procedure;

[ ... ]

## CHANGE REQUEST

# 25.322 CR 226 # rev - # Current version: 4.8.0 #

For **HELP** on using this form, see bottom of this page or look at the pop-up text over the # symbols.

**Proposed change affects:** UICC apps  ME  Radio Access Network  Core Network

<b>Title:</b>	# RLC window size reconfigurations		
<b>Source:</b>	# RAN WG2		
<b>Work item code:</b>	# TEI4	<b>Date:</b>	# March 2003
<b>Category:</b>	# <b>F</b>	<b>Release:</b>	# Rel-4
	<i>Use <u>one</u> of the following categories:</i> <b>F</b> (correction) <b>A</b> (corresponds to a correction in an earlier release) <b>B</b> (addition of feature), <b>C</b> (functional modification of feature) <b>D</b> (editorial modification) Detailed explanations of the above categories can be found in 3GPP <a href="#">TR 21.900</a> .		<i>Use <u>one</u> of the following releases:</i> 2 (GSM Phase 2) R96 (Release 1996) R97 (Release 1997) R98 (Release 1998) R99 (Release 1999) Rel-4 (Release 4) Rel-5 (Release 5) Rel-6 (Release 6)

<b>Reason for change:</b>	# RRC signalling currently support reconfiguration of RLC parameters during a connection, e.g. with a RADIO BEARER RECONFIGURATION message. However, as discussed at RAN2#32 in relation to document R2-022651 the actions related to a reconfiguration (particularly a reduction) of the RLC window size are not explicitly specified in 25.322.  As a result, the UTRAN will always have to consider possible future RABs to be established during this connection, and reserve memory for these possible future RABs), when it is establishing the first RAB.  Especially for higher rate RAB's, not using all available memory will limit throughput unnecessarily.  Tdoc's R2-022651 and R2-031023 provide detailed information on this issue.
<b>Summary of change:</b>	# A new section is proposed which describes the UE behaviour in case of RLC parameter reconfiguration by higher layers.  In this section it is clearly indicated, that reconfigurations may never lead to data loss on AM RB's.  <b>Isolated Impact Change Analysis.</b> The behaviour for UE's not complying to this CR is not specified in detail today. If it is assumed that such a UE will reject a concerning RRC request, then a UTRAN will be made aware of the UE limitation. As a result, the UTRAN might not be able to add additional RAB's.

**Consequences if not approved:** ⌘ If this CR is not accepted, the indicated suboptimal memory usage and unnecessary throughput limitation will remain.

**Clauses affected:** ⌘ 9.7.x

<b>Other specs affected:</b>	⌘	<b>Y</b>	<b>N</b>	Other core specifications	⌘	
			<b>X</b>			Test specifications
			<b>X</b>			O&M Specifications

**Other comments:** ⌘

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## 9.7.x Reconfiguration of RLC parameters by upper layers

The RLC parameters for an RLC entity may be reconfigured (modified) by upper layers.

When an RLC parameter is reconfigured by the upper layer, the UE shall:

- start using the reconfigured value of the RLC parameter.

If the parameter Configured Rx Window Size is reconfigured:

- the UE shall update the state variable VR(MR), (see clause 9.4);
- for AMD PDUs with “Sequence Number” x such that  $VR(MR) \leq x < VR(H)$ :
  - the UE may discard these AMD PDUs-

If the parameter Configured Tx Window Size is reconfigured:

- the UE shall set the state variable VT(WS) equal to the Configured Tx Window Size;
- the UE shall update the state variable VT(MS), (see clause 9.4);
- for AMD PDUs with “Sequence Number” x such that  $VT(MS) \leq x < VT(S)$ :
  - the UE shall not discard any AMD PDUs that are not positively acknowledged;
  - the UE may discard AMD PDUs that are positively acknowledged.

## CHANGE REQUEST

# **25.322 CR 227** # rev **-** # Current version: **5.4.0** #

For **HELP** on using this form, see bottom of this page or look at the pop-up text over the # symbols.

**Proposed change affects:** UICC apps#  ME  Radio Access Network  Core Network

<b>Title:</b>	# RLC window size reconfigurations		
<b>Source:</b>	# RAN WG2		
<b>Work item code:</b>	# TEI4 <span style="float: right;"><b>Date:</b> # March 2003</span>		
<b>Category:</b>	<table style="width: 100%; border: none;"> <tr> <td style="width: 50%; vertical-align: top;"> # <b>A</b>  Use <u>one</u> of the following categories:  <b>F</b> (correction)  <b>A</b> (corresponds to a correction in an earlier release)  <b>B</b> (addition of feature),  <b>C</b> (functional modification of feature)  <b>D</b> (editorial modification)  Detailed explanations of the above categories can be found in 3GPP <a href="#">TR 21.900</a>. </td> <td style="width: 50%; vertical-align: top;"> <b>Release:</b> # Rel-5  Use <u>one</u> of the following releases:  2 (GSM Phase 2)  R96 (Release 1996)  R97 (Release 1997)  R98 (Release 1998)  R99 (Release 1999)  Rel-4 (Release 4)  Rel-5 (Release 5)  Rel-6 (Release 6) </td> </tr> </table>	# <b>A</b> Use <u>one</u> of the following categories: <b>F</b> (correction) <b>A</b> (corresponds to a correction in an earlier release) <b>B</b> (addition of feature), <b>C</b> (functional modification of feature) <b>D</b> (editorial modification) Detailed explanations of the above categories can be found in 3GPP <a href="#">TR 21.900</a> .	<b>Release:</b> # Rel-5 Use <u>one</u> of the following releases: 2 (GSM Phase 2) R96 (Release 1996) R97 (Release 1997) R98 (Release 1998) R99 (Release 1999) Rel-4 (Release 4) Rel-5 (Release 5) Rel-6 (Release 6)
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**Consequences if not approved:** ⌘ If this CR is not accepted, the indicated suboptimal memory usage and unnecessary throughput limitation will remain.

**Clauses affected:** ⌘ 9.7.x

<b>Other specs affected:</b>	⌘	<b>Y</b>	<b>N</b>	Other core specifications	⌘	
			<b>X</b>			Test specifications
			<b>X</b>			O&M Specifications

**Other comments:** ⌘

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- start using the reconfigured value of the RLC parameter.

If the parameter Configured Rx Window Size is reconfigured:

- the UE shall update the state variable VR(MR), (see clause 9.4);
- for AMD PDUs with “Sequence Number” x such that  $VR(MR) \leq x < VR(H)$ :
  - the UE may discard these AMD PDUs-

If the parameter Configured Tx Window Size is reconfigured:

- the UE shall set the state variable VT(WS) equal to the Configured Tx Window Size;
- the UE shall update the state variable VT(MS), (see clause 9.4);
- for AMD PDUs with “Sequence Number” x such that  $VT(MS) \leq x < VT(S)$ :
  - the UE shall not discard any AMD PDUs that are not positively acknowledged;
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