RP-020494

TSG RAN Meeting #17 Biarritz, France, 3 - 6 September, 2002

TitleCRs (Rel-5) to TS 25.105 and TS25.142 "Correction of the total power dynamic
range definition"SourceTSG RAN WG4Agenda Item7.4.5

RAN4 Tdoc	Spec	CR	R	Cat	Rel	Curr Ver	Title	Work Item
R4-021205	25.105	125		F	Rel-5	5.1.0	Total power dynamic range definition	TEI5
R4-021215	25.142	140		F	Rel-5	5.1.0	Total power dynamic range definition.	TEI5

3GPP TSR RAN WG4 Meeting #24

R4-021205

Helsinki, Finland 12 - 16 August 2002

CHANGE REQUEST											
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Proposed change affects: UICC apps# ME Radio Access Network X Core Network											
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Source:	ж <mark>г</mark>	RAN WG4									
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3 Definitions, symbols and abbreviations

3.1 Definitions

For the purposes of the present document, the following definitions apply.

Power Spectral Density: The units of Power Spectral Density (PSD) are extensively used in this document. PSD is a function of power versus frequency and when integrated across a given bandwidth, the function represents the mean power in such a bandwidth. When the mean power is normalised to (divided by) the chip-rate it represents the mean energy per chip. Some signals are directly defined in terms of energy per chip, (DPCH_Ec, Ec, and P-CCPCH_Ec) and others defined in terms of PSD (Io, Ioc, Ior and Îor). There also exist quantities that are a ratio of energy per chip to PSD (DPCH_Ec/Ior, Ec/Ior etc.). This is the common practice of relating energy magnitudes in communication systems.

It can be seen that if both energy magnitudes in the ratio are divided by time, the ratio is converted from an energy ratio to a power ratio, which is more useful from a measurement point of view. It follows that an energy per chip of X dBm/3.84 MHz (3.84 Mcps TDD option) or X dBm/1.28 MHz (1.28 Mcps TDD option) can be expressed as a mean power per chip of X dBm. Similarly, a signal PSD of Y dBm/3.84 MHz (3.84 Mcps TDD option) or Y dBm/1.28 MHz (1.28 Mcps TDD option) option) or Y dBm/1.28 MHz (1.28 Mcps TDD option) option) option) option) option option) option option) option option

Mean power: When applied to a CDMA modulated signal this is the power (transmitted or received) in a bandwidth of at least $(1 + \alpha)$ times the chip rate of the radio access mode. The period of measurement shall be a transmit timeslot excluding the guard period unless otherwise stated.

NOTE: The roll-off factor α is defined in section 6.8.1.

RRC filtered mean power: The mean power as measured through a root raised cosine filter with roll-off factor α and a bandwidth equal to the chip rate of the radio access mode.

NOTE: The RRC filtered mean power of a perfectly modulated CDMA signal is 0.246 dB lower than the mean power of the same signal.

Code domain power: That part of the mean power which correlates with a particular (OVSF) code channel. The sum of all powers in the code domain equals the mean power in a bandwidth of $(1 + \alpha)$ times the chip rate of the radio access mode.

Output power: The mean power of one carrier of the base station, delivered to a load with resistance equal to the nominal load impedance of the transmitter.

Maximum output power: The mean power level per carrier of the base station measured at the antenna connector in a specified reference condition. The period of measurement shall be a transmit timeslot excluding the guard period.

Rated output power: Rated output power of the base station is the mean power level per carrier that the manufacturer has declared to be available at the antenna connector.

Total power dynamic range: The difference between the maximum and the minimum output power of the base station for a specified reference condition.

--- Next Section---

6.8.2 Modulation Accuracy

The Error Vector Magnitude is a measure of the difference between the reference waveform and the measured waveform. This difference is called the error vector. Both waveforms pass through a matched Root Raised Cosine filter with bandwidth corresponding to the considered chip rate and roll-off $\alpha = 0,22$. Both waveforms are then further modified by selecting the frequency, absolute phase, absolute amplitude and chip clock timing so as to minimise the error vector. The EVM result is defined as the square root of the ratio of the mean error vector power to the mean

reference power expressed as a %. The measurement interval is one timeslot. The requirement is valid over the total power dynamic range as specified in subclause 3.1 - 6.4.3. See Annex C of TS 25.142 for further details.

6.8.2.1 Minimum Requirement

The Modulation accuracy shall not be worse than 12.5 %.

3GPP TSR RAN WG4 Meeting #24

R4-021215

Helsinki, Finland 12 - 16 August 2002

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--- Next Section----

6.8 Transmit Modulation

6.8.1 Modulation accuracy

6.8.1.1 Definition and applicability

The Error Vector Magnitude is a measure of the difference between the reference waveform and the measured waveform. This difference is called the error vector. Both waveforms pass through a matched Root Raised Cosine filter with bandwidth corresponding to the considered chip rate and roll-off $\alpha = 0,22$. Both waveforms are then further modified by selecting the frequency, absolute phase, absolute amplitude and chip clock timing so as to minimise the error vector. The EVM result is defined as the square root of the ratio of the mean error vector power to the mean reference power expressed as a %. The measurement interval is one timeslot. The requirement is valid over the total power dynamic range as specified in section 3.1-25.105 subclause 6.4.3. See Annex C of this specification for further details.

The requirements in this subclause shall apply to both Wide Area BS and Local Area BS.

NOTE: The theoretical modulated waveform shall be calculated on the basis that the transmit pulse shaping filter is a root-raised cosine (RRC) with roll-off $\alpha = 0,22$ in the frequency domain. The impulse response of the chip impulse filter $RC_0(t)$ is

$$RC_{0}(t) = \frac{\sin\left(\pi \frac{t}{T_{c}}(1-\alpha)\right) + 4\alpha \frac{t}{T_{c}}\cos\left(\pi \frac{t}{T_{c}}(1+\alpha)\right)}{\pi \frac{t}{T_{c}}\left(1-\left(4\alpha \frac{t}{T_{c}}\right)^{2}\right)}$$

Where the roll-off factor $\alpha = 0,22$ and T_C is the chip duration

6.8.1.2 Minimum Requirements

The error vector magnitude (EVM) shall not exceed 12,5 %. The requirement is valid over the total power dynamic range as specified in <u>section 3.1subclause 6.4.3 of TS 25.105</u>.

The normative reference for this requirement is TS 25.105 [1] subclause 6.8.2.1.