

TSG-RAN Meeting No. 14
December 11-14, 2001 Kyoto, Japan

TSGRP-010952

Source: Intel

Title: "Mitigating the Effect of CPICH Interference at the UE": Proposed
Way Forward

Document for: Approval

The following text is proposed as a possible way forward for the SI: "Mitigating the Effect of CPICH Interference at the UE".:

WG4 will progress the SI in the next WG4 meeting, and if consensus is reached the companies (not WG4) can present a CR and a WI proposal at the next RAN meeting.