

## **Presentation of Specification to TSG or WG**

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**Presentation to:** TSG-RAN Meeting #11

**Document for presentation:** TR 25.937 Version 2.0.0

**Presented for:** Approval

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### **Abstract of document:**

The present document is to help the TSG RAN WG3 group to specify the changes to existing specifications, needed for the introduction of the low chip rate TDD option in the UTRAN for Rel.4. It is intended to gather all information in order to trace the history and the status of the Work Task in RAN WG3. It is not intended to replace contributions and Change Requests, but only to list conclusions and make reference to agreed contributions and CRs. When solutions are sufficiently stable, the CRs can be issued.

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### **Changes since last presentation to TSG-RAN Meeting #10:**

Includes the input approved by the Low chip rate TDD ad hoc in WG3 #18. Includes the input proposed at the Low chip rate TDD ad hoc WG3 #19.

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### **Outstanding Issues:**

None.

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### **Contentious Issues:**

None.