TSG RAN#6 December 13-15 1999

Tdoc RP-99613

Nice, France

Source: TSG RAN WG1 Chairman

TSG RAN WG1 Report

TSG RAN WG1 Chairman

Antti Toskala

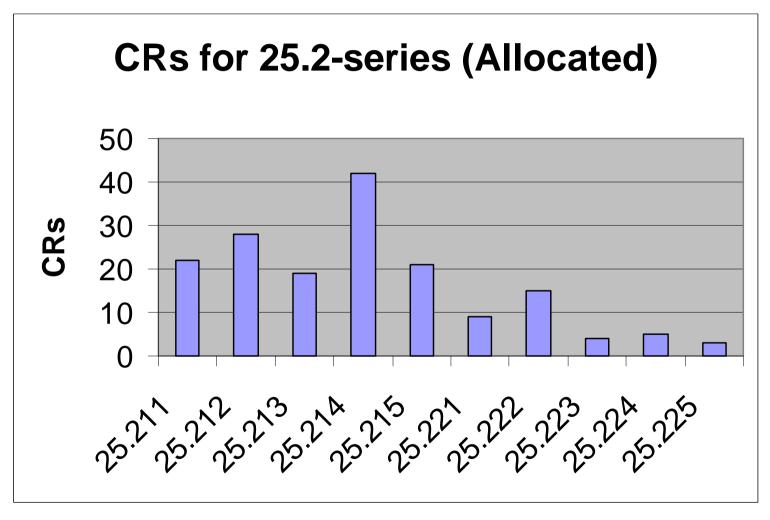
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CRsfrom WG1#8 & WG1#9

Over 160 CR numbers allocated





25.201 Physical Layer General Description

• No CRs presented to RAN



25.211 Physical Channels and M apping of Transport Channels to Physical Channels (FDD)

- Editorial changes and small adjustment
- Newer items:
 - Idle periods for position
 - Sliding paging indicators
 - 20 ms RACH message length included
 - CPCH power control preamble length defined
- Open Item: CPCH: Rel-99 or Rel-00 (also 25.213 & 25.214)



25.212 M ultiplexing and Channel coding (FDD)

- TFCI Coding/mapping for compressed mode refined
- Blind transport format detection description in Annex modified
- Common channel coding defined:
 DSCH and FACH with same coding options DCH
- Open Item: Compressed mode with puncturing
 For Rel-99 or Rel-00 (Different views in WG1)
- Addition for Rel-99: Smaller size Turbo
 More interleavers (Optional in Rel-99 due late introduction)

25.213 Spreading and scrambling (FDD)

- Editorial changes for the structure
- Compressed mode operation with secondary scrambling code corrected
- CPCH issues specified for power control preamble and message part
- SF 512 limitations clarified



25.214 Physical Layer Procedures (FDD)

- 20 ms message length included for RACH
 procedure + other updates for RACH procedure
- Open loop power control removed (In WG2 specs)
- AICH power offset refined
- Power control refined in SHO
- Closed loop TX diversity clarifications
- Open items:
 - DPCCH gating (Rel-99 or Rel-00)
 - Out-of-sync-state need some finalisation for

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25.215 M easurements (FDD)

- Ranges for measurement values defined
- Clarifications for compressed mode parameterization, CFN-SFN timing measurements and Primary CPICH measurements

 Decision item: CPICH SIR measurement for FDD. This needs to aligned based on WG2 and WG4 views (Reference: CR 25.215-017rev1 & WG1 Liaison statement)



25.221 Physical Channels and mapping of transport channels to physical channels (TDD)

- Slot structures clarified
- Primary and secondary CCPCH concept introduced
- Variable/multicode issue defined uplink/downlink
- Superframe concept removed
- midamble allocation
- TFCI bit transmission with multiple slot defined

25.222 M ultiplexing and Channel coding (TDD)

- TFCI with secondary CCPCH included
- TFCI mapping clarified
- Physical channel mapping modified
- Rate matching rule updated for TDD

Open item: Small size turbo interleavers (same as FDD)



25.223 Spreading and Scrambling (TDD)

- Code allocation clarified
- Terminology aligned
- Primary and Secondary CCPCH terminology included



25.224 Physical Layer Procedures (TDD)

- STTD with common channels included
- Spreading terminology clarified

 Open Item: Out-of-sync needs some definitions for TDD as well.



25.225 M easurements (TDD)

- Superframe references were removed
- Block STTD capability for Primary CCPCH included
- M easurement definitions and ranges updated



Meetings Since TSG RAN#5

- WG1#8 October 12-15 New York, USA, Host GBT, Inter Digital, Lucent & Omnipoint
- WG1#9 November 30 December 3, Dresden, Germany, Host Mannesman



RAN WG1 meetings year 2000 (Tentative)

- WG1#10 Jan 18-21 (China, Host: Nokia China) (Confirmed)
- WG1#11 Feb 28-March 3 (USA, Host: T1P1)
- WG1#12 April 10-14 (Korea, Host: TTA)
- WG1#13 May 22-26 (Japan)
- WG1#14 July 3-7 (Finland, Host: Nokia)
- WG1#15 August 21-25 (Germany, Host: Siemens)
- WG1#16 October 9-13 (Korea, Host: TTA)
- WG1#17 November 20-24 (TBD)
- Note: Dates indicate the week, meeting duration 4 days



RAN WG1 1999 Statistics

- 9 regular meetings held in 1999
- 1 Extra meeting (WG1#7bis) before RAN#5
- 1 Joint meeting with TSG SA WG4 on AMR issues
- T docs total over 2000



RANWG1Technicalreports

Also 2 TRs to be produced on:

- M ultiplexing and channel coding examples
- Items for not inclusion in release -99
 - Both presented for information, RAN WG1 would like to continue with TR on multiplexing and channel coding examples and to present to RAN 03/2000 version for approval.



Summary of open items for Rel-99 (RAN decision needed)

- Compressed mode with puncturing (FDD only)
- CPCH (FDD only)
- DPCCH gating (FDD only)
- Small size turbo inter leavers (FDD+TDD)
- Out-of-synchronisation state handling (FDD+TDD)
- Cell parameter cycling (TDD only)



Annex: RAN WG1 year 2000 Schedule

- TDD cell parameter cycling: 3/2000
- Out-of-synchronisation refinements: 3/2000
- Small turbo block sizes: 3/2000
- Compressed mode with puncturing: 3/2000
- CPCH issues: 3/2000
- DPCCH gating: 3/2000
- Hybrid ARQ: Conclusions by 6/2000, Updates to the specifications completed by 9/2000
- TX diversity refinements completed by 9/2000
- TDD 1.28 Mchips/s details completed by 9/2000
- FAUSCH changes completed by 9/2000
- NOTE: Presented separately in Tdoc RP-99705

