

**TSG-RAN Meeting #6**  
**Nice, France, 13 – 15 December 1999**

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**To:** RAN WG2  
**CC:** RAN Plenary  
**Source:** RAN WG1  
**Title:** LS on parity bit attachment to 0 bit transport block

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In WG1#9, the followings are discussed to measure BLER of one transport channel during DTX of this transport channel for the outer-loop power control:

- 1) Parity bits can be attached to 0 bit-transport block to measure the quality for the outer loop TPC.
- 2) The parity bit pattern is the same as CRC parity bit pattern of transport block size =0, i.e. all parity bit equals to 0.
- 3) Necessity of the parity bits attachment is designated via TFS. The number of transport blocks =0 of TFS designates that the parity bits need not to be attached to 0 bit- transport block. Transport block size =0 bit of TFS designates that the parity bits shall be attached to 0 bit- transport block.

CR for TS25.212 according to the above is discussed in WG1.

In the above 3), transport block size = 0 is used for control of parity bit attachment. However, WG1 noted that transport block size = 0 is not included in the range of transport block size in WG2 specifications.

In order to avoid inconsistency between WG1 and WG2, WG1 would like WG2 to clarify the situation of transport block size = 0 bit in the range of transport block.