**3GPP TSG-RAN WG4 Meeting #94-e *R4-2002412***

**Electronic meeting, 24th Feb - 6th Mar, 2020**

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| *CR-Form-v12.0* |
| **CHANGE REQUEST** |
|  |
|  | **38.141-2** | **CR** | **0126** | **rev** | **1** | **Current version:** | **16.2.0** |  |
|  |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* |
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| ***Proposed change affects:*** | UICC apps |  | ME |  | Radio Access Network | **x** | Core Network |  |

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|  |
| ***Title:***  | CR for 38.141-2: Introduction of PRACH demodulation requirements for NR HST  |
|  |  |
| ***Source to WG:*** | Huawei, HiSilicon |
| ***Source to TSG:*** | RAN4 |
|  |  |
| ***Work item code:*** | NR\_HST-Perf |  | ***Date:*** | 2020-02-10 |
|  |  |  |  |  |
| ***Category:*** | **B** |  | ***Release:*** | Rel-16 |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)Rel-12 (Release 12)**Rel-13 (Release 13)Rel-14 (Release 14)Rel-15 (Release 15)Rel-16 (Release 16)* |
|  |  |
| ***Reason for change:*** | This CR introduces PRACH performance requirements of high speed train scenario including UE velocity of up to 350km/h. |
|  |  |
| ***Summary of change:*** | Add new tables for introducing PRACH performance requirements of high speed train scenario of UE velocity of up to 350km/h. |
|  |  |
| ***Consequences if not approved:*** | The performance requirement part of PRACH under high speed train condition is missing |
|  |  |
| ***Clauses affected:*** | 8.4.1.1, 8.4.1.4.2, 8.4.1.5.1, A.6 |
|  |  |
|  | **Y** | **N** |  |  |
| ***Other specs*** |  | **x** |  Other core specifications  | TS/TR ... CR ...  |
| ***affected:*** |  | **x** |  Test specifications | TS 38.141-1, TS 38.141-2 |
| ***(show related CRs)*** |  | **x** |  O&M Specifications | TS/TR ... CR ...  |
|  |  |
| ***Other comments:*** |  |
|  |  |
| ***This CR's revision history:*** |  |

**<Start of First change>**

#### 8.4.1.1 Definition and applicability

The performance requirement of PRACH for preamble detection is determined by the two parameters: total probability of false detection of the preamble (Pfa) and the probability of detection of preamble (Pd). The performance is measured by the required SNR at probability of detection, Pd of 99%. Pfa shall be 0.1% or less.

Pfa is defined as a conditional total probability of erroneous detection of the preamble (i.e. erroneous detection from any detector) when input is only noise.

Pd is defined as conditional probability of detection of the preamble when the signal is present. The erroneous detection consists of several error cases – detecting only different preamble(s) than the one that was sent, not detecting any preamble at all, or detecting the correct preamble but with the out-of-bounds timing estimation value. For AWGN, TDLC300-100 and TDLA30-300, a timing estimation error occurs if the estimation error of the timing of the strongest path is larger than the time error tolerance values given in table 8.4.1.1-1.

Table 8.4.1.1-1: Time error tolerance for AWGN, TDLC300-100 and TDLA30-300

|  |  |  |
| --- | --- | --- |
| PRACH preamble | PRACH SCS (kHz) | Time error tolerance |
| AWGN | TDLC300-100 | TDLA30-300 |
| 0 | 1.25 | 1.04 us | 2.55 us | N/A |
| A1, A2, A3, B4, C0, C2 | 15 | 0.52 us | 2.03 us | N/A |
| 30 | 0.26 us | 1.77 us | N/A |
| 60 (FR2) | 0.13 us | N/A | 0.28 us |
| 120 | 0.07 us | N/A | 0.22 us |

The test preambles for normal mode are listed in table A.6-1. The test preambles for high speed train restricted set type A are listed in table A.6-2 and the test preambles for high speed train restricted set type B are listed in table A.6-3.

Which specific test(s) are applicable to BS is based on the test applicability rules defined in clause 8.1.2. The performance requirements for high speed train (table 8.4.1.5.1-4 and 8.4.1.5.1-5) are optional.

**<UNCHANGED PARTS HAVE BEEN OMITTED>**

**<END OF FIRST CHANGE>**

**<Start of SECOND change>**

##### 8.4.1.4.2 Procedure

1) Place the BS with its manufacturer declared coordinate system reference point in the same place as calibrated point in the test system, as shown in annex E.3.

2) Align the manufacturer declared coordinate system orientation of the BS with the test system.

3) Set the BS in the declared direction to be tested.

4) Connect the BS tester generating the wanted signal, multipath fading simulators and AWGN generators to a test antenna via a combining network in OTA test setup, as shown in annex E.3. Each of the demodulation branch signals should be transmitted on one polarization of the test antenna(s).

5) The characteristics of the wanted signal shall be configured according to the corresponding UL reference measurement channel defined in annex A and the test parameter *msg1-FrequencyStart* is set to 0.

6) The multipath fading emulators shall be configured according to the corresponding channel model defined in annex J.

7) Adjust the AWGN generator, according to the SCS and channel bandwidth. The power level for the transmission may be set such that the AWGN level at the RIB is equal to the AWGN level in table 8.4.1.4.2-1.

Table 8.4.1.4.2-1: AWGN power level at the BS input

|  |  |  |  |
| --- | --- | --- | --- |
| BS type | Sub-carrier spacing (kHz) | Channel bandwidth (MHz) | AWGN power level |
| *BS type 1-O* | 15 | 5 | -83.5 - ΔOTAREFSENS dBm / 4.5MHz |
| 10 | -80.3 - ΔOTAREFSENS dBm / 9.36MHz |
| 20 | -77.2 - ΔOTAREFSENS dBm / 19.08MHz |
| 30 | 10 | -80.6 - ΔOTAREFSENS dBm / 8.64MHz |
| 20 | -77.4 - ΔOTAREFSENS dBm / 18.36MHz |
| 40 | -74.2 - ΔOTAREFSENS dBm / 38.16MHz |
| 100 | -70.1 - ΔOTAREFSENS dBm / 98.28MHz |
| *BS type 2-O* | 60 | 50 | EISREFSENS\_50M + ΔFR2\_REFSENS + 15 dBm / 47.52 MHz |
| 100 | EISREFSENS\_50M + ΔFR2\_REFSENS + 18 dBm / 95.04 MHz |
| 120 | 50 | EISREFSENS\_50M + ΔFR2\_REFSENS + 15 dBm / 46.08 MHz |
| 100 | EISREFSENS\_50M + ΔFR2\_REFSENS + 18 dBm / 95.04 MHz |
| 200 | EISREFSENS\_50M + ΔFR2\_REFSENS + 21 dBm / 190.08 MHz |
| NOTE 1: ΔOTAREFSENS as declared in D.53 in table 4.6-1 and subclause 7.1.NOTE 2: ΔFR2\_REFSENS = -3 dB as described in subclause 7.1, since the OTA REFSENS receiver target reference direction (as declared in D.54 in table 4.6-1) is used for testing.NOTE 3: EISREFSENS\_50M as declared in D.28 in table 4.6-1. |

8) Adjust the frequency offset of the test signal according to table 8.4.1.5.1-1 or 8.4.1.5.1-2 or 8.4.1.5.1-3 or 8.4.1.5.1-4 or 8.4.1.5.1-5 or 8.4.1.5.2-1 or 8.4.1.5.2-2.

9) Adjust the equipment so that the SNR specified in table 8.4.1.5.1-1 or 8.4.1.5.1-2 or 8.4.1.5.1-3 or 8.4.1.5.1-4 or 8.4.1.5.1-5 or 8.4.1.5.2-1 or 8.4.1.5.2-2 is achieved at the BS input during the PRACH preambles.

10) The test signal generator sends a preamble and the receiver tries to detect the preamble. This pattern is repeated as illustrated in figure 8.4.1.4.2-1. The preambles are sent with certain timing offsets as described below. The following statistics are kept: the number of preambles detected in the idle period and the number of missed preambles.



Figure 8.4.1.4.2-1: PRACH preamble test pattern

The timing offset base value for PRACH preamble format 0 is set to 50% of Ncs. This offset is increased within the loop, by adding in each step a value of 0.1us, until the end of the tested range, which is 0.9us. Then the loop is being reset and the timing offset is set again to 50% of Ncs. The timing offset scheme for PRACH preamble format 0 is presented in Figure 8.4.1.4.2-2.



Figure 8.4.1.4.2-2: Timing offset scheme for PRACH preamble format 0

The timing offset base value for PRACH preamble format A1, A2, A3, B4, C0 and C2 is set to 0. This offset is increased within the loop, by adding in each step a value of 0.1us, until the end of the tested range, which is 0.8us. Then the loop is being reset and the timing offset is set again to 0. The timing offset scheme for PRACH preamble format A1, A2, A3, B4, C0 and C2 is presented in Figure 8.4.1.4.2-3.



**Figure 8.4.1.4.2-3: Timing offset scheme for PRACH preamble format A1 A2, A3, B4, C0 and C2**

#### 8.4.1.5 Test requirement

##### 8.4.1.5.1 Test requirement for *BS type 1-O*

Pfa shall not exceed 0.1%. Pd shall not be below 99% for the SNRs in tables 8.4.1.5.1-1 to 8.4.1.5.1-5.

Table 8.4.1.5.1-1: PRACH missed detection test requirements for Normal Mode, 1.25 kHz SCS

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Number of TX antennas | Number of demodulation branches | Propagation conditions and correlation matrix (annex J) | Frequency offset | SNR (dB) |
| Burst format 0 |
| 1 | 2 | AWGN | 0 | -14.2 |
| TDLC300-100 Low | 400 Hz  | -6.0 |

Table 8.4.1.5.1-2: PRACH missed detection test requirements for Normal Mode, 15 kHz SCS

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Number of TX antennas | Number of demodulation branches | Propagation conditions and correlation matrix (annex J) | Frequency offset | SNR (dB) |
| Burst format A1 | Burst format A2 | Burst format A3 | Burst format B4 | Burst format C0 | Burst format C2 |
| 1 | 2 | AWGN | 0 | -9.0 | -12.3 | -13.9 | -16.5 | -6.0 | -12.2 |
| TDLC300-100 Low | 400 Hz | -1.5 | -4.2 | -6.0 | -8.2 | 1.4 | -4.3 |

Table 8.4.1.5.1-3: PRACH missed detection test requirements for Normal Mode, 30 kHz SCS

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Number of TX antennas | Number of demodulation branches | Propagation conditions and correlation matrix (annex J) | Frequency offset | SNR (dB) |
| Burst format A1 | Burst format A2 | Burst format A3 | Burst format B4 | Burst format C0 | Burst format C2 |
| 1 | 2 | AWGN | 0 | -8.8 | -11.7 | -13.5 | -16.2 | -5.8 | -11.6 |
| TDLC300-100 Low | 400 Hz | -2.2 | -5.1 | -6.8 | -9.3 | 0.7 | -5.0 |

Table 8.4.1.5.1-4: PRACH missed detection requirements for high speed train, restricted set type A, 1.25 kHz SCS

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Number of TX antennas | Number of demodulation branches | Propagation conditions and correlation matrix (annex J) | Frequency offset | SNR (dB) |
| Burst format 0 |
| 1 | 2 | AWGN | 625 Hz | TBD |
| AWGN | 1340 Hz | TBD |

Table 8.4.1.5.1-5: PRACH missed detection requirements for high speed train, restricted set type B, 1.25 kHz SCS

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Number of TX antennas | Number of demodulation branches | Propagation conditions and correlation matrix (annex J) | Frequency offset | SNR (dB) |
| Burst format 0 |
| 1 | 2 | AWGN | 625 Hz | TBD |
| AWGN | 2334 Hz | TBD |

**<END of SECOND change>**

**<Start of THIRD change>**

# A.6 PRACH test preambles

Table A.6-1 Test preambles for Normal Mode in FR1

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Burst format | SCS (kHz) | Ncs | Logical sequence index | v |
| 0 | 1.25 | 13 | 22 | 32 |
| A1, A2, A3, B4, C0, C2 | 15 | 23 | 0 | 0 |
| 30 | 46 | 0 | 0 |

Table A.6-2: Test preambles for high speed train restricted set type A

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Burst format | SCS (kHz) | Ncs | Logical sequence index | v |
| 0 | 1.25 | 15 | 384 | 0 |

Table A.6-3: Test preambles for high speed train restricted set type B

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Burst format | SCS (kHz) | Ncs | Logical sequence index | v |
| 0 | 1.25 | 15 | 30 | 30 |

**<END of THIRD change>**