**3GPP TSG-RAN4 Meeting #94-e *R4-2002213***

**Online, 24th February – 6th March 2020**

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| *CR-Form-v12.0* |
| **CHANGE REQUEST** |
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|  | **38.133** | **CR** | **0515** | **rev** | **1** | **Current version:** | **15.8.0** |  |
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| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* |
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| ***Proposed change affects:*** | UICC apps |  | ME |  | Radio Access Network |  | Core Network |  |

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| ***Title:***  | Correction to BWP switching delay |
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| ***Source to WG:*** | Huawei, HiSilicon |
| ***Source to TSG:*** | R4 |
|  |  |
| ***Work item code:*** | NR\_newRAT-Core |  | ***Date:*** | 2020-02-15 |
|  |  |  |  |  |
| ***Category:*** | **F** |  | ***Release:*** | Rel-15 |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed e?planations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)Rel-12 (Release 12)**Rel-13 (Release 13)Rel-14 (Release 14)Rel-15 (Release 15)Rel-16 (Release 16)* |
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| ***Reason for change:*** | According to the delay requirements defined in 38.133, UE shall be able to perform transmission/reception in the slot right after the beginning of DL slot n+TBWPswitchDelay when UE receives DCI or BWP inactivity timer expires in DL slot n.However, SCS may change during BWP switching. According to Table 8.6.2-1 TBWPswitchDelay is defined in number of slots with the smaller SCS between the SCS before after BWP switch. As a result, when SCS after BWP switching is smaller, unit of TBWPswitchDelay will be different with unit of slot n. the expression “slot n+TBWPswitchDelay” is confusing. UE may face the similar issue during RRC-based BWP switching.To eliminate the ambiguity, we purpose to change the wording of BWP switch delay as follows:* For DCI-based BWP switch UE shall be able to receive or transmit on the new BWP on the serving cell on which BWP switch on the first DL or UL slot occurs right after TBWPswitchDelay after the beginning of DL slot n, as depicted in following figures.

C:\Users\w00195360\AppData\Local\Microsoft\Windows\INetCache\Content.Word\bwp1.pngC:\Users\w00195360\AppData\Local\Microsoft\Windows\INetCache\Content.Word\bwp2.png* Similar wording changing also applies to Timer-based BWP switching and RRC based BWP switching.
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| ***Summary of change:*** | The wording of BWP switch delay is changed as follows:1. For DCI-based BWP switch UE shall be able to receive or transmit on the new BWP on the serving cell on which BWP switch on the first DL or UL slot occurs right after a time duration of TBWPswitchDelay which starts from the beginning of DL slot n, as depicted in following figures.
2. Similar wording changing also applies to Timer-based BWP switching and RRC based BWP switching
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| ***Consequences if not approved:*** | Confusion may be caused. |
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| ***Clauses affected:*** | 8.6.2, 8.6.3 |
|  |  |
|  | **Y** | **N** |  |  |
| ***Other specs*** |  | **X** |  Other core specifications  | TS/TR ... CR ...  |
| ***affected:*** |  | **X** |  Test specifications | TS/TR ... CR ...  |
| ***(show related CRs)*** |  | **X** |  O&M Specifications | TS/TR ... CR ...  |
|  |  |
| ***Other comments:*** |  |
|  |  |
| ***This CR's revision history:*** | Revisd from R4-2001586:The wording “on the first DL or UL slot occurs right after TBWPswitchDelay after the beginning of DL slot n” is changed to “on the first DL or UL slot occurs right after a time duration of TBWPswitchDelay which starts from the beginning of DL slot n” |

**<Start of modified section 1>**

### 8.6.2 DCI and timer based BWP switch delay

The requirements in this clause only apply to the case that the BWP switch is performed on a single CC.

For DCI-based BWP switch, after the UE receives BWP switching request at DL slot n on a serving cell, UE shall be able to receive PDSCH (for DL active BWP switch) or transmit PUSCH (for UL active BWP switch) on the new BWP on the serving cell on which BWP switch on the first DL or UL slot occurs right after a time duration of TBWPswitchDelay which starts from the beginning of DL slot n.

The UE is not required to transmit UL signals or receive DL signals until the first DL or UL slot occurs right after a time duration of TBWPswitchDelay which starts from the beginning of DL slot n except DCI triggering BWP switch on the cell where DCI-based BWP switch occurs. The UE is not required to follow the requirements defined in this clause when performing a DCI-based BWP switch between the BWPs in disjoint channel bandwidths or in partially overlapping channel bandwidths.

For timer-based BWP switch, the UE shall start BWP switch at DL slot n, where n is the beginning of a DL subframe (FR1) or DL half-subframe (FR2) immediately after a BWP-inactivity timer *bwp-InactivityTimer* [2] expires on a serving cell, and the UE shall be able to receive PDSCH (for DL active BWP switch) or transmit PUSCH (for UL active BWP switch) on the new BWP on the serving cell on which BWP switch on the first DL or UL slot occurs right after a time duration of TBWPswitchDelay which starts from the beginning of DL slot n.

The UE is not required to transmit UL signals or receive DL signals after *bwp-InactivityTimer* [2] expires on the cell where timer-based BWP switch occurs.

Depending on UE capability *bwp-SwitchingDelay* [2], UE shall finish BWP switch within the time duration TBWPswitchDelay defined in Table 8.6.2-1.

Table 8.6.2-1: BWP switch delay

|  |  |  |
| --- | --- | --- |
|  | NR Slot length (ms) | BWP switch delay TBWPswitchDelay (slots) |
| Type 1Note 1 | Type 2Note 1 |
| 0 | 1 | 1 | 3 |
| 1 | 0.5 | 2 | 5 |
| 2 | 0.25 | 3 | 9 |
| 3 | 0.125 | 6 | 18 |
| Note 1: Depends on UE capability.Note 2: If the BWP switch involves changing of SCS, the BWP switch delay is determined by the smaller SCS between the SCS before BWP switch and the SCS after BWP switch. |

Provided the UE does not have the required TCI-state information to receive PDCCH and PDSCH in the new BWP, the UE shall use old TCI-states before the BWP switch until a new MAC CE updating the required TCI-state information for PDCCH and PDSCH is received after the BWP switch.

If UE has the information on the required TCI-state information to receive PDCCH and PDSCH in the new BWP,

- UE shall be able to receive PDCCH and PDSCH with old TCI-states before the delay as specified in Clause 8.10 in the new BWP.

- UE shall be able to receive PDCCH and PDSCH with new TCI-states after the delay as specified in Clause 8.10 in the new BWP

### 8.6.3 RRC based BWP switch delay

For RRC-based BWP switch, after the UE receives RRC reconfiguration involving active BWP switching or parameter change of its active BWP, UE shall be able to receive PDSCH/PDCCH (for DL active BWP switch) or transmit PUSCH (for UL active BWP switch) on the new BWP on the serving cell on which BWP switch occurs on the first DL or UL slot right after a time duration of slots which begins from the beginning of DL slot n, where

DL slot n is the last slot containing the RRC command, and

is the length of the RRC procedure delay in millisecond as defined in clause 12 in TS 38.331 [2], and

 is the time used by the UE to perform BWP switch.

The UE is not required to transmit UL signals or receive DL signals during the time defined by on the cell where RRC-based BWP switch occurs.

**<End of modified section 1>**