**3GPP TSG-RAN WG4 Meeting #102-e R4-2204773**

**Online Meeting, 21 Feb. – 3 Mar. 2022**

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| --- |
| *CR-Form-v12.2* |
| **CHANGE REQUEST** |
|  |
|  | **38.101-3** | **CR** | **0689** | **rev** |  | **Current version:** | **17.4.0** |  |
|  |
| *For* ***[HELP](http://www.3gpp.org/3G_Specs/CRs.htm%22%20%5Cl%20%22_blank)*** *on using this form: comprehensive instructions can be found at <http://www.3gpp.org/Change-Requests>.* |
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| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network |  | Core Network |  |

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|  |
| ***Title:***  | CR to reflect the completed ENDC combinations for 3 bands DL with 3 bands UL into Rel16 TS 38.101-3 |
|  |  |
| ***Source to WG:*** | ZTE Corporation |
| ***Source to TSG:*** | R4 |
|  |  |
| ***Work item code:*** | DC\_R17\_LTE\_NR\_3DL3UL-Core |  | ***Date:*** | 2022-03-07 |
|  |  |  |  |  |
| ***Category:*** | B |  | ***Release:*** | Rel-17 |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)…Rel-16 (Release 16)Rel-17 (Release 17)Rel-18 (Release 18)**Rel-19 (Release 19)* |
|  |  |
| ***Reason for change:*** | Completed inter-band ENDC combinations for 3 band DL with 3 bands UL are introduced into TS 38.101-3 from RAN4 #101bis-e and #102-e meeting. |
|  |  |
| ***Summary of change:*** | The following approved contributions for the completed inter-band ENDC band combinations for 3 band DL with 3 bands UL are added from RAN4 #101-e1. R4-2204749 TP for TR 37.717-33\_DC\_3A\_n40A-n258A |
|  |  |
| ***Consequences if not approved:*** | The requirements for above band combinations are incomplete. |
|  |  |
| ***Clauses affected:*** | 5.5B.6.2 |
|  |  |
|  | **Y** | **N** |  |  |
| ***Other specs*** |  | **X** |  Other core specifications  | TS/TR ... CR ...  |
| ***affected:*** | **X** |  |  Test specifications | TS 38.521-3 |
| ***(show related CRs)*** |  | **X** |  O&M Specifications | TS/TR ... CR ...  |
|  |  |
| ***Other comments:*** |  |
|  |  |
| ***This CR's revision history:*** |  |

## << Start of change >>

### 5.5B.6 Inter-band EN-DC including FR1 and FR2

#### 5.5B.6.1 Void

#### 5.5B.6.2 Inter-band EN-DC configurations including FR1 and FR2 (three bands)

Table 5.5B.6.2-1: Inter-band EN-DC configurations including FR1 and FR2 (three bands)

| EN-DC configuration | Uplink EN-DC configuration (NOTE 1) |
| --- | --- |
| DC\_1A\_n3A-n257A2DC\_1A\_n3A-n257G2DC\_1A\_n3A-n257H2DC\_1A\_n3A-n257I2 | DC\_1A\_n3ADC\_1A\_n257ADC\_1A\_n257GDC\_1A\_n257HDC\_1A\_n257I |
| DC\_1A\_n28A-n257A2DC\_1A\_n28A-n257G2DC\_1A\_n28A-n257H2DC\_1A\_n28A-n257I2 | DC\_1A\_n28ADC\_1A\_n257ADC\_1A\_n257GDC\_1A\_n257HDC\_1A\_n257I |
| DC\_1A\_n77A-n257A2DC\_1A\_n77A-n257D2DC\_1A\_n77A-n257E2DC\_1A\_n77A-n257F2DC\_1A\_n77A-n257G2DC\_1A\_n77A-n257H2DC\_1A\_n77A-n257I2DC\_1A\_n77C-n257A2DC\_1A\_n77C-n257D2DC\_1A\_n77C-n257E2DC\_1A\_n77C-n257F2 | DC\_1A\_n77ADC\_1A\_n257ADC\_1A\_n257DDC\_1A\_n257GDC\_1A\_n257HDC\_1A\_n257IDC\_1A\_n77A-n257ADC\_1A\_n77A-n257GDC\_1A\_n77A-n257HDC\_1A\_n77A-n257I |
| DC\_1A\_n77(2A)-n257A2DC\_1A\_n77(2A)-n257D2DC\_1A\_n77(2A)-n257G2DC\_1A\_n77(2A)-n257H2DC\_1A\_n77(2A)-n257I2 | DC\_1A\_n77ADC\_1A\_n257ADC\_1A\_n257DDC\_1A\_n257GDC\_1A\_n257HDC\_1A\_n257I |
| DC\_1A\_n77A-n258A | DC\_1A\_n77ADC\_1A\_n258A |
| DC\_1A\_n78A-n257A2DC\_1A\_n78A-n257D2DC\_1A\_n78A-n257E2DC\_1A\_n78A-n257F2DC\_1A\_n78C-n257A2DC\_1A\_n78C-n257D2DC\_1A\_n78C-n257E2DC\_1A\_n78C-n257F2 | DC\_1A\_n78ADC\_1A\_n257ADC\_1A\_n257DDC\_1A\_n78A-n257A |
| DC\_1A\_n78A-n257G2DC\_1A\_n78A-n257H2DC\_1A\_n78A-n257I2DC\_1A\_n78A-n257J2DC\_1A\_n78A-n257K2DC\_1A\_n78A-n257L2DC\_1A\_n78A-n257M2 | DC\_1A\_n78ADC\_1A\_n257ADC\_1A\_n257GDC\_1A\_n257HDC\_1A\_n257IDC\_1A\_n78A-n257ADC\_1A\_n78A-n257GDC\_1A\_n78A-n257HDC\_1A\_n78A-n257I |
| DC\_1A\_n78A-n258A | DC\_1A\_n78ADC\_1A\_n258A |
| DC\_1A\_n79A-n257A2DC\_1A\_n79A-n257D2DC\_1A\_n79A-n257E2DC\_1A\_n79A-n257F2DC\_1A\_n79A-n257G2DC\_1A\_n79A-n257H2DC\_1A\_n79A-n257I2DC\_1A\_n79C-n257A2DC\_1A\_n79C-n257D2DC\_1A\_n79C-n257E2DC\_1A\_n79C-n257F2DC\_1A\_n79A-n257G2DC\_1A\_n79A-n257H2DC\_1A\_n79A-n257I2 | DC\_1A\_n79ADC\_1A\_n257ADC\_1A\_n257GDC\_1A\_n257HDC\_1A\_n257IDC\_1A\_n79A-n257ADC\_1A\_n79A-n257GDC\_1A\_n79A-n257HDC\_1A\_n79A-n257I |
| DC\_1A\_n79A-n258A | DC\_1A\_n79ADC\_1A\_n258A |
| DC\_2A\_n12A-n258A | DC\_2A\_n258ADC\_2A\_n12A |
| DC\_2A\_n12A-n260A | DC\_2A\_n260ADC\_2A\_n12A |
| DC\_2A\_n12A-n261A | DC\_2A\_n261ADC\_2A\_n12A |
| DC\_2A\_n41A-n260A | DC\_2A\_n41A |
| DC\_2A\_n41A-n260(2A)DC\_2A\_n41A-n260(3A)DC\_2A\_n41A-n260(4A) | DC\_2A\_n41A |
| DC\_2A\_n41A-n261A | DC\_2A\_n41A |
| DC\_2A\_n41A-n261(2A) | DC\_2A\_n41A |
| DC\_2A\_n71A-n261A | DC\_2A\_n261ADC\_2A\_n71A |
| DC\_2A\_n71A-n261(2A) | DC\_2A\_n261ADC\_2A\_n71A |
| DC\_3A\_n1A-n257A2DC\_3A\_n1A-n257D2DC\_3A\_n1A-n257E2DC\_3A\_n1A-n257F2DC\_3A\_n1A-n257G2DC\_3A\_n1A-n257H2DC\_3A\_n1A-n257I2DC\_3A\_n1A-n257J2DC\_3A\_n1A-n257K2DC\_3A\_n1A-n257L2DC\_3A\_n1A-n257M2 | DC\_3A\_n1ADC\_3A\_n257A |
| DC\_3A-3A\_n1A-n257A2DC\_3A-3A\_n1A-n257D2DC\_3A-3A\_n1A-n257E2DC\_3A-3A\_n1A-n257F2DC\_3A-3A\_n1A-n257G2DC\_3A-3A\_n1A-n257H2DC\_3A-3A\_n1A-n257I2DC\_3A-3A\_n1A-n257J2DC\_3A-3A\_n1A-n257K2DC\_3A-3A\_n1A-n257L2DC\_3A-3A\_n1A-n257M2 | DC\_3A\_n1ADC\_3A\_n257A |
| DC\_3A\_n40A-n258A | DC\_3A\_n40ADC\_3A\_n258ADC\_3A\_n40A-n258A |
| DC\_3A\_n28A-n257A2DC\_3A\_n28A-n257G2DC\_3A\_n28A-n257H2DC\_3A\_n28A-n257I2 | DC\_3A\_n28ADC\_3A\_n257ADC\_3A\_n257GDC\_3A\_n257HDC\_3A\_n257I |
| DC\_3A\_n40A-n258A | DC\_3A\_n40ADC\_3A\_n258A |
| DC\_3A\_n77A-n257A2DC\_3A\_n77A-n257D2DC\_3A\_n77A-n257E2DC\_3A\_n77A-n257F2DC\_3A\_n77A-n257G2DC\_3A\_n77A-n257H2DC\_3A\_n77A-n257I2DC\_3A\_n77C-n257A2DC\_3A\_n77C-n257D2DC\_3A\_n77C-n257E2DC\_3A\_n77C-n257F2 | DC\_3A\_n77ADC\_3A\_n257ADC\_3A\_n257DDC\_3A\_n257GDC\_3A\_n257HDC\_3A\_n257IDC\_3A\_n77A-n257ADC\_3A\_n77A-n257GDC\_3A\_n77A-n257HDC\_3A\_n77A-n257I |
| DC\_3A\_n77(2A)-n257A2DC\_3A\_n77(2A)-n257D2DC\_3A\_n77(2A)-n257G2DC\_3A\_n77(2A)-n257H2DC\_3A\_n77(2A)-n257I2 | DC\_3A\_n77ADC\_3A\_n257ADC\_3A\_n257DDC\_3A\_n257GDC\_3A\_n257HDC\_3A\_n257I |
| DC\_3A\_n77A-n258A | DC\_3A\_n77ADC\_3A\_n258ADC\_3A\_n77A-n258A |
| DC\_3A\_n78A-n257A2DC\_3A\_n78A-n257D2DC\_3A\_n78A-n257E2DC\_3A\_n78A-n257F2DC\_3A\_n78A-n257G2DC\_3A\_n78A-n257H2DC\_3A\_n78A-n257I2DC\_3A\_n78A-n257J2DC\_3A\_n78A-n257K2DC\_3A\_n78A-n257L2DC\_3A\_n78A-n257M2DC\_3A\_n78C-n257A2DC\_3A\_n78C-n257D2DC\_3A\_n78C-n257E2DC\_3A\_n78C-n257F2 | DC\_3A\_n78ADC\_3A\_n257ADC\_3A\_n257DDC\_3A\_n257GDC\_3A\_n257HDC\_3A\_n257IDC\_3A\_n257JDC\_3A\_n257KDC\_3A\_n78A-n257ADC\_3A\_n78A-n257GDC\_3A\_n78A-n257HDC\_3A\_n78A-n257I |
| DC\_3C\_n78A-n257A2DC\_3C\_n78A-n257D2DC\_3C\_n78A-n257E2DC\_3C\_n78A-n257F2DC\_3C\_n78A-n257G2DC\_3C\_n78A-n257H2DC\_3C\_n78A-n257I2DC\_3C\_n78A-n257J2DC\_3C\_n78A-n257K2DC\_3C\_n78A-n257L2DC\_3C\_n78A-n257M2 | DC\_3A\_n78ADC\_3A\_n257A |
| DC\_3A\_n78A-n258A | DC\_3A\_n78ADC\_3A\_n258A |
| DC\_3A-3A\_n78A-n257A2DC\_3A-3A\_n78A-n257D2DC\_3A-3A\_n78A-n257E2DC\_3A-3A\_n78A-n257F2DC\_3A-3A\_n78A-n257G2DC\_3A-3A\_n78A-n257H2DC\_3A-3A\_n78A-n257I2DC\_3A-3A\_n78A-n257J2DC\_3A-3A\_n78A-n257K2DC\_3A-3A\_n78A-n257L2DC\_3A-3A\_n78A-n257M2 | DC\_3A\_n78ADC\_3A\_n257ADC\_3A\_n257GDC\_3A\_n257HDC\_3A\_n257IDC\_3A\_n257JDC\_3A\_n257K |
| DC\_3A\_n78A-n258ADC\_3A\_n78A-n258GDC\_3A\_n78A-n258HDC\_3A\_n78A-n258IDC\_3A\_n78A-n258JDC\_3A\_n78A-n258KDC\_3A\_n78A-n258LDC\_3A\_n78A-n258M | DC\_3A\_n78ADC\_3A\_n258A |
| DC\_3A\_n79A-n257A2DC\_3A\_n79A-n257D2DC\_3A\_n79A-n257E2DC\_3A\_n79A-n257F2DC\_3A\_n79A-n257G2DC\_3A\_n79A-n257H2DC\_3A\_n79A-n257I2DC\_3A\_n79C-n257A2DC\_3A\_n79C-n257D2DC\_3A\_n79C-n257E2DC\_3A\_n79C-n257F2 | DC\_3A\_n79ADC\_3A\_n257ADC\_3A\_n257GDC\_3A\_n257HDC\_3A\_n257IDC\_3A\_n79A-n257ADC\_3A\_n79A-n257GDC\_3A\_n79A-n257HDC\_3A\_n79A-n257I |
| DC\_3A\_n79A-n258ADC\_3A\_n79A-n258DDC\_3A\_n79A-n258EDC\_3A\_n79A-n258FDC\_3A\_n79A-n258GDC\_3A\_n79A-n258HDC\_3A\_n79A-n258IDC\_3A\_n79A-n258JDC\_3A\_n79A-n258KDC\_3A\_n79A-n258L | DC\_3A\_n79ADC\_3A\_n258ADC\_3A\_n79A-n258A |
| DC\_5A\_n78A-n257A2DC\_5A\_n78A-n257DDC\_5A\_n78A-n257EDC\_5A\_n78A-n257FDC\_5A\_n78A-n257GDC\_5A\_n78A-n257HDC\_5A\_n78A-n257IDC\_5A\_n78A-n257JDC\_5A\_n78A-n257KDC\_5A\_n78A-n257LDC\_5A\_n78A-n257M | DC\_5A\_n78ADC\_5A\_n257A |
| DC\_7A\_n1A-n257A2DC\_7A\_n1A-n257D2DC\_7A\_n1A-n257E2DC\_7A\_n1A-n257F2DC\_7A\_n1A-n257G2DC\_7A\_n1A-n257H2DC\_7A\_n1A-n257I2DC\_7A\_n1A-n257J2DC\_7A\_n1A-n257K2DC\_7A\_n1A-n257L2DC\_7A\_n1A-n257M2 | DC\_7A\_n1ADC\_7A\_n257A |
| DC\_7A-7A\_n1A-n257A2DC\_7A-7A\_n1A-n257D2DC\_7A-7A\_n1A-n257E2DC\_7A-7A\_n1A-n257F2DC\_7A-7A\_n1A-n257G2DC\_7A-7A\_n1A-n257H2DC\_7A-7A\_n1A-n257I2DC\_7A-7A\_n1A-n257J2DC\_7A-7A\_n1A-n257K2DC\_7A-7A\_n1A-n257L2DC\_7A-7A\_n1A-n257M2 | DC\_7A\_n1ADC\_7A\_n257A |
| DC\_7A\_n78A-n257A2DC\_7A\_n78A-n257D2DC\_7A\_n78A-n257E2DC\_7A\_n78A-n257F2DC\_7A\_n78A-n257G2DC\_7A\_n78A-n257H2DC\_7A\_n78A-n257I2DC\_7A\_n78A-n257J2DC\_7A\_n78A-n257K2DC\_7A\_n78A-n257L2DC\_7A\_n78A-n257M2 | DC\_7A\_n78ADC\_7A\_n257ADC\_7A\_n257GDC\_7A\_n257HDC\_7A\_n257IDC\_7A\_n257JDC\_7A\_n257K |
| DC\_7A-7A\_n78A-n257A2DC\_7A-7A\_n78A-n257D2DC\_7A-7A\_n78A-n257E2DC\_7A-7A\_n78A-n257F2DC\_7A-7A\_n78A-n257G2DC\_7A-7A\_n78A-n257H2DC\_7A-7A\_n78A-n257I2DC\_7A-7A\_n78A-n257J2DC\_7A-7A\_n78A-n257K2DC\_7A-7A\_n78A-n257L2DC\_7A-7A\_n78A-n257M2 | DC\_7A\_n78ADC\_7A\_n257ADC\_7A\_n257GDC\_7A\_n257HDC\_7A\_n257IDC\_7A\_n257JDC\_7A\_n257KDC\_7A\_n78A-n257A |
| DC\_7A\_n78A-n258ADC\_7A\_n78A-n258GDC\_7A\_n78A-n258HDC\_7A\_n78A-n258IDC\_7A\_n78A-n258JDC\_7A\_n78A-n258KDC\_7A\_n78A-n258LDC\_7A\_n78A-n258MDC\_7C\_n78A-n258ADC\_7C\_n78A-n258GDC\_7C\_n78A-n258HDC\_7C\_n78A-n258IDC\_7C\_n78A-n258JDC\_7C\_n78A-n258KDC\_7C\_n78A-n258LDC\_7C\_n78A-n258M | DC\_7A\_n78ADC\_7A\_n258ADC\_7A\_n258GDC\_7A\_n258HDC\_7A\_n258IDC\_7C\_n78ADC\_7C\_n258ADC\_7C\_n258GDC\_7C\_n258HDC\_7C\_n258I |
| DC\_7A\_n79A-n257ADC\_7A\_n79A-n257GDC\_7A\_n79A-n257HDC\_7A\_n79A-n257IDC\_7A\_n79A-n257JDC\_7A\_n79A-n257KDC\_7A\_n79A-n257LDC\_7A\_n79A-n257M | DC\_7A\_n257ADC\_7A\_n257GDC\_7A\_n257HDC\_7A\_n257IDC\_7A\_n257JDC\_7A\_n257KDC\_7A\_n257LDC\_7A\_n257MDC\_7A\_n79A |
| DC\_7A\_n79A-n258ADC\_7A\_n79A-n258GDC\_7A\_n79A-n258HDC\_7A\_n79A-n258IDC\_7A\_n79A-n258JDC\_7A\_n79A-n258KDC\_7A\_n79A-n258LDC\_7A\_n79A-n258M | DC\_7A\_n258ADC\_7A\_n258GDC\_7A\_n258HDC\_7A\_n258IDC\_7A\_n258JDC\_7A\_n258KDC\_7A\_n258LDC\_7A\_n258MDC\_7A\_n79A |
| DC\_8A\_n40A-n258ADC\_8A\_n40A-n258DDC\_8A\_n40A-n258EDC\_8A\_n40A-n258FDC\_8A\_n40A-n258GDC\_8A\_n40A-n258HDC\_8A\_n40A-n258IDC\_8A\_n40A-n258JDC\_8A\_n40A-n258KDC\_8A\_n40A-n258LDC\_8A\_n40A-n258M | DC\_8A\_n40ADC\_8A\_n258ADC\_8A\_n40A-n258A |
| DC\_8A\_n77A-n257A2DC\_8A\_n77A-n257D2DC\_8A\_n77A-n257G2DC\_8A\_n77A-n257H2DC\_8A\_n77A-n257I2 | DC\_8A\_n77ADC\_8A\_n257ADC\_8A\_n257DDC\_8A\_n257GDC\_8A\_n257HDC\_8A\_n257I |
| DC\_8A\_n77(2A)-n257A2DC\_8A\_n77(2A)-n257D2DC\_8A\_n77(2A)-n257G2DC\_8A\_n77(2A)-n257H2DC\_8A\_n77(2A)-n257I2 | DC\_8A\_n77ADC\_8A\_n257ADC\_8A\_n257DDC\_8A\_n257GDC\_8A\_n257HDC\_8A\_n257I |
| DC\_11A\_n77A-n257A2DC\_11A\_n77A-n257D2DC\_11A\_n77A-n257G2DC\_11A\_n77A-n257H2DC\_11A\_n77A-n257I2 | DC\_11A\_n77ADC\_11A\_n257ADC\_11A\_n257DDC\_11A\_n257GDC\_11A\_n257HDC\_11A\_n257I |
| DC\_11A\_n77(2A)-n257A2DC\_11A\_n77(2A)-n257D2DC\_11A\_n77(2A)-n257G2DC\_11A\_n77(2A)-n257H2DC\_11A\_n77(2A)-n257I2 | DC\_11A\_n77ADC\_11A\_n257ADC\_11A\_n257DDC\_11A\_n257GDC\_11A\_n257HDC\_11A\_n257I |
| DC\_19A\_n77A-n257A2DC\_19A\_n77A-n257D2DC\_19A\_n77A-n257E2DC\_19A\_n77A-n257F2DC\_19A\_n77A-n257G2DC\_19A\_n77A-n257H2DC\_19A\_n77A-n257I2DC\_19A\_n77C-n257A2DC\_19A\_n77C-n257D2DC\_19A\_n77C-n257E2DC\_19A\_n77C-n257F2 | DC\_19A\_n77ADC\_19A\_n257ADC\_19A\_n257GDC\_19A\_n257HDC\_19A\_n257IDC\_19A\_n77A-n257ADC\_19A\_n77A-n257GDC\_19A\_n77A-n257HDC\_19A\_n77A-n257I |
| DC\_19A\_n78A-n257A2DC\_19A\_n78A-n257D2DC\_19A\_n78A-n257E2DC\_19A\_n78A-n257F2DC\_19A\_n78A-n257G2DC\_19A\_n78A-n257H2DC\_19A\_n78A-n257I2DC\_19A\_n78C-n257A2DC\_19A\_n78C-n257D2DC\_19A\_n78C-n257E2DC\_19A\_n78C-n257F2 | DC\_19A\_n78ADC\_19A\_n257ADC\_19A\_n257GDC\_19A\_n257HDC\_19A\_n257IDC\_19A\_n78A-n257ADC\_19A\_n78A-n257GDC\_19A\_n78A-n257HDC\_19A\_n78A-n257I |
| DC\_19A\_n79A-n257A2DC\_19A\_n79A-n257D2DC\_19A\_n79A-n257E2DC\_19A\_n79A-n257F2DC\_19A\_n79A-n257G2DC\_19A\_n79A-n257H2DC\_19A\_n79A-n257I2DC\_19A\_n79C-n257A2DC\_19A\_n79C-n257D2DC\_19A\_n79C-n257E2DC\_19A\_n79C-n257F2 | DC\_19A\_n79ADC\_19A\_n257ADC\_19A\_n257GDC\_19A\_n257HDC\_19A\_n257IDC\_19A\_n79A-n257ADC\_19A\_n79A-n257GDC\_19A\_n79A-n257HDC\_19A\_n79A-n257I |
| DC\_21A\_n77A-n257A2DC\_21A\_n77A-n257G2DC\_21A\_n77A-n257H2DC\_21A\_n77A-n257I2 | DC\_21A\_n77ADC\_21A\_n257ADC\_21A\_n257GDC\_21A\_n257HDC\_21A\_n257IDC\_21A\_n77A-n257ADC\_21A\_n77A-n257GDC\_21A\_n77A-n257HDC\_21A\_n77A-n257I |
| DC\_8A\_n78A-n257A2DC\_8A\_n78A-n257D2DC\_8A\_n78A-n257E2DC\_8A\_n78A-n257F2DC\_8A\_n78A-n257G2DC\_8A\_n78A-n257H2DC\_8A\_n78A-n257I2DC\_8A\_n78A-n257J2DC\_8A\_n78A-n257K2DC\_8A\_n78A-n257L2DC\_8A\_n78A-n257M2 | DC\_8A\_n78ADC\_8A\_n257A |
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| DC\_21A\_n79A-n257A2DC\_21A\_n79A-n257G2DC\_21A\_n79A-n257H2DC\_21A\_n79A-n257I2 | DC\_21A\_n79ADC\_21A\_n257ADC\_21A\_n257GDC\_21A\_n257HDC\_21A\_n257IDC\_21A\_n79A-n257ADC\_21A\_n79A-n257GDC\_21A\_n79A-n257HDC\_21A\_n79A-n257I |
| DC\_28A\_n3A-n257A2DC\_28A\_n3A-n257G2DC\_28A\_n3A-n257H2DC\_28A\_n3A-n257I2 | DC\_28A\_n3ADC\_28A\_n257ADC\_28A\_n257GDC\_28A\_n257HDC\_28A\_n257I |
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| DC\_66A\_n12A-n260A | DC\_66A\_n260ADC\_66A\_n12A |
| DC\_66A\_n12A-n261A | DC\_66A\_n261ADC\_66A\_n12A |
| DC\_66A\_n5A-n260(2A)DC\_66A\_n5A-n260(3A)DC\_66A\_n5A-n260(4A)DC\_66A\_n5A-n260(5A)DC\_66A\_n5A-n260(6A)DC\_66A\_n5A-n260(2H)DC\_66A\_n5A-n260(2G)DC\_66A\_n5A-n260(A-2G)DC\_66A\_n5A-n260(A-H)DC\_66A\_n5A-n260(A-G)DC\_66A\_n5A-n260(G-H)DC\_66A\_n5A-n260(2A-G)DC\_66A\_n5A-n260(2A-2G)DC\_66A\_n5A-n260(3A-G) | DC\_66A\_n5A-n260A |
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| DC\_66A\_n5A-n261(2A)DC\_66A\_n5A-n261(3A)DC\_66A\_n5A-n261(2G)DC\_66A\_n5A-n261(2H)DC\_66A\_n5A-n261(A-G)DC\_66A\_n5A-n261(A-H)DC\_66A\_n5A-n261(A-I)DC\_66A\_n5A-n261(A-J)DC\_66A\_n5A-n261(A-K)DC\_66A\_n5A-n261(G-H)DC\_66A\_n5A-n261(G-I)DC\_66A\_n5A-n261(G-J)DC\_66A\_n5A-n261(H-I)DC\_66A\_n5A-n261(A-2G)DC\_66A\_n5A-n261(A-G-H)DC\_66A\_n5A-n261(A-G-I)DC\_66A\_n5A-n261(2A-G)DC\_66A\_n5A-n261(2A-H)DC\_66A\_n5A-n261(2A-I)DC\_66A\_n5A-n261(3A-G) | DC\_66A\_n5A-n261A |
| DC\_66A\_n5A-n261(2A)DC\_66A\_n5A-n261(3A)DC\_66A\_n5A-n261(2G)DC\_66A\_n5A-n261(2H)DC\_66A\_n5A-n261(A-G)DC\_66A\_n5A-n261(A-H)DC\_66A\_n5A-n261(A-I)DC\_66A\_n5A-n261(A-J)DC\_66A\_n5A-n261(A-K)DC\_66A\_n5A-n261(G-H)DC\_66A\_n5A-n261(G-I)DC\_66A\_n5A-n261(G-J)DC\_66A\_n5A-n261(H-I)DC\_66A\_n5A-n261(A-2G)DC\_66A\_n5A-n261(A-G-H)DC\_66A\_n5A-n261(A-G-I)DC\_66A\_n5A-n261(2A-G)DC\_66A\_n5A-n261(2A-H)DC\_66A\_n5A-n261(2A-I)DC\_66A\_n5A-n261(3A-G) | DC\_66A\_n5A-n261A |
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| DC\_66A\_n41A-n260(2A)DC\_66A\_n41A-n260(3A)DC\_66A\_n41A-n260(4A) | DC\_66A\_n41A |
| DC\_66A\_n41A-n261A | DC\_66A\_n41A |
| DC\_66A\_n41A-n261(2A) | DC\_66A\_n41A |
| DC\_66A\_n71A-n260A | DC\_66A\_n71ADC\_66A\_n260A |
| DC\_66A\_n71A-n260(2A) | DC\_66A\_n71ADC\_66A\_n260A |
| DC\_66A\_n71A-n261A | DC\_66A\_n71ADC\_66A\_n261A |
| DC\_66A\_n71A-n261(2A) | DC\_66A\_n71ADC\_66A\_n261A |
| NOTE 1: Uplink EN-DC configurations are the configurations supported by the present release of specifications.NOTE 2: Applicable for UE supporting inter-band EN-DC with mandatory simultaneous Rx/Tx capability. |

## << End of change >>