**3GPP TSG- Meeting #**

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| *CR-Form-v12.1* |
| **CHANGE REQUEST** |
|  |
|  |  | **CR** |  | **rev** |  | **Current version:** |  |  |
|  |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* |
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| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network |  | Core Network |  |

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| --- |
|  |
| ***Title:***  |  |
|  |  |
| ***Source to WG:*** |  |
| ***Source to TSG:*** |  |
|  |  |
| ***Work item code:*** |  |  | ***Date:*** | 2022-03-05 |
|  |  |  |  |  |
| ***Category:*** | **B** |  | ***Release:*** |  |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)…Rel-15 (Release 15)Rel-16 (Release 16)Rel-17 (Release 17)Rel-18 (Release 18)* |
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| ***Reason for change:*** | Completed inter-band CA combinations for 3DL with 2 bands UL are introduced into TS 38.101-3 from RAN4 #101-bis-e and RAN4 #102-e meeting. |
|  |  |
| ***Summary of change:*** | The changes for the configurations for inter-band CA between FR1 and FR2 (three bands) in Table 5.5A.1-2 are captured in R4-2203968, which is the big CR for 3DL bands and 1UL band inter-band CA between FR1 and FR2. In this big CR, only changes for inter-band NR-DC configurations between FR1 and FR2 are captured.The following approved contributions of inter-band CA for 3 bands DL with 2 bands UL are added from RAN4 #101-bis-e and RAN4 #102-e.1. R4-2201049, Draft CR for 38.101-3 to introduce new combinations for NR inter-band CA DC 3 bands DL with 2 bands UL, Samsung, KDDI
2. R4-2201579, draft CR 38.101-3 to add new DC combinations, , Telstra
3. R4-2202189,TP for TR 38.717-03-02: CA\_n41-n66-n260 and DC\_n41-n66-n260, Ericsson, T-Mobile US
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|  |  |
| ***Consequences if not approved:*** | The requirements for above band combinations are incomplete. |
|  |  |
| ***Clauses affected:*** | 5.5B.7.2 |
|  |  |
|  | **Y** | **N** |  |  |
| ***Other specs*** |  | **X** |  Other core specifications  | TS/TR ... CR ...  |
| ***affected:*** | **X** |  |  Test specifications | TS/TR ... CR ... 38.521-3 |
| ***(show related CRs)*** |  | **X** |  O&M Specifications | TS/TR ... CR ...  |
|  |  |
| ***Other comments:*** |  |
|  |  |
| ***This CR's revision history:*** |  |

### *<< start of changes >>*

### *<< Unchanged sections omitted >>*

#### 5.5B.7.2 Inter-band NR-DC configurations between FR1 and FR2 (three bands)

Table 5.5B.7-2: Inter-band NR-DC configurations between FR1 and FR2 (three bands)

| Downlink NR DCconfiguration | Uplink NR DCconfiguration |
| --- | --- |
| DC\_n1A-n3A-n257ADC\_n1A-n3A-n257GDC\_n1A-n3A-n257HDC\_n1A-n3A-n257I | DC\_n1A-n3ADC\_n1A-n257ADC\_n1A-n257GDC\_n1A-n257HDC\_n1A-n257IDC\_n3A-n257ADC\_n3A-n257GDC\_n3A-n257HDC\_n3A-n257I |
| DC\_n1A-n18A-n257ADC\_n1A-n18A-n257GDC\_n1A-n18A-n257HDC\_n1A-n18A-n257I | DC\_n1A-n18ADC\_n1A-n257ADC\_n1A-n257GDC\_n1A-n257HDC\_n1A-n257IDC\_n18A-n257ADC\_n18A-n257GDC\_n18A-n257HDC\_n18A-n257I |
| DC\_n1A-n28A-n257ADC\_n1A-n28A-n257GDC\_n1A-n28A-n257HDC\_n1A-n28A-n257I | DC\_n1A-n28ADC\_n1A-n257ADC\_n1A-n257GDC\_n1A-n257HDC\_n1A-n257IDC\_n28A-n257ADC\_n28A-n257GDC\_n28A-n257HDC\_n28A-n257I |
| DC\_n1A-n41A-n257ADC\_n1A-n41A-n257GDC\_n1A-n41A-n257HDC\_n1A-n41A-n257I | DC\_n1A-n41ADC\_n1A-n257ADC\_n1A-n257GDC\_n1A-n257HDC\_n1A-n257IDC\_n41A-n257ADC\_n41A-n257GDC\_n41A-n257HDC\_n41A-n257I |
| DC\_n1A-n77A-n257ADC\_n1A-n77A-n257GDC\_n1A-n77A-n257HDC\_n1A-n77A-n257I | DC\_n1A-n257ADC\_n1A-n257GDC\_n1A-n257HDC\_n1A-n257IDC\_n77A-n257ADC\_n77A-n257GDC\_n77A-n257HDC\_n77A-n257I |
| DC\_n1A-n77(2A)-n257ADC\_n1A-n77(2A)-n257GDC\_n1A-n77(2A)-n257HDC\_n1A-n77(2A)-n257I | DC\_n1A-n77ADC\_n1A-n257ADC\_n1A-n257GDC\_n1A-n257HDC\_n1A-n257IDC\_n77A-n257ADC\_n77A-n257GDC\_n77A-n257HDC\_n77A-n257I |
| DC\_n1A-n78A-n257A1DC\_n1A-n78A-n257G1DC\_n1A-n78A-n257H1DC\_n1A-n78A-n257I1DC\_n1A-n78A-n257J1DC\_n1A-n78A-n257K1DC\_n1A-n78A-n257L1DC\_n1A-n78A-n257M1 | DC\_n1A-n78ADC\_n1A-n257ADC\_n1A-n257GDC\_n1A-n257HDC\_n1A-n257IDC\_n1A-n257JDC\_n1A-n257KDC\_n78A-n257ADC\_n78A-n257GDC\_n78A-n257HDC\_n78A-n257IDC\_n78A-n257JDC\_n78A-n257K |
| DC\_n1A-n79A-n257ADC\_n1A-n79A-n257GDC\_n1A-n79A-n257HDC\_n1A-n79A-n257I | DC\_n1A-n257ADC\_n1A-n257GDC\_n1A-n257HDC\_n1A-n257IDC\_n79A-n257ADC\_n79A-n257GDC\_n79A-n257HDC\_n79A-n257I |
| DC\_n2A-n5A-n260ADC\_n2A-n5A-n260GDC\_n2A-n5A-n260HDC\_n2A-n5A-n260IDC\_n2A-n5A-n260JDC\_n2A-n5A-n260KDC\_n2A-n5A-n260LDC\_n2A-n5A-n260M | DC\_n2A-n5ADC\_n2A-n260ADC\_n5A-n260ADC\_n2A-n260GDC\_n5A-n260GDC\_n2A-n260HDC\_n5A-n260HDC\_n2A-n260IDC\_n5A-n260IDC\_n2A-n260JDC\_n5A-n260JDC\_n2A-n260KDC\_n5A-n260KDC\_n2A-n260LDC\_n5A-n260LDC\_n2A-n260MDC\_n5A-n260M |
| DC\_n2A-n30A-n260ADC\_n2A-n30A-n260GDC\_n2A-n30A-n260HDC\_n2A-n30A-n260IDC\_n2A-n30A-n260JDC\_n2A-n30A-n260KDC\_n2A-n30A-n260LDC\_n2A-n30A-n260M | DC\_n2A-n30ADC\_n2A-n260ADC\_n30A-n260ADC\_n2A-n260GDC\_n30A-n260GDC\_n2A-n260HDC\_n30A-n260HDC\_n2A-n260IDC\_n30A-n260IDC\_n2A-n260JDC\_n30A-n260JDC\_n2A-n260KDC\_n30A-n260KDC\_n2A-n260LDC\_n30A-n260LDC\_n2A-n260MDC\_n30A-n260M |
| DC\_n2A-n66A-n260ADC\_n2A-n66A-n260GDC\_n2A-n66A-n260HDC\_n2A-n66A-n260IDC\_n2A-n66A-n260JDC\_n2A-n66A-n260KDC\_n2A-n66A-n260LDC\_n2A-n66A-n260M | DC\_n2A-n66ADC\_n2A-n260ADC\_n66A-n260ADC\_n2A-n260GDC\_n66A-n260GDC\_n2A-n260HDC\_n66A-n260HDC\_n2A-n260IDC\_n66A-n260IDC\_n2A-n260JDC\_n66A-n260JDC\_n2A-n260KDC\_n66A-n260KDC\_n2A-n260LDC\_n66A-n260LDC\_n2A-n260MDC\_n66A-n260M |
| DC\_n2A-n77A-n260ADC\_n2A-n77A-n260IDC\_n2A-n77A-n260JDC\_n2A-n77A-n260KDC\_n2A-n77A-n260LDC\_n2A-n77A-n260M | DC\_n2A-n260ADC\_n2A-n260GDC\_n2A-n260HDC\_n2A-n260IDC\_n77A-n260ADC\_n77A-n260GDC\_n77A-n260HDC\_n77A-n260I |
| DC\_n2A-n77A-n261ADC\_n2A-n77A-n261IDC\_n2A-n77A-n261JDC\_n2A-n77A-n261KDC\_n2A-n77A-n261LDC\_n2A-n77A-n261M | DC\_n2A-n261ADC\_n2A-n261GDC\_n2A-n261HDC\_n2A-n261IDC\_n77A-n261ADC\_n77A-n261GDC\_n77A-n261HDC\_n77A-n261I |
| DC\_n3A-n7A-n258ADC\_n3A-n7A-n258BDC\_n3A-n7A-n258CDC\_n3A-n7A-n258DDC\_n3A-n7A-n258EDC\_n3A-n7A-n258FDC\_n3A-n7A-n258GDC\_n3A-n7A-n258HDC\_n3A-n7A-n258IDC\_n3A-n7A-n258JDC\_n3A-n7A-n258KDC\_n3A-n7A-n258LDC\_n3A-n7A-n258M | DC\_n3A-n258ADC\_n3A-n258GDC\_n3A-n258HDC\_n3A-n258IDC\_n7A-n258ADC\_n7A-n258GDC\_n7A-n258HDC\_n7A-n258I |
| DC\_n3A-n7B-n258ADC\_n3A-n7B-n258BDC\_n3A-n7B-n258CDC\_n3A-n7B-n258DDC\_n3A-n7B-n258EDC\_n3A-n7B-n258FDC\_n3A-n7B-n258GDC\_n3A-n7B-n258HDC\_n3A-n7B-n258IDC\_n3A-n7B-n258JDC\_n3A-n7B-n258KDC\_n3A-n7B-n258LDC\_n3A-n7B-n258M | DC\_n3A-n258ADC\_n3A-n258GDC\_n3A-n258HDC\_n3A-n258IDC\_n7A-n258ADC\_n7A-n258GDC\_n7A-n258HDC\_n7A-n258I |
| DC\_n3A-n18A-n257ADC\_n3A-n18A-n257GDC\_n3A-n18A-n257HDC\_n3A-n18A-n257I | DC\_n3A-n18ADC\_n3A-n257ADC\_n3A-n257GDC\_n3A-n257HDC\_n3A-n257IDC\_n18A-n257ADC\_n18A-n257GDC\_n18A-n257HDC\_n18A-n257I |
| DC\_n3A-n28A-n257A1DC\_n3A-n28A-n257G1DC\_n3A-n28A-n257H1DC\_n3A-n28A-n257I1 | DC\_n3A-n28ADC\_n3A-n257ADC\_n3A-n257GDC\_n3A-n257HDC\_n3A-n257IDC\_n28A-n257ADC\_n28A-n257GDC\_n28A-n257HDC\_n28A-n257I |
| DC\_n3A-n41A-n257ADC\_n3A-n41A-n257GDC\_n3A-n41A-n257HDC\_n3A-n41A-n257I | DC\_n3A-n41ADC\_n3A-n257ADC\_n3A-n257GDC\_n3A-n257HDC\_n3A-n257IDC\_n41A-n257ADC\_n41A-n257GDC\_n41A-n257HDC\_n41A-n257I |
| DC\_n3A-n77A-n257A1DC\_n3A-n77A-n257G1DC\_n3A-n77A-n257H1DC\_n3A-n77A-n257I1 | DC\_n3A-n77ADC\_n3A-n257ADC\_n3A-n257GDC\_n3A-n257HDC\_n3A-n257IDC\_n77A-n257ADC\_n77A-n257GDC\_n77A-n257HDC\_n77A-n257I |
| DC\_n3A-n77(2A)-n257A1DC\_n3A-n77(2A)-n257G1DC\_n3A-n77(2A)-n257H1DC\_n3A-n77(2A)-n257I1 | DC\_n3A-n77ADC\_n3A-n257ADC\_n3A-n257GDC\_n3A-n257HDC\_n3A-n257IDC\_n77A-n257ADC\_n77A-n257GDC\_n77A-n257HDC\_n77A-n257I |
| DC\_n3A-n78A-n257A1DC\_n3A-n78A-n257G1DC\_n3A-n78A-n257H1DC\_n3A-n78A-n257I1 | DC\_n3A-n78ADC\_n3A-n257ADC\_n3A-n257GDC\_n3A-n257HDC\_n3A-n257IDC\_n78A-n257ADC\_n78A-n257GDC\_n78A-n257HDC\_n78A-n257I |
| DC\_n3A-n78A-n258ADC\_n3A-n78A-n258BDC\_n3A-n78A-n258CDC\_n3A-n78A-n258DDC\_n3A-n78A-n258EDC\_n3A-n78A-n258FDC\_n3A-n78A-n258GDC\_n3A-n78A-n258HDC\_n3A-n78A-n258IDC\_n3A-n78A-n258JDC\_n3A-n78A-n258KDC\_n3A-n78A-n258LDC\_n3A-n78A-n258M | DC\_n3A-n258ADC\_n3A-n258GDC\_n3A-n258HDC\_n3A-n258IDC\_n78A-n258ADC\_n78A-n258GDC\_n78A-n258HDC\_n78A-n258IDC\_n3A-n78A |
| DC\_n3A-n79A-n257ADC\_n3A-n79A-n257GDC\_n3A-n79A-n257HDC\_n3A-n79A-n257I | DC\_n3A-n79ADC\_n3A-n257ADC\_n3A-n257GDC\_n3A-n257HDC\_n3A-n257IDC\_n79A-n257ADC\_n79A-n257GDC\_n79A-n257HDC\_n79A-n257I |
| DC\_n5A-n30A-n260ADC\_n5A-n30A-n260GDC\_n5A-n30A-n260HDC\_n5A-n30A-n260IDC\_n5A-n30A-n260JDC\_n5A-n30A-n260KDC\_n5A-n30A-n260LDC\_n5A-n30A-n260M | DC\_n5A-n30ADC\_n5A-n260ADC\_n30A-n260ADC\_n5A-n260GDC\_n30A-n260GDC\_n5A-n260HDC\_n30A-n260HDC\_n5A-n260IDC\_n30A-n260IDC\_n5A-n260JDC\_n30A-n260JDC\_n5A-n260KDC\_n30A-n260KDC\_n5A-n260LDC\_n30A-n260LDC\_n5A-n260MDC\_n30A-n260M |
| DC\_n5A-n66A-n260ADC\_n5A-n66A-n260GDC\_n5A-n66A-n260HDC\_n5A-n66A-n260IDC\_n5A-n66A-n260JDC\_n5A-n66A-n260KDC\_n5A-n66A-n260LDC\_n5A-n66A-n260M | DC\_n5A-n66ADC\_n5A-n260ADC\_n5A-n260GDC\_n5A-n260HDC\_n5A-n260IDC\_n5A-n260JDC\_n5A-n260KDC\_n5A-n260LDC\_n5A-n260MDC\_n66A-n260ADC\_n66A-n260GDC\_n66A-n260HDC\_n66A-n260IDC\_n66A-n260JDC\_n66A-n260KDC\_n66A-n260LDC\_n66A-n260M |
| DC\_n5A-n77A-n260ADC\_n5A-n77A-n260IDC\_n5A-n77A-n260JDC\_n5A-n77A-n260KDC\_n5A-n77A-n260LDC\_n5A-n77A-n260M | DC\_n5A-n260ADC\_n5A-n260GDC\_n5A-n260HDC\_n5A-n260IDC\_n77A-n260ADC\_n77A-n260GDC\_n77A-n260HDC\_n77A-n260I |
| DC\_n5A-n77A-n261ADC\_n5A-n77A-n261IDC\_n5A-n77A-n261JDC\_n5A-n77A-n261KDC\_n5A-n77A-n261LDC\_n5A-n77A-n261M | DC\_n5A-n261ADC\_n5A-n261GDC\_n5A-n261HDC\_n5A-n261IDC\_n77A-n261ADC\_n77A-n261GDC\_n77A-n261HDC\_n77A-n261I |
| DC\_n7A-n78A-n258ADC\_n7A-n78A-n258BDC\_n7A-n78A-n258CDC\_n7A-n78A-n258DDC\_n7A-n78A-n258EDC\_n7A-n78A-n258FDC\_n7A-n78A-n258GDC\_n7A-n78A-n258HDC\_n7A-n78A-n258IDC\_n7A-n78A-n258JDC\_n7A-n78A-n258KDC\_n7A-n78A-n258LDC\_n7A-n78A-n258M | DC\_n7A-n258ADC\_n7A-n258GDC\_n7A-n258HDC\_n7A-n258IDC\_n78A-n258ADC\_n78A-n258GDC\_n78A-n258HDC\_n78A-n258IDC\_n7A-n78A |
| DC\_n7B-n78A-n258ADC\_n7B-n78A-n258BDC\_n7B-n78A-n258CDC\_n7B-n78A-n258DDC\_n7B-n78A-n258EDC\_n7B-n78A-n258FDC\_n7B-n78A-n258GDC\_n7B-n78A-n258HDC\_n7B-n78A-n258IDC\_n7B-n78A-n258JDC\_n7B-n78A-n258KDC\_n7B-n78A-n258LDC\_n7B-n78A-n258M | DC\_n7A-n258ADC\_n7A-n258GDC\_n7A-n258HDC\_n7A-n258IDC\_n78A-n258ADC\_n78A-n258GDC\_n78A-n258HDC\_n78A-n258IDC\_n7A-n78A |
| DC\_n18A-n28A-n257ADC\_n18A-n28A-n257GDC\_n18A-n28A-n257HDC\_n18A-n28A-n257I | DC\_n18A-n28ADC\_n18A-n257ADC\_n18A-n257GDC\_n18A-n257HDC\_n18A-n257IDC\_n28A-n257ADC\_n28A-n257GDC\_n28A-n257HDC\_n28A-n257I |
| DC\_n18A-n41A-n257ADC\_n18A-n41A-n257GDC\_n18A-n41A-n257HDC\_n18A-n41A-n257I | DC\_n18A-n41ADC\_n18A-n257ADC\_n18A-n257GDC\_n18A-n257HDC\_n18A-n257IDC\_n41A-n257ADC\_n41A-n257GDC\_n41A-n257HDC\_n41A-n257I |
| DC\_n18A-n77A-n257ADC\_n18A-n77A-n257GDC\_n18A-n77A-n257HDC\_n18A-n77A-n257I | DC\_n18A-n77ADC\_n18A-n257ADC\_n18A-n257GDC\_n18A-n257HDC\_n18A-n257IDC\_n77A-n257ADC\_n77A-n257GDC\_n77A-n257HDC\_n77A-n257I |
| DC\_n18A-n77(2A)-n257ADC\_n18A-n77(2A)-n257GDC\_n18A-n77(2A)-n257HDC\_n18A-n77(2A)-n257I | DC\_n18A-n77ADC\_n18A-n257ADC\_n18A-n257GDC\_n18A-n257HDC\_n18A-n257IDC\_n77A-n257ADC\_n77A-n257GDC\_n77A-n257HDC\_n77A-n257I |
| DC\_n18A-n78A-n257ADC\_n18A-n78A-n257GDC\_n18A-n78A-n257HDC\_n18A-n78A-n257I | DC\_n18A-n78ADC\_n18A-n257ADC\_n18A-n257GDC\_n18A-n257HDC\_n18A-n257IDC\_n78A-n257ADC\_n78A-n257GDC\_n78A-n257HDC\_n78A-n257I |
| DC\_n25A-n41A-n260ADC\_n25A-n41A-n260GDC\_n25A-n41A-n260HDC\_n25A-n41A-n260IDC\_n25A-n41A-n260(2A) | DC\_n25A-n260ADC\_n41A-n260A |
| DC\_n28A-n41A-n257ADC\_n28A-n41A-n257GDC\_n28A-n41A-n257HDC\_n28A-n41A-n257I | DC\_n28A-n41ADC\_n28A-n257ADC\_n28A-n257GDC\_n28A-n257HDC\_n28A-n257IDC\_n41A-n257ADC\_n41A-n257GDC\_n41A-n257HDC\_n41A-n257 I |
| DC\_n28A-n77A-n257A1DC\_n28A-n77A-n257G1DC\_n28A-n77A-n257H1DC\_n28A-n77A-n257I1 | DC\_n28A-n77ADC\_n28A-n257ADC\_n28A-n257GDC\_n28A-n257HDC\_n28A-n257IDC\_n77A-n257ADC\_n77A-n257GDC\_n77A-n257HDC\_n77A-n257I |
| DC\_n28A-n77(2A)-n257ADC\_n28A-n77(2A)-n257GDC\_n28A-n77(2A)-n257HDC\_n28A-n77(2A)-n257I | DC\_n28A-n77ADC\_n28A-n257ADC\_n28A-n257GDC\_n28A-n257HDC\_n28A-n257IDC\_n77A-n257ADC\_n77A-n257GDC\_n77A-n257HDC\_n77A-n257I |
| DC\_n28A-n78A-n257A1DC\_n28A-n78A-n257G1DC\_n28A-n78A-n257H1DC\_n28A-n78A-n257I1 | DC\_n28A-n78ADC\_n28A-n257ADC\_n28A-n257GDC\_n28A-n257HDC\_n28A-n257IDC\_n78A-n257ADC\_n78A-n257GDC\_n78A-n257HDC\_n78A-n257I |
| DC\_n28A-n79A-n257A1DC\_n28A-n79A-n257G1DC\_n28A-n79A-n257H1DC\_n28A-n79A-n257I1 | DC\_n28A-n79ADC\_n28A-n257ADC\_n28A-n257GDC\_n28A-n257HDC\_n28A-n257IDC\_n79A-n257ADC\_n79A-n257GDC\_n79A-n257HDC\_n79A-n257I |
| DC\_n30A-n66A-n260ADC\_n30A-n66A-n260GDC\_n30A-n66A-n260HDC\_n30A-n66A-n260IDC\_n30A-n66A-n260JDC\_n30A-n66A-n260KDC\_n30A-n66A-n260LDC\_n30A-n66A-n260M | DC\_n30A-n66ADC\_n30A-n260ADC\_n30A-n260GDC\_n30A-n260HDC\_n30A-n260IDC\_n30A-n260JDC\_n30A-n260KDC\_n30A-n260LDC\_n30A-n260MDC\_n66A-n260ADC\_n66A-n260GDC\_n66A-n260HDC\_n66A-n260IDC\_n66A-n260JDC\_n66A-n260KDC\_n66A-n260LDC\_n66A-n260M |
| DC\_n40A\_n77A-n257ADC\_n40A\_n77A-n257DDC\_n40A\_n77A-n257EDC\_n40A\_n77A-n257FDC\_n40A\_n77A-n257GDC\_n40A\_n77A-n257HDC\_n40A\_n77A-n257IDC\_n40A\_n77A-n257JDC\_n40A\_n77A-n257KDC\_n40A\_n77A-n257LDC\_n40A\_n77A-n257MDC\_n40A\_n77C-n257ADC\_n40A\_n77C-n257DDC\_n40A\_n77C-n257EDC\_n40A\_n77C-n257F | DC\_n40A\_n77ADC\_n40A\_n257ADC\_n40A\_n257DDC\_n40A\_n257EDC\_n40A\_n257FDC\_n40A\_n257GDC\_n40A\_n257HDC\_n40A\_n257IDC\_n40A\_n257JDC\_n40A\_n257KDC\_n40A\_n257LDC\_n40A\_n257MDC\_n77A-n257ADC\_n77A-n257EDC\_n77A-n257FDC\_n77A-n257GDC\_n77A-n257HDC\_n77A-n257IDC\_n77A-n257JDC\_n77A-n257KDC\_n77A-n257LDC\_n77A-n257M |
| DC\_n40A\_n78A-n257ADC\_n40A\_n78A-n257DDC\_n40A\_n78A-n257EDC\_n40A\_n78A-n257FDC\_n40A\_n78A-n257GDC\_n40A\_n78A-n257HDC\_n40A\_n78A-n257IDC\_n40A\_n78A-n257JDC\_n40A\_n78A-n257KDC\_n40A\_n78A-n257LDC\_n40A\_n78A-n257MDC\_n40A\_n78C-n257ADC\_n40A\_n78C-n257DDC\_n40A\_n78C-n257EDC\_n40A\_n78C-n257FDC\_n40A\_n78C-n257GDC\_n40A\_n78C-n257HDC\_n40A\_n78C-n257IDC\_n40A\_n78C-n257JDC\_n40A\_n78C-n257KDC\_n40A\_n78C-n257LDC\_n40A\_n78C-n257M | DC\_n40A\_n78ADC\_n40A\_n257ADC\_n40A\_n257DDC\_n40A\_n257EDC\_n40A\_n257FDC\_n40A\_n257GDC\_n40A\_n257HDC\_n40A\_n257IDC\_n40A\_n257JDC\_n40A\_n257KDC\_n40A\_n257LDC\_n40A\_n257MDC\_n78A-n257ADC\_n78A-n257EDC\_n78A-n257FDC\_n78A-n257GDC\_n78A-n257HDC\_n78A-n257IDC\_n78A-n257JDC\_n78A-n257KDC\_n78A-n257LDC\_n78A-n257M |
| DC\_n41A-n66A-n260A | DC\_n41A-n260ADC\_n66A-n260A |
| DC\_n41A-n66A-n260(2A) | DC\_n41A-n260ADC\_n66A-n260A |
| DC\_n41A-n66A-n260G | DC\_n41A-n260ADC\_n41A-n260GDC\_n66A-n260ADC\_n66A-n260G |
| DC\_n41A-n66A-n260H | DC\_n41A-n260ADC\_n41A-n260GDC\_n41A-n260HDC\_n66A-n260ADC\_n66A-n260GDC\_n66A-n260H |
| DC\_n41A-n66A-n260I | DC\_n41A-n260ADC\_n41A-n260GDC\_n41A-n260HDC\_n41A-n260IDC\_n66A-n260ADC\_n66A-n260GDC\_n66A-n260HDC\_n66A-n260I |
| DC\_n41A-n77A-n257ADC\_n41A-n77A-n257GDC\_n41A-n77A-n257HDC\_n41A-n77A-n257I | DC\_n41A-n77ADC\_n41A-n257ADC\_n41A-n257GDC\_n41A-n257HDC\_n41A-n257IDC\_n77A-n257ADC\_n77A-n257GDC\_n77A-n257HDC\_n77A-n257I |
| DC\_n41A-n77(2A)-n257ADC\_n41A-n77(2A)-n257GDC\_n41A-n77(2A)-n257HDC\_n41A-n77(2A)-n257I | DC\_n41A-n77A DC\_n41A-n257A DC\_n41A-n257GDC\_n41A-n257HDC\_n41A-n257I DC\_n77A-n257A DC\_n77A-n257G DC\_n77A-n257H DC\_n77A-n257I |
| DC\_n41A-n78A-n257ADC\_n41A-n78A-n257GDC\_n41A-n78A-n257HDC\_n41A-n78A-n257I | DC\_n41A-n78ADC\_n41A-n257ADC\_n41A-n257GDC\_n41A-n257HDC\_n41A-n257IDC\_n78A-n257ADC\_n78A-n257GDC\_n78A-n257HDC\_n78A-n257I |
| DC\_n66A-n77A-n260ADC\_n66A-n77A-n260GDC\_n66A-n77A-n260HDC\_n66A-n77A-n260IDC\_n66A-n77A-n260JDC\_n66A-n77A-n260KDC\_n66A-n77A-n260LDC\_n66A-n77A-n260M | DC\_n66A-n260ADC\_n66A-n260GDC\_n66A-n260HDC\_n66A-n260IDC\_n77A-n260ADC\_n77A-n260GDC\_n77A-n260HDC\_n77A-n260I |
| DC\_n66A-n77A-n261ADC\_n66A-n77A-n261IDC\_n66A-n77A-n261JDC\_n66A-n77A-n261KDC\_n66A-n77A-n261LDC\_n66A-n77A-n261M | DC\_n66A-n261ADC\_n66A-n261GDC\_n66A-n261HDC\_n66A-n261IDC\_n77A-n261ADC\_n77A-n261GDC\_n77A-n261HDC\_n77A-n261I |
| DC\_n77A-n79A-n257ADC\_n77A-n79A-n257GDC\_n77A-n79A-n257HDC\_n77A-n79A-n257I | DC\_n77A-n79ADC\_n77A-n257ADC\_n77A-n257GDC\_n77A-n257HDC\_n77A-n257IDC\_n79A-n257ADC\_n79A-n257GDC\_n79A-n257HDC\_n79A-n257I |
| DC\_n77(2A)-n79A-n257ADC\_n77(2A)-n79A-n257GDC\_n77(2A)-n79A-n257HDC\_n77(2A)-n79A-n257I | DC\_n77A-n79ADC\_n77A-n257ADC\_n77A-n257GDC\_n77A-n257HDC\_n77A-n257IDC\_n79A-n257ADC\_n79A-n257GDC\_n79A-n257HDC\_n79A-n257I |
| DC\_n78A-n79A-n257ADC\_n78A-n79A-n257GDC\_n78A-n79A-n257HDC\_n78A-n79A-n257I | DC\_n78A-n257ADC\_n78A-n257GDC\_n78A-n257HDC\_n78A-n257IDC\_n79A-n257ADC\_n79A-n257GDC\_n79A-n257HDC\_n79A-n257I |
| NOTE 1: Applicable for UE supporting inter-band NR DC with mandatory simultaneous Rx/Tx capability. |

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### *<< End of changes >>*