**3GPP TSG- Meeting #**

**, 21 February -**

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| *CR-Form-v12.1* |
| **CHANGE REQUEST** |
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|  |  | **CR** |  | **rev** |  | **Current version:** |  |  |
|  |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* |
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| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network |  | Core Network |  |

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|  |
| ***Title:***  | CR to TS 38.133: intra-frequency measurements without gaps for FR2 NR HST |
|  |  |
| ***Source to WG:*** |  |
| ***Source to TSG:*** |  |
|  |  |
| ***Work item code:*** | NR\_HST\_FR2-Core |  | ***Date:*** | 21 |
|  |  |  |  |  |
| ***Category:*** | **B** |  | ***Release:*** | 7 |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)…Rel-15 (Release 15)Rel-16 (Release 16)Rel-17 (Release 17)Rel-18 (Release 18)* |
|  |  |
| ***Reason for change:*** | Enhancements on intra-frequency measurement without gaps in connected mode for FR2 NR HST were agreed but are not specified in the specification.   |
|  |  |
| ***Summary of change:*** |  The following requirements for FR2 NR HST are defined:* Intra-frequency measurements without gaps in connected mode including PSS/SSS detection, and measurement period

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| ***Consequences if not approved:*** | FR2 NR HST enhancements for intra-frequency measurements without measurement gaps are missing from the specification.  |
|  |  |
| ***Clauses affected:*** | 9.2.5 |
|  |  |
|  | **Y** | **N** |  |  |
| ***Other specs*** |  | **X** |  Other core specifications  | TS/TR ... CR ...  |
| ***affected:*** |  | **X** |  Test specifications | TS/TR ... CR ... |
| ***(show related CRs)*** |  | **X** |  O&M Specifications | TS/TR ... CR ...  |
|  |  |
| ***Other comments:*** |  |
|  |  |
| ***This CR's revision history:*** |  |

<Start of Change 1>

### 9.2.5 Intrafrequency measurements without measurement gaps

#### 9.2.5.1 Intrafrequency cell identification

The UE shall be able to identify a new detectable intra-frequency cell within Tidentify\_intra\_without\_index if the UE is not indicated to report SSB based RRM measurement result with the associated SSB index(*reportQuantityRsIndexes* or *maxNrofRSIndexesToReport* is not configured), or the UE is indicated that the neighbour cell is synchronous with the serving cell (*deriveSSB-IndexFromCell* is enabled). Otherwise UE shall be able to identify a new detectable intra frequency cell within Tidentify\_intra\_with\_index. The UE shall be able to identify a new detectable intra frequency SS block of an already detected cell within Tidentify\_intra\_without\_index. It is assumed that *deriveSSB-IndexFromCell* is always enabled for FR1 TDD and FR2.

Tidentify\_intra\_without\_index = (TPSS/SSS\_sync\_intra + T SSB\_measurement\_period\_intra) ms

Tidentify\_intra\_with\_index = (TPSS/SSS\_sync\_intra + T SSB\_measurement\_period\_intra + TSSB\_time\_index\_intra) ms

Where:

 TPSS/SSS\_sync\_intra: it is the time period used in PSS/SSS detection given in table 9.2.5.1-1, 9.2.5.1-2, 9.2.5.1-4 (deactivated SCell) or 9.2.5.1-5 (deactivated SCell) or 9.2.5.1-9 or 9.2.5.1-11.

 TSSB\_time\_index\_intra: it is the time period used to acquire the index of the SSB being measured given in table 9.2.5.1-3 or 9.2.5.1-6 (deactivated SCell) or 9.2.5.1-10

 T SSB\_measurement\_period\_intra: equal to a measurement period of SSB based measurement given in table 9.2.5.2-1, table 9.2.5.2-2 table 9.2.5.2-3 (deactivated SCell) or 9.2.5.2-4(deactivated SCell) or 9.2.5.2-5 or 9.2.5.2-6

 CSSFintra: it is a carrier specific scaling factor and is determined

 according to CSSFoutside\_gap,i in clause 9.1.5.1 for measurement conducted outside measurement gaps, i.e. when intra-frequency SMTC is fully non overlapping or partially overlapping with measurement gaps, or according to CSSFwithin\_gap,i in clause 9.1.5.2 for measurement conducted within measurement gaps, i.e. when intra-frequency SMTC is fully overlapping with measurement gaps.

 if the high layer in TS 38.331 [2] signalling of *smtc2* is configured, the assumed periodicity of intra-frequency SMTC occasions corresponds to the value of higher layer parameter *smtc2*; Otherwise the assumed periodicity of intra-frequency SMTC occasions corresponds to the value of higher layer parameter *smtc1*.

 Mpss/sss\_sync\_w/o\_gaps : For a UE supporting FR2 power class 1 or 5, Mpss/sss\_sync\_w/o\_gaps =40. For a UE supporting power class 2, Mpss/sss\_sync\_w/o\_gaps =24. For a UE supporting FR2 power class 3, Mpss/sss\_sync\_w/o\_gaps =24. For a UE supporting FR2 power class 4, Mpss/sss\_sync\_w/o\_gaps =24

 Mmeas\_period\_w/o\_gaps : For a UE supporting power class 1 or 5, Mmeas\_period\_w/o\_gaps =40. For a UE supporting FR2 power class 2, Mmeas\_period\_w/o\_gaps =24. For a UE supporting power class 3, Mmeas\_period\_w/o\_gaps =24. For a UE supporting power class 4, Mmeas\_period\_w/o\_gaps =24.

 When intra-frequency SMTC is fully non overlapping with measurement gaps or intra-frequency SMTC is fully overlapping with MGs, Kp=1

 When intra-frequency SMTC is partially overlapping with measurement gaps, Kp = 1/(1- (SMTC period /MGRP)), where SMTC period < MGRP. For calculation of Kp, if the high layer signalling (TS 38.331 [2]) of *smtc2* is configured, for cells indicated in the *pci-List* parameter in *smtc2*, the SMTC periodicity corresponds to the value of higher layer parameter *smtc2*; for the other cells, the SMTC periodicity corresponds to the value of higher layer parameter *smtc1.*

 If the higher layer signaling in TS38.331 [2] signalling of *smtc2* is present and smtc1 is fully overlapping with measurement gaps and smtc2 is partially overlapping with measurement gaps, requirements are not specified for Tidentify\_intra\_without\_index or Tidentify\_intra\_with\_index

 For FR2,

 Klayer1\_measurement=1,

- if all of the reference signals configured for RLM, BFD, CBD or L1-RSRP for beam reporting on any FR2 serving frequency in the same band outside measurement gap are not fully overlapped by intra-frequency SMTC occasions, or

- if all of the reference signal configured for RLM, BFD, CBD or L1-RSRP for beam reporting on any FR2 serving frequency in the same band outside measurement gap and fully-overlapped by intra-frequency SMTC occasions are not overlapped with any of the SSB symbols and the RSSI symbols, and 1 symbol before each consecutive SSB symbols and the RSSI symbols, and 1 symbol after each consecutive SSB symbols and the RSSI symbols, given that *SSB-ToMeasure* and *SS-RSSI-Measurement* are configured, where SSB symbols are indicated by the union set of SSB-ToMeasure from all the configured measurement objects on the same serving carrier which can be merged.and RSSI symbols are indicated by *SS-RSSI-Measurement*;

 Klayer1\_measurement=1.5, otherwise.

 If the above-mentioned reference signal configured for L1-RSRP measurement is aperiodic CSI-RS resource, longer cell identification delay would be expected.

 If MCG DRX is in use, cell identification requirements for intra-frequency measurement in MCG specified in Table 9.2.5.1-1, Table 9.2.5.1-2, Table 9.2.5.1-3, Table 9.2.5.1-4, Table 9.2.5.1-5 and Table 9.2.5.1-6 shall depend on the MCG DRX cycle. If SCG DRX is in use, cell identification requirements for intra-frequency measurement in SCG specified in Table 9.2.5.1-1, Table 9.2.5.1-2, Table 9.2.5.1-3, Table 9.2.5.1-4, Table 9.2.5.1-5 and Table 9.2.5.1-6 shall depend on the SCG DRX cycle. Otherwise, the requirements for when DRX is not in use shall apply.

Table 9.2.5.1-1: Time period for PSS/SSS detection, (Frequency range FR1)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max( 600ms, ceil( 5 x Kp) x SMTC period )Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max( 600ms, ceil(M2 Note 2x 5 x Kp) x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | ceil(5 x Kp) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1.NOTE 3: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *[intraRAT-MeasurementEnhancement-r16]* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.NOTE 4: When [highSpeedMeasFlag for CA] is configured, the requirements apply to measurements of secondary component carrier with active SCell. |

Table 9.2.5.1-2: Time period for PSS/SSS detection, (Frequency range FR2)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max(600ms, ceil(Mpss/sss\_sync\_w/o\_gaps x Kp x Klayer1\_measurement)x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max(600ms, ceil(1.5 x Mpss/sss\_sync\_w/o\_gaps x Kp x Klayer1\_measurement)x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | ceil(Mpss/sss\_sync\_w/o\_gaps x Kp x Klayer1\_measurement) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identified |

**Table 9.2.5.1-3: Time period for time index detection (FR1)**

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | max(120ms, ceil( 3 x Kp )x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max(120ms, ceil (M2 Note 2 x 3 x Kp) x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | Ceil(3 x Kp) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms;,otherwise M2=1NOTE 3: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *[intraRAT-MeasurementEnhancement-r16]* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.NOTE 4: When [*highSpeedMeasFlag for CA*] is configured, the requirements apply to measurements of secondary component carrier with active SCell. |

**Table 9.2.5.1-4: Time period for PSS/SSS detection, deactivated SCell (FR1)**

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | Ceil(5 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(5 x Kp) x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(5 x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |

**Table 9.2.5.1-5: Time period for PSS/SSS detection, deactivated SCell (FR2)**

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |

**Table 9.2.5.1-6: Time period for time index detection, deactivated SCell (FR1)**

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | Ceil(3 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(3 x Kp) x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(3 x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |

Table 9.2.5.1-7: Void

Table 9.2.5.1-8: Void

**Table 9.2.5.1-9: Time period for PSS/SSS detection, deactivated SCell (FR1), when *[highSpeedMeasFlag for CA]* is configured**

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | Ceil(5 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms |  Ceil(5 x Kp) x max(measCycleSCell, M2 Note 1xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(5 x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1 |

**Table 9.2.5.1-10: Time period for time index detection, deactivated SCell (FR1)，when *[highSpeedMeasFlag for CA]* is configured**

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | Ceil(3 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms |  Ceil(3 x Kp) x max(measCycleSCell, M2 Note 1xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(3 x Kp)x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1 |

Table 9.2.5.1-11: Time period for PSS/SSS detection when [*highSpeedMeasFlagFR2-r17*] is configured, (Frequency range FR2)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max(600ms, ceil(M1Note 2 x Kp x Klayer1\_measurement)x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 80ms | max(600ms, ceil(M1Note 2 x M2Note 3 x Kp x Klayer1\_measurement)x max(SMTC period,DRX cycle)) x CSSFintra |
| 80ms< DRX cycle≤ 320ms | ceil(M2Note 3 x Mpss/sss\_sync\_w/o\_gaps x Kp x Klayer1\_measurement)x max(SMTC period,DRX cycle) x CSSFintra |
| DRX cycle>320ms | ceil(Mpss/sss\_sync\_w/o\_gaps x Kp x Klayer1\_measurement) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: For UE supporting power class 6, M1= 6 if [*highSpeedMeasFlagFR2-r17* = set1] or M1= 18 if [*highSpeedMeasFlagFR2-r17* = set2]NOTE 3: M2 = 1 if SMTC periodicity ≤ 40 ms; otherwise M2 = 1.5 |

#### 9.2.5.2 Measurement period

The measurement period for intra-frequency measurements without gaps is as shown in table 9.2.5.2-1, 9.2.5.2-2, 9.2.5.2-3 (deactivated SCell) or 9.2.5.2-4(deactivated SCell). When *highSpeedMeasFlag-r16* is configured, T SSB\_measurement\_period\_intra is specified in Table 9.2.5.2-5.

If the higher layer signaling in TS38.331 [2] signalling of *smtc2* is present and smtc1 is fully overlapping with measurement gaps and smtc2 is partially overlapping with measurement gaps, requirements are not specified for TSSB\_measurement\_period\_intra

If MCG DRX is in use, measurement period requirements for intra-frequency measurement in MCG specified in Table 9.2.5.2-1, Table 9.2.5.2-2, Table 9.2.5.2-3 and Table 9.2.5.2-4 shall depend on the MCG DRX cycle. If SCG DRX is in use, measurement period requirements for intra-frequency measurement in SCG specified in Table 9.2.5.2-1, Table 9.2.5.2-2, Table 9.2.5.2-3 and Table 9.2.5.2-4 shall depend on the SCG DRX cycle. Otherwise, the requirements for when DRX is not in use shall apply.

For FR2, a longer measurement period is allowed, if aperiodic CSI-RS resource is measured for L1-RSRP measurement on any FR2 serving frequency in the same band, and the CSI-RS resource is outside measurement gap and overlapped with any of the SSB symbols and the RSSI symbols, and 1 symbol before each consecutive SSB symbols and the RSSI symbols, and 1 symbol after each consecutive SSB symbols and the RSSI symbols. If *SSB-ToMeasure* or *SS-RSSI-Measurement* is configured, the SSB symbols are indicated by the union set of *SSB-ToMeasure* from all the configured measurement objects on the same band which can be merged and the RSSI symbols are indicated by *SS-RSSI-Measurement*.

Table 9.2.5.2-1: Measurement period for intra-frequency measurements without gaps (FR1)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | max(200ms, ceil( 5 x Kp) x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max(200ms, ceil(1.5x 5 x Kp) x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | ceil( 5 x Kp ) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identified |

Table 9.2.5.2-2: Measurement period for intra-frequency measurements without gaps (FR2)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | max(400ms, ceil(Mmeas\_period\_w/o\_gaps x Kp x Klayer1\_measurement) x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max(400ms, ceil(1.5x Mmeas\_period\_w/o\_gaps x Kp x Klayer1\_measurement) x max(SMTC period,DRX cycle)) x CSSFintra  |
| DRX cycle>320ms | ceil(Mmeas\_period\_w/o\_gaps xKp x Klayer1\_measurement ) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identified |

Table 9.2.5.2-3: Measurement period for intra-frequency measurements without gaps (deactivated SCell) (FR1)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | Ceil(5 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(5 x Kp) x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(5 x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |

**Table 9.2.5.2-4: Measurement period for intra-frequency measurements without gaps (deactivated SCell) (FR2)**

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | Ceil(Mmeas\_period\_w/o\_gaps x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(Mmeas\_period\_w/o\_gaps x Kp) x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(Mmeas\_period\_w/o\_gaps x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |

Table 9.2.5.2-5: T SSB\_measurement\_period\_intra When *highSpeedMeasFlag-r16* is configured (Frequency range FR1

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX Note 2 | max(200ms, ceil( 5 x Kp) x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 160ms | max(200ms, ceil(5 x M2 Note 2 x Kp) x max(SMTC period,DRX cycle)) x CSSFintra |
| 160ms < DRX cycle≤ 320ms | ceil(4 x M2 Note 2 x Kp) x DRX cycle x CSSFintra |
| DRX cycle>320ms | ceil( Y Note 3 x Kp ) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: M2 = 1.5 if SMTC period > 40 ms, otherwise M2=1NOTE 3: Y=3 when SMTC period <= 40ms, Y=5 when SMTC period > 40msNOTE 4: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *[intraRAT-MeasurementEnhancement-r16]* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.NOTE 5: When [highSpeedMeasFlag for CA] is configured, the requirements apply to measurements of secondary component carrier with active SCell. |

Table 9.2.5.2-6: Measurement period for intra-frequency measurements without gaps (deactivated SCell) (FR1), when [*highSpeedMeasFlag for CA*] is configured

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | ceil( 5 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 160ms | ceil(5 x Kp) x max(measCycleSCell, M2 Note 1 x DRX cycle) x CSSFintra |
| 160ms < DRX cycle≤ 320ms | ceil(4 x Kp) x max(measCycleSCell, M2 Note 1 x DRX cycle) |
| DRX cycle>320ms | ceil( Y Note 2 x Kp ) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: M2 = 1.5 if SMTC periodicity > 40 ms, otherwise M2=1NOTE 2: Y=3 when SMTC <= 40ms, Y=5 when SMTC > 40ms |

Table 9.2.5.2-7: Measurement period for intra-frequency measurements without gaps when [*highSpeedMeasFlagFR2-r17*] is configured (FR2)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | max(400ms, ceil(M1Note 2 x Kp x Klayer1\_measurement) x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 80ms | max(400ms, ceil(M1Note 2 x M2Note 3 x Kp x Klayer1\_measurement) x max(SMTC period,DRX cycle)) x CSSFintra |
| 80ms< DRX cycle≤ 320ms | ceil(M2Note 3 x Mmeas\_period\_w/o\_gaps x Kp x Klayer1\_measurement) x max(SMTC period,DRX cycle) x CSSFintra  |
| DRX cycle>320ms | ceil(Mmeas\_period\_w/o\_gaps xKp x Klayer1\_measurement ) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: For UE supporting power class 6, M1= 6 if [*highSpeedMeasFlagFR2-r17* = set1] or M1= 18 if [*highSpeedMeasFlagFR2-r17* = set2]NOTE 3: M2 = 1 if SMTC periodicity ≤ 40 ms; otherwise M2 = 1.5 |

<End of Change 1>