**3GPP TSG-RAN WG4 Meeting # 102-e *Rev 1 of R4-2203994***

**Electronic meeting, February 21 – March 3, 2022**

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| --- |
| *CR-Form-v12.1* |
| **CHANGE REQUEST** |
|  |
|  | **38.101-3** | **CR** | **<CR#>** | **rev** | **-** | **Current version:** | **17.4.0** |  |
|  |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* |
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| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network | **X** | Core Network |  |

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|  |
| ***Title:***  |  |
|  |  |
| ***Source to WG:*** | ZTE Corporation |
| ***Sou01-3rce to TSG:*** | R4 |
|  |  |
| ***Work item code:*** | DC\_R17\_xBLTE\_2BNR\_yDL2UL-Core |  | ***Date:*** | 2022-02-10 |
|  |  |  |  |  |
| ***Category:*** | **F** |  | ***Release:*** | Rel-17 |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)…Rel-15 (Release 15)Rel-16 (Release 16)Rel-17 (Release 17)Rel-18 (Release 18)* |
|  |  |
| ***Reason for change:*** | The EN-DC configurations of DC\_5A-7A-7A\_n78C-n257A/D/E/F/G/H/I/J/K/L/M are in wrong groups and should be corrected. |
|  |  |
| ***Summary of change:*** | Regroup and correct the EN-DC configurations for DC\_5A-7A-7A\_n78C-n257A/D/E/F/G/H/I/J/K/L/M. |
|  |  |
| ***Consequences if not approved:*** | The EN-DC configurations of DC\_5A-7A-7A\_n78C-n257A/D/E/F/G/H/I/J/K/L/M are incorrect. |
|  |  |
| ***Clauses affected:*** | 5.5B.6.3 |
|  |  |
|  | **Y** | **N** |  |  |
| ***Other specs*** |  | **X** |  Other core specifications  | TS/TR ... CR ...  |
| ***affected:*** | **X** |  |  Test specifications | TS/TR ... CR ... 38.521-3 |
| ***(show related CRs)*** |  | **X** |  O&M Specifications | TS/TR ... CR ...  |
|  |  |
| ***Other comments:*** |  |
|  |  |
| ***This CR's revision history:*** |  |

### *<< start of changes >>*

### *<< Unchanged sections omitted >>*

#### 5.5B.6.3 Inter-band EN-DC configurations including FR1 and FR2 (four bands)

Table 5.5B.6.3-1: Inter-band EN-DC configurations including FR1 and FR2 (four bands)

| EN-DC configuration | Uplink EN-DC configuration (NOTE 1) |
| --- | --- |
| DC\_1A-3A\_n28A-n257A2DC\_1A-3A\_n28A-n257G2DC\_1A-3A\_n28A-n257H2DC\_1A-3A\_n28A-n257I2 | DC\_1A\_n28ADC\_1A\_n257ADC\_1A\_n257GDC\_1A\_n257HDC\_1A\_n257IDC\_3A\_n28ADC\_3A\_n257ADC\_3A\_n257GDC\_3A\_n257HDC\_3A\_n257I |
| DC\_1A-3A\_n77A-n257A2 | DC\_1A\_n77ADC\_3A\_n77ADC\_1A\_n257ADC\_3A\_n257A |
| DC\_1A-3A\_n77A-n257D2 | DC\_1A\_n77ADC\_3A\_n77ADC\_1A\_n257ADC\_1A\_n257DDC\_3A\_n257ADC\_3A\_n257D |
| DC\_1A-3A\_n77A-n257G2 | DC\_1A\_n77ADC\_3A\_n77ADC\_1A\_n257ADC\_1A\_n257GDC\_3A\_n257ADC\_3A\_n257GDC\_1A\_n77A-n257ADC\_1A\_n77A-n257GDC\_3A\_n77A-n257ADC\_3A\_n77A-n257G |
| DC\_1A-3A\_n77A-n257H2 | DC\_1A\_n77ADC\_3A\_n77ADC\_1A\_n257ADC\_1A\_n257GDC\_1A\_n257HDC\_3A\_n257ADC\_3A\_n257GDC\_3A\_n257HDC\_1A\_n77A-n257ADC\_1A\_n77A-n257GDC\_1A\_n77A-n257HDC\_3A\_n77A-n257ADC\_3A\_n77A-n257GDC\_3A\_n77A-n257H |
| DC\_1A-3A\_n77A-n257I2 | DC\_1A\_n77ADC\_3A\_n77ADC\_1A\_n257ADC\_1A\_n257GDC\_1A\_n257HDC\_1A\_n257IDC\_3A\_n257ADC\_3A\_n257GDC\_3A\_n257HDC\_3A\_n257IDC\_1A\_n77A-n257ADC\_1A\_n77A-n257GDC\_1A\_n77A-n257HDC\_1A\_n77A-n257IDC\_3A\_n77A-n257ADC\_3A\_n77A-n257GDC\_3A\_n77A-n257HDC\_3A\_n77A-n257I |
| DC\_1A-3A\_n78A-n257A2DC\_1A-3A\_n78A-n257D2DC\_1A-3A\_n78A-n257E2DC\_1A-3A\_n78A-n257F2DC\_1A-3A\_n78A-n257G2DC\_1A-3A\_n78A-n257H2DC\_1A-3A\_n78A-n257I2DC\_1A-3A\_n78A-n257J2DC\_1A-3A\_n78A-n257K2DC\_1A-3A\_n78A-n257L2DC\_1A-3A\_n78A-n257M2 | DC\_1A\_n78ADC\_1A\_n257ADC\_1A\_n257DDC\_1A\_n257GDC\_1A\_n257HDC\_1A\_n257IDC\_3A\_n78ADC\_3A\_n257ADC\_3A\_n257DDC\_3A\_n257GDC\_3A\_n257HDC\_3A\_n257IDC\_1A\_n78A-n257ADC\_1A\_n78A-n257GDC\_1A\_n78A-n257HDC\_1A\_n78A-n257IDC\_3A\_n78A-n257ADC\_3A\_n78A-n257GDC\_3A\_n78A-n257HDC\_3A\_n78A-n257I |
| DC\_1A-3A\_n78C-n257ADC\_1A-3A\_n78C-n257DDC\_1A-3A\_n78C-n257EDC\_1A-3A\_n78C-n257FDC\_1A-3A\_n78C-n257GDC\_1A-3A\_n78C-n257HDC\_1A-3A\_n78C-n257IDC\_1A-3A\_n78C-n257JDC\_1A-3A\_n78C-n257KDC\_1A-3A\_n78C-n257LDC\_1A-3A\_n78C-n257M | DC\_1A\_n78A-n257ADC\_1A\_n78A-n257GDC\_1A\_n78A-n257HDC\_1A\_n78A-n257IDC\_3A\_n78A-n257ADC\_3A\_n78A-n257GDC\_3A\_n78A-n257HDC\_3A\_n78A-n257I |
| DC\_1A-3A\_n78A-n258ADC\_1A-3A\_n78A-n258DDC\_1A-3A\_n78A-n258EDC\_1A-3A\_n78A-n258FDC\_1A-3A\_n78A-n258GDC\_1A-3A\_n78A-n258HDC\_1A-3A\_n78A-n258IDC\_1A-3A\_n78A-n258JDC\_1A-3A\_n78A-n258KDC\_1A-3A\_n78A-n258LDC\_1A-3A\_n78A-n258M | DC\_1A\_n78ADC\_1A\_n258ADC\_1A\_n258DDC\_1A\_n258EDC\_1A\_n258FDC\_1A\_n258GDC\_1A\_n258HDC\_1A\_n258IDC\_3A\_n78ADC\_3A\_n258ADC\_3A\_n258DDC\_3A\_n258EDC\_3A\_n258FDC\_3A\_n258GDC\_3A\_n258HDC\_3A\_n258I |
| DC\_1A-3A\_n79A-n257A2DC\_1A-3A\_n79A-n257G2DC\_1A-3A\_n79A-n257H2DC\_1A-3A\_n79A-n257I2 | DC\_1A\_n79ADC\_1A\_n257ADC\_1A\_n257GDC\_1A\_n257HDC\_1A\_n257IDC\_3A\_n79ADC\_3A\_n257ADC\_3A\_n257GDC\_3A\_n257HDC\_3A\_n257I |
| DC\_1A-3A\_n79A-n257A2DC\_1A-3A\_n79A-n257G2DC\_1A-3A\_n79A-n257H2DC\_1A-3A\_n79A-n257I2 | DC\_1A\_n79A-n257ADC\_1A\_n79A-n257GDC\_1A\_n79A-n257HDC\_1A\_n79A-n257IDC\_3A\_n79A-n257ADC\_3A\_n79A-n257GDC\_3A\_n79A-n257HDC\_3A\_n79A-n257I |
| DC\_1A-5A\_n78A-n257ADC\_1A-5A\_n78A-n257DDC\_1A-5A\_n78A-n257EDC\_1A-5A\_n78A-n257FDC\_1A-5A\_n78A-n257GDC\_1A-5A\_n78A-n257HDC\_1A-5A\_n78A-n257IDC\_1A-5A\_n78A-n257JDC\_1A-5A\_n78A-n257KDC\_1A-5A\_n78A-n257LDC\_1A-5A\_n78A-n257M | DC\_1A\_n78ADC\_1A\_n257ADC\_5A\_n78ADC\_5A\_n257ADC\_1A\_n78A-n257ADC\_1A\_n78A-n257GDC\_1A\_n78A-n257HDC\_1A\_n78A-n257IDC\_5A\_n78A-n257ADC\_5A\_n78A-n257GDC\_5A\_n78A-n257HDC\_5A\_n78A-n257I |
| DC\_1A-5A\_n78C-n257ADC\_1A-5A\_n78C-n257DDC\_1A-5A\_n78C-n257EDC\_1A-5A\_n78C-n257FDC\_1A-5A\_n78C-n257GDC\_1A-5A\_n78C-n257HDC\_1A-5A\_n78C-n257IDC\_1A-5A\_n78C-n257JDC\_1A-5A\_n78C-n257KDC\_1A-5A\_n78C-n257LDC\_1A-5A\_n78C-n257M | DC\_1A\_n78A-n257ADC\_1A\_n78A-n257GDC\_1A\_n78A-n257HDC\_1A\_n78A-n257IDC\_5A\_n78A-n257ADC\_5A\_n78A-n257GDC\_5A\_n78A-n257HDC\_5A\_n78A-n257I |
| DC\_1A-7A\_n78A-n257ADC\_1A-7A\_n78A-n257DDC\_1A-7A\_n78A-n257EDC\_1A-7A\_n78A-n257FDC\_1A-7A\_n78A-n257GDC\_1A-7A\_n78A-n257HDC\_1A-7A\_n78A-n257IDC\_1A-7A\_n78A-n257JDC\_1A-7A\_n78A-n257KDC\_1A-7A\_n78A-n257LDC\_1A-7A\_n78A-n257M | DC\_1A\_n78ADC\_1A\_n257ADC\_7A\_n78ADC\_7A\_n257ADC\_1A\_n78A-n257ADC\_1A\_n78A-n257GDC\_1A\_n78A-n257HDC\_1A\_n78A-n257IDC\_7A\_n78A-n257ADC\_7A\_n78A-n257GDC\_7A\_n78A-n257HDC\_7A\_n78A-n257I |
| DC\_1A-7A\_n78C-n257ADC\_1A-7A\_n78C-n257DDC\_1A-7A\_n78C-n257EDC\_1A-7A\_n78C-n257FDC\_1A-7A\_n78C-n257GDC\_1A-7A\_n78C-n257HDC\_1A-7A\_n78C-n257IDC\_1A-7A\_n78C-n257JDC\_1A-7A\_n78C-n257KDC\_1A-7A\_n78C-n257LDC\_1A-7A\_n78C-n257M | DC\_1A\_n78A-n257ADC\_1A\_n78A-n257GDC\_1A\_n78A-n257HDC\_1A\_n78A-n257IDC\_7A\_n78A-n257ADC\_7A\_n78A-n257GDC\_7A\_n78A-n257HDC\_7A\_n78A-n257I |
| DC\_1A-7A-7A\_n78A-n257ADC\_1A-7A-7A\_n78A-n257DDC\_1A-7A-7A\_n78A-n257EDC\_1A-7A-7A\_n78A-n257FDC\_1A-7A-7A\_n78A-n257GDC\_1A-7A-7A\_n78A-n257HDC\_1A-7A-7A\_n78A-n257IDC\_1A-7A-7A\_n78A-n257JDC\_1A-7A-7A\_n78A-n257KDC\_1A-7A-7A\_n78A-n257LDC\_1A-7A-7A\_n78A-n257M | DC\_1A\_n78ADC\_1A\_n257ADC\_7A\_n78ADC\_7A\_n257ADC\_1A\_n78A-n257ADC\_1A\_n78A-n257GDC\_1A\_n78A-n257HDC\_1A\_n78A-n257IDC\_7A\_n78A-n257ADC\_7A\_n78A-n257GDC\_7A\_n78A-n257HDC\_7A\_n78A-n257I |
| DC\_1A-7A-7A\_n78C-n257ADC\_1A-7A-7A\_n78C-n257DDC\_1A-7A-7A\_n78C-n257EDC\_1A-7A-7A\_n78C-n257FDC\_1A-7A-7A\_n78C-n257GDC\_1A-7A-7A\_n78C-n257HDC\_1A-7A-7A\_n78C-n257IDC\_1A-7A-7A\_n78C-n257JDC\_1A-7A-7A\_n78C-n257KDC\_1A-7A-7A\_n78C-n257LDC\_1A-7A-7A\_n78C-n257M | DC\_1A\_n78A-n257ADC\_1A\_n78A-n257GDC\_1A\_n78A-n257HDC\_1A\_n78A-n257IDC\_7A\_n78A-n257ADC\_7A\_n78A-n257GDC\_7A\_n78A-n257HDC\_7A\_n78A-n257I |
| DC\_1A-7A\_n78A-n258ADC\_1A-7A\_n78A-n258DDC\_1A-7A\_n78A-n258EDC\_1A-7A\_n78A-n258FDC\_1A-7A\_n78A-n258GDC\_1A-7A\_n78A-n258HDC\_1A-7A\_n78A-n258IDC\_1A-7A\_n78A-n258JDC\_1A-7A\_n78A-n258KDC\_1A-7A\_n78A-n258LDC\_1A-7A\_n78A-n258M | DC\_1A\_n78ADC\_1A\_n258ADC\_1A\_n258DDC\_1A\_n258EDC\_1A\_n258FDC\_1A\_n258GDC\_1A\_n258HDC\_1A\_n258IDC\_7A\_n78ADC\_7A\_n258ADC\_7A\_n258DDC\_7A\_n258EDC\_7A\_n258FDC\_7A\_n258GDC\_7A\_n258HDC\_7A\_n258I |
| DC\_1A-8A\_n40A-n258ADC\_1A-8A\_n40A-n258DDC\_1A-8A\_n40A-n258EDC\_1A-8A\_n40A-n258FDC\_1A-8A\_n40A-n258GDC\_1A-8A\_n40A-n258HDC\_1A-8A\_n40A-n258IDC\_1A-8A\_n40A-n258JDC\_1A-8A\_n40A-n258KDC\_1A-8A\_n40A-n258LDC\_1A-8A\_n40A-n258M | DC\_1A\_n40ADC\_1A\_n258ADC\_8A\_n40ADC\_8A\_n258A |
| DC\_1A-8A\_n77A-n257A2DC\_1A-8A\_n77A-n257D2DC\_1A-8A\_n77A-n257G2DC\_1A-8A\_n77A-n257H2DC\_1A-8A\_n77A-n257I2 | DC\_1A\_n77ADC\_1A\_n257ADC\_1A\_n257DDC\_1A\_n257GDC\_1A\_n257HDC\_1A\_n257IDC\_8A\_n77ADC\_8A\_n257ADC\_8A\_n257DDC\_8A\_n257GDC\_8A\_n257HDC\_8A\_n257I |
| DC\_1A-8A\_n77(2A)-n257A2DC\_1A-8A\_n77(2A)-n257D2DC\_1A-8A\_n77(2A)-n257G2DC\_1A-8A\_n77(2A)-n257H2DC\_1A-8A\_n77(2A)-n257I2 | DC\_1A\_n77ADC\_1A\_n257ADC\_1A\_n257DDC\_1A\_n257GDC\_1A\_n257HDC\_1A\_n257IDC\_8A\_n77ADC\_8A\_n257A DC\_8A\_n257DDC\_8A\_n257GDC\_8A\_n257HDC\_8A\_n257I |
| DC\_1A-8A\_n78A-n257A2DC\_1A-8A\_n78A-n257D2DC\_1A-8A\_n78A-n257E2DC\_1A-8A\_n78A-n257F2DC\_1A-8A\_n78A-n257G2DC\_1A-8A\_n78A-n257H2DC\_1A-8A\_n78A-n257I2DC\_1A-8A\_n78A-n257J2DC\_1A-8A\_n78A-n257K2DC\_1A-8A\_n78A-n257L2DC\_1A-8A\_n78A-n257M2 | DC\_1A\_n78ADC\_8A\_n78ADC\_1A\_n257ADC\_8A\_n257A |
| DC\_1A-8A\_n78A-n258ADC\_1A-8A\_n78A-n258DDC\_1A-8A\_n78A-n258EDC\_1A-8A\_n78A-n258FDC\_1A-8A\_n78A-n258GDC\_1A-8A\_n78A-n258HDC\_1A-8A\_n78A-n258IDC\_1A-8A\_n78A-n258JDC\_1A-8A\_n78A-n258KDC\_1A-8A\_n78A-n258LDC\_1A-8A\_n78A-n258M | DC\_1A\_n78ADC\_1A\_n258ADC\_8A\_n78ADC\_8A\_n258A |
| DC\_1A-11A\_n77A-n257A2DC\_1A-11A\_n77A-n257D2DC\_1A-11A\_n77A-n257G2DC\_1A-11A\_n77A-n257H2DC\_1A-11A\_n77A-n257I2 | DC\_1A\_n77ADC\_1A\_n257ADC\_1A\_n257DDC\_1A\_n257GDC\_1A\_n257HDC\_1A\_n257IDC\_11A\_n77ADC\_11A\_n257ADC\_11A\_n257DDC\_11A\_n257GDC\_11A\_n257HDC\_11A\_n257I |
| DC\_1A-11A\_n77(2A)-n257A2DC\_1A-11A\_n77(2A)-n257D2DC\_1A-11A\_n77(2A)-n257G2DC\_1A-11A\_n77(2A)-n257H2DC\_1A-11A\_n77(2A)-n257I2 | DC\_1A\_n77ADC\_1A\_n257ADC\_1A\_n257DDC\_1A\_n257GDC\_1A\_n257HDC\_1A\_n257IDC\_11A\_n77ADC\_11A\_n257ADC\_11A\_n257DDC\_11A\_n257GDC\_11A\_n257HDC\_11A\_n257I |
| DC\_1A-18A\_n3A-n257ADC\_1A-18A\_n3A-n257GDC\_1A-18A\_n3A-n257HDC\_1A-18A\_n3A-n257I | DC\_1A\_n3ADC\_1A\_n257ADC\_1A\_n257GDC\_1A\_n257HDC\_1A\_n257IDC\_18A\_n3ADC\_18A\_n257ADC\_18A\_n257GDC\_18A\_n257HDC\_18A\_n257I |
| DC\_1A-18A\_n78A-n257ADC\_1A-18A\_n78A-n257GDC\_1A-18A\_n78A-n257HDC\_1A-18A\_n78A-n257I | DC\_1A\_n78ADC\_1A\_n257ADC\_1A\_n257GDC\_1A\_n257HDC\_1A\_n257IDC\_18A\_n78ADC\_18A\_n257ADC\_18A\_n257GDC\_18A\_n257HDC\_18A\_n257I |
| DC\_1A-19A\_n77A-n257A2DC\_1A-19A\_n77A-n257G2DC\_1A-19A\_n77A-n257H2DC\_1A-19A\_n77A-n257I2 | DC\_1A\_n77ADC\_1A\_n257ADC\_1A\_n257GDC\_1A\_n257HDC\_1A\_n257IDC\_19A\_n77ADC\_19A\_n257ADC\_19A\_n257GDC\_19A\_n257HDC\_19A\_n257I |
| DC\_1A-19A\_n78A-n257A2DC\_1A-19A\_n78A-n257G2DC\_1A-19A\_n78A-n257H2DC\_1A-19A\_n78A-n257I2 | DC\_1A\_n78ADC\_1A\_n257ADC\_1A\_n257GDC\_1A\_n257HDC\_1A\_n257IDC\_19A\_n78ADC\_19A\_n257ADC\_19A\_n257GDC\_19A\_n257HDC\_19A\_n257I |
| DC\_1A-19A\_n79A-n257A2DC\_1A-19A\_n79A-n257G2DC\_1A-19A\_n79A-n257H2DC\_1A-19A\_n79A-n257I2 | DC\_1A\_n79ADC\_1A\_n257ADC\_1A\_n257GDC\_1A\_n257HDC\_1A\_n257IDC\_19A\_n79ADC\_19A\_n257ADC\_19A\_n257GDC\_19A\_n257HDC\_19A\_n257I |
| DC\_1A-21A\_n77A-n257A2DC\_1A-21A\_n77A-n257G2DC\_1A-21A\_n77A-n257H2DC\_1A-21A\_n77A-n257I2 | DC\_1A\_n77ADC\_1A\_n257ADC\_1A\_n257GDC\_1A\_n257HDC\_1A\_n257IDC\_21A\_n77ADC\_21A\_n257ADC\_21A\_n257GDC\_21A\_n257HDC\_21A\_n257I |
| DC\_1A-21A\_n78A-n257A2DC\_1A-21A\_n78A-n257G2DC\_1A-21A\_n78A-n257H2DC\_1A-21A\_n78A-n257I2 | DC\_1A\_n78ADC\_1A\_n257ADC\_1A\_n257GDC\_1A\_n257HDC\_1A\_n257IDC\_21A\_n78ADC\_21A\_n257ADC\_21A\_n257GDC\_21A\_n257HDC\_21A\_n257I |
| DC\_1A-21A\_n79A-n257A2DC\_1A-21A\_n79A-n257G2DC\_1A-21A\_n79A-n257H2DC\_1A-21A\_n79A-n257I2 | DC\_1A\_n79ADC\_1A\_n257ADC\_1A\_n257GDC\_1A\_n257HDC\_1A\_n257IDC\_21A\_n79ADC\_21A\_n257ADC\_21A\_n257GDC\_21A\_n257HDC\_21A\_n257I |
| DC\_1A-19A\_n79A-n257A2DC\_1A-19A\_n79A-n257G2DC\_1A-19A\_n79A-n257H2DC\_1A-19A\_n79A-n257I2 | DC\_1A\_n79A-n257ADC\_1A\_n79A-n257GDC\_1A\_n79A-n257HDC\_1A\_n79A-n257IDC\_19A\_n79A-n257ADC\_19A\_n79A-n257GDC\_19A\_n79A-n257HDC\_19A\_n79A-n257I |
| DC\_1A-21A\_n77A-n257A2DC\_1A-21A\_n77A-n257G2DC\_1A-21A\_n77A-n257H2DC\_1A-21A\_n77A-n257I2 | DC\_1A\_n77A-n257ADC\_1A\_n77A-n257GDC\_1A\_n77A-n257HDC\_1A\_n77A-n257IDC\_21A\_n77A-n257ADC\_21A\_n77A-n257GDC\_21A\_n77A-n257HDC\_21A\_n77A-n257I |
| DC\_1A-21A\_n78A-n257A2DC\_1A-21A\_n78A-n257G2DC\_1A-21A\_n78A-n257H2DC\_1A-21A\_n78A-n257I2 | DC\_1A\_n78A-n257ADC\_1A\_n78A-n257GDC\_1A\_n78A-n257HDC\_1A\_n78A-n257IDC\_21A\_n78A-n257ADC\_21A\_n78A-n257GDC\_21A\_n78A-n257HDC\_21A\_n78A-n257I |
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| NOTE 1: Uplink EN-DC configurations are the configurations supported by the present release of specifications.NOTE 2: Applicable for UE supporting inter-band EN-DC with mandatory simultaneous Rx/Tx capability. |

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### *<< End of changes >>*