**3GPP TSG-RAN4 Meeting #101bis-e *R4-22XXXX***

**Electronic meeting, Jan. 17 - 25, 2022**

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| *CR-Form-v12.2* |
| **CHANGE REQUEST** |
|  |
|  | **38.133** | **CR** | **Draft** | **rev** | **1** | **Current version:** | **17.4.0** |  |
|  |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* |
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| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network |  | Core Network |  |

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|  |
| ***Title:***  | Draft CR on measurement delay requirements with NCSG.  |
|  |  |
| ***Source to WG:*** | CATT |
| ***Source to TSG:*** | R4 |
|  |  |
| ***Work item code:*** | NR\_MG\_enh-Core |  | ***Date:*** | 2022-01-10 |
|  |  |  |  |  |
| ***Category:*** | B |  | ***Release:*** | Rel-17 |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)…Rel-15 (Release 15)Rel-16 (Release 16)Rel-17 (Release 17)Rel-18 (Release 18)* |
|  |  |
| ***Reason for change:*** | NCSG support is defined in R17 measurement gap enhancement WI and the measurement requirements based on NCSG need to be specified in TS 38.133.  |
|  |  |
| ***Summary of change:*** | 1. Introduce the intra-frequency measurement requirements with NCSG in TS 38.133.
2. Introduce the inter-frequency measurement requirements with NCSG in TS 38.133.
3. Introduce the measurement requirements on deactivated Scell with NCSG in TS 38.133.
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|  |  |
| ***Consequences if not approved:*** | The measurement requirements with NCSG are missing.  |
|  |  |
| ***Clauses affected:*** | New 9.2.7, new 9.3.10 |
|  |  |
|  | **Y** | **N** |  |  |
| ***Other specs*** |  |  |  Other core specifications  | TS/TR ... CR ...  |
| ***affected:*** |  |  |  Test specifications | TS/TR ... CR ...  |
| ***(show related CRs)*** |  |  |  O&M Specifications | TS/TR ... CR ...  |
|  |  |
| ***Other comments:*** |  |
|  |  |
| ***This CR's revision history:*** |  |

# <Start of Change 1>

### 9.2.7 Intra-frequency measurements with NCSG

#### 9.2.7.1 Intra-frequency cell identification

For the UE supporting NCSG, if NCSG is provided, the UE shall be able to identify a new detectable intra frequency cell within Tidentify\_intra\_without\_index if UE is not indicated to report SSB based RRM measurement result with the associated SSB index (*reportQuantityRsIndexes* or *maxNrofRSIndexesToReport* is not configured), or the UE has been indicated that the neighbour cell is synchronous with the serving cell (*deriveSSB-IndexFromCell* is enabled). Otherwise UE shall be able to identify a new detectable intra frequency cell within Tidentify\_intra\_with\_index. The UE shall be able to identify a new detectable intra frequency SS block of an already detected cell within Tidentify\_intra\_without\_index. It is assumed that *deriveSSB-IndexFromCell* is always enabled for FR1 TDD and FR2.

 Tidentify\_intra\_without\_index = TPSS/SSS\_sync\_intra + T SSB\_measurement\_period\_intra ms

 Tidentify\_intra\_with\_index = TPSS/SSS\_sync\_ntra + T SSB\_measurement\_period\_intra + TSSB\_time\_index\_intra ms

Where:

 TPSS/SSS\_sync\_intra: it is the time period used in PSS/SSS detection given in table 9.2.7.1-1, 9.2.7.1-2, 9.2.7.1-4 (deactivated Scell) or 9.2.7.1-5 (deactivated Scell).

 TSSB\_time\_index\_intra: it is the time period used to acquire the index of the SSB being measured given in table 9.2.7.1-3 or 9.2.7.1-6 (deactivated Scell).

 T SSB\_measurement\_period\_intra: equal to a measurement period of SSB based measurement given in table 9.2.7.2-1, 9.2.7.2-2, 9.2.7.2-3, 9.2.7.2-4 (deactivated Scell) or 9.2.7.2-5 (deactivated Scell).

 CSSFintra: it is a carrier specific scaling factor and is determined according to CSSFwithin\_ncsg,i in clause 9.1.5.x for measurement conducted within NCSG.

 Mpss/sss\_sync\_with\_gaps : For a UE supporting FR2 power class 1 or 5, Mpss/sss\_sync with\_gaps=40. For a UE supporting FR2 power class 2, Mpss/sss\_sync with\_gaps =24. For a UE supporting FR2 power class 3, Mpss/sss\_sync with\_gaps =24. For a UE supporting power class 4, Mpss/sss\_sync with\_gaps =24

 Mmeas\_period\_ with\_gaps: For a UE supporting power class 1 or 5, Mmeas\_period\_ with\_gaps =40. For a UE supporting power class 2, Mmeas\_period\_ with\_gaps =24. For a UE supporting power class 3, Mmeas\_period\_ with\_gaps =24. For a UE supporting power class 4, Mmeas\_period with\_gaps =24.

If the higher layer signaling in TS 38.331 [2] of *smtc2* is present and smtc1 is fully overlapping with NCSG and smtc2 is partially overlapping with NCSG, requirements are not specified for Tidentify\_intra\_without\_index or Tidentify\_intra\_with\_index.

Table 9.2.7.1-1: Time period for PSS/SSS detection with NCSG (FR1)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max(600ms, 5 x max(VIRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(600ms, ceil(M2Note 1x 5) x max(VIRP, SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | 5 x max(VIRP, DRX cycle) x CSSFintra |
| NOTE 1: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms, otherwise M2=1.NOTE 2: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *[intraRAT-MeasurementEnhancement-r16]* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell. |

**Table 9.2.7.1-2: Time period for PSS/SSS detection with NCSG (FR2)**

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max(600ms, Mpss/sss\_sync\_with\_gaps x max(VIRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(600ms, ceil(1.5x Mpss/sss\_sync\_with\_gaps) x max(VIRP, SMTC period, DRX cycle))x CSSFintra |
| DRX cycle>320ms | Mpss/sss\_sync\_with\_gaps x max(VIRP, DRX cycle) x CSSFintra |

Table 9.2.7.1-3: Time period for time index detection with NCSG (FR1)

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | max(120ms, 3 x max(VIRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(120ms, ceil(M2Note 1x 3) x max(VIRP, SMTC period,DRX cycle) x CSSFintra) |
| DRX cycle>320ms | 3 x max(VIRP, DRX cycle) x CSSFintra |
| NOTE 1: *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms, otherwise M2=1.NOTE 2: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *[intraRAT-MeasurementEnhancement-r16]* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell. |

Table 9.2.7.1-4: Time period for PSS/SSS detection with NCSG (deactivated SCell) (FR1)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | 5 x max(measCycleSCell, VIRP) x CSSFintra |
| DRX cycle≤ 320ms | 5 x max(measCycleSCell, VIRP, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | 5 x max(measCycleSCell, VIRP, DRX cycle) x CSSFintra |

**Table 9.2.7.1-5: Time period for PSS/SSS detection with NCSG (deactivated SCell) (FR2)**

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | Mpss/sss\_with\_ncsg x max(measCycleSCell, VIRP) x CSSFintra |
| DRX cycle≤ 320ms | Mpss/sss\_with\_ncsg x max(measCycleSCell, VIRP, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Mpss/sss\_with\_ncsg x max(measCycleSCell, VIRP, DRX cycle) x CSSFintra |

Table 9.2.7.1-6: Time period for time index detection with NCSG (deactivated SCell) (FR1)

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | 3 x max(measCycleSCell, VIRP) x CSSFintra |
| DRX cycle≤ 320ms | 3 x max(measCycleSCell, VIRP, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | 3 x max(measCycleSCell, VIRP,DRX cycle) x CSSFintra |

#### 9.2.7.2 Measurement period

When *highSpeedMeasFlag-r16* is configured, the measurement period with NCSG is specified in Table 9.2.7.2-3.

For either an FR1 or FR2 serving cell, longer measurement period would be expected during the period Tidentify\_CGI when the UE is requested to decode an NR CGI.

Table 9.2.7.2-1: Measurement period for intra-frequency measurements with NCSG (FR1)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | max(200ms, 5 x max(VIRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(200ms, ceil(1.5x 5) x max(VIRP, SMTC period,DRX cycle))x CSSFintra |
| DRX cycle>320ms | 5 x max(VIRP, DRX cycle) x CSSFintra |

Table 9.2.7.2-2: Measurement period for intra-frequency measurements with NCSG (FR2)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | max(400ms, Mmeas\_period with\_gaps x max(VIRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(400ms, ceil(1.5 x Mmeas\_period with\_gaps) x max(VIRP, SMTC period, DRX cycle)) Note 1 x CSSFintra |
| DRX cycle>320ms | Mmeas\_period with\_gaps x max(VIRP, DRX cycle) x CSSFintra |

Table 9.2.7.2-3: Measurement period with NCSG When *highSpeedMeasFlag-r16* is configured (FR1)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | max(200ms, 5 x max(VIRP, SMTC period)) Note 1 x CSSFintra |
| DRX cycle≤ 160ms | max(200ms, ceil(M2Note 2 x 5) x max(VIRP, SMTC period,DRX cycle)) x CSSFintra |
| 160ms < DRX cycle≤ 320ms | max(200ms, ceil(M2Note 2 x 4) x max(VIRP, DRX cycle)) x CSSFintra |
| DRX cycle>320ms | Y Note 3 x max(VIRP, DRX cycle) x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: M2 = 1.5 if SMTC periodicity > 40 ms, otherwise M2=1NOTE 3: Y=3 when SMTC <= 40ms, Y=5 when SMTC > 40msNOTE 4: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *[intraRAT-MeasurementEnhancement-r16]* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell. |

Table 9.2.7.2-4: Measurement period for intra-frequency measurements without NCSG (deactivated SCell) (FR1)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | 5 x max(measCycleSCell, VIRP) x CSSFintra |
| DRX cycle≤ 320ms | 5 x max(measCycleSCell, VIRP, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | 5 x max(measCycleSCell, VIRP, DRX cycle) x CSSFintra |

**Table 9.2.7.2-5: Measurement period for intra-frequency measurements without NCSG (deactivated SCell) (FR2)**

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | Mmeas\_period with\_gaps x max(measCycleSCell, VIRP) x CSSFintra |
| DRX cycle≤ 320ms | Mmeas\_period with\_gaps x max(measCycleSCell, VIRP, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Mmeas\_period with\_gaps x max(measCycleSCell, VIRP, DRX cycle) x CSSFintra |

* Note: Requirements for measurement on deactivated SCC in this clause do not apply if SMTC on the deactivated SCC is fully non-overlapped with NCSG, and the requirements for measurement on deactivated SCC specified in clause 9.2.5 apply.

#### 9.2.7.3 Scheduling availability during intra-frequency measurement with NCSG

# <End of Change 1>

# <Start of Change 2>

### 9.3.10 Inter-frequency measurement with NCSG

#### 9.3.10.1 Inter-frequency cell identification

For the UE supporting NCSG, if NCSG is provided, the UE shall be able to identify a new detectable inter frequency cell within Tidentify\_inter\_without\_index if UE is not indicated to report SSB based RRM measurement result with the associated SSB index (*reportQuantityRsIndexes* or *maxNrofRSIndexesToReport* is not configured). Otherwise UE shall be able to identify a new detectable inter frequency cell within Tidentify\_inter\_with\_index. The UE shall be able to identify a new detectable inter frequency SS block of an already detected cell within Tidentify\_inter\_without\_index.

Tidentify\_inter\_without\_index = (TPSS/SSS\_sync\_inter + T SSB\_measurement\_period\_inter) ms

Tidentify\_inter\_with\_index = (TPSS/SSS\_sync\_inter + T SSB\_measurement\_period\_inter + TSSB\_time\_index\_inter) ms

Where:

 TPSS/SSS\_sync\_inter: it is the time period used in PSS/SSS detection given in table 9.3.10.1-1 and table 9.3.10.1-2.

 TSSB\_time\_index\_inter: it is the time period used to acquire the index of the SSB being measured given in table 9.3.10.1-3 and table 9.3.10.1-4.

 TSSB\_measurement\_period\_inter: equal to a measurement period of SSB based measurement given in table 9.3.10.2-1 and table 9.3.10.2-2.

 Mpss/sss\_sync\_inter: For a UE supporting FR2 power class 1 or 5, Mpss/sss\_sync\_inter = 64 samples. For a UE supporting FR2 power class 2, Mpss/sss\_sync\_inter = 40 samples. For a UE supporting FR2 power class 3, Mpss/sss\_sync\_inter = 40 samples. For a UE supporting FR2 power class 4, Mpss/sss\_sync\_inter = 40 samples.

 MSSB\_index\_inter: For a UE supporting FR2 power class 1 or 5, MSSB\_index\_inter = 40 samples. For a UE supporting FR2 power class 2, MSSB\_index\_inter = 24 samples. For a UE supporting FR2 power class 3, MSSB\_index\_inter = 24 samples. For a UE supporting FR2 power class 4, MSSB\_index\_inter = 24 samples.

 Mmeas\_period\_inter: For a UE supporting FR2 power class 1 or 5, Mmeas\_period\_inter =64 samples. For a UE supporting FR2 power class 2, Mmeas\_period\_inter=40 samples. For a UE supporting FR2 power class 3, Mmeas\_period\_inter =40 samples. For a UE supporting FR2 power class 4, Mmeas\_period\_inter = 40 samples.

 CSSFinter: it is a carrier specific scaling factor and is determined according to CSSFwithin\_ncsg,i in clause 9.1.5.x for measurement conducted within NCSG.

**Table 9.3.10.1-1: Time period for PSS/SSS detection with NCSG (FR1)**

|  |  |
| --- | --- |
| **Condition NOTE1,2** | **TPSS/SSS\_sync\_inter** |
| No DRX |  Max(600ms, 8 × Max(VIRP, SMTC period)) × CSSFinter |
| DRX cycle ≤ 320ms | Max(600ms, Ceil(8\*1.5) × Max(VIRP, SMTC period, DRX cycle)) × CSSFinter |
| DRX cycle > 320ms | 8 × DRX cycle × CSSFinter |
| NOTE 1: DRX or non DRX requirements apply according to the conditions described in clause 3.6.1NOTE 2: In EN-DC operation, the parameters, timers and scheduling requests referred to in clause 3.6.1 are for the secondary cell group. The DRX cycle is the DRX cycle of the secondary cell group. |

**Table 9.3.10.1-2: Time period for PSS/SSS detection with NCSG (FR2)**

|  |  |
| --- | --- |
| **Condition NOTE1,2** | **TPSS/SSS\_sync\_inter** |
| No DRX | Max(600ms, Mpss/sss\_sync\_inter × Max(VIRP, SMTC period)) × CSSFinter |
| DRX cycle ≤ 320ms | Max(600ms, (1.5 × Mpss/sss\_sync\_inter) × Max(VIRP, SMTC period, DRX cycle)) × CSSFinter |
| DRX cycle > 320ms | Mpss/sss\_sync\_inter × DRX cycle × CSSFinter |
| NOTE 1: DRX or non DRX requirements apply according to the conditions described in clause 3.6.1NOTE 2: In EN-DC operation, the parameters, timers and scheduling requests referred to in clause 3.6.1 are for the secondary cell group. The DRX cycle is the DRX cycle of the secondary cell group. |

**Table 9.3.10.1-3: Time period for time index detection with NCSG (FR1)**

|  |  |
| --- | --- |
| **Condition NOTE1,2** | **TSSB\_time\_index\_inter** |
| No DRX | Max(120ms, 3 × Max(VIRP, SMTC period)) × CSSFinter |
| DRX cycle ≤ 320ms | Max(120ms, Ceil(3 × 1.5) × Max(VIRP, SMTC period, DRX cycle)) × CSSFinter |
| DRX cycle > 320ms | 3 × DRX cycle × CSSFinter |
| NOTE 1: DRX or non DRX requirements apply according to the conditions described in clause 3.6.1NOTE 2: In EN-DC operation, the parameters, timers and scheduling requests referred to in clause 3.6.1 are for the secondary cell group. The DRX cycle is the DRX cycle of the secondary cell group. |

**Table 9.3.10.1-4: Time period for time index detection with NCSG (FR2)**

|  |  |
| --- | --- |
| **Condition NOTE1,2** | **TSSB\_time\_index\_inter** |
| No DRX | Max(200ms, MSSB\_index\_inter × Max(VIRP, SMTC period)) × CSSFinter |
| DRX cycle ≤ 320ms | Max(200ms, (1.5 × MSSB\_index\_inter) × Max(VIRP, SMTC period, DRX cycle)) × CSSFinter |
| DRX cycle > 320ms | MSSB\_index\_inter × DRX cycle × CSSFinter |
| NOTE 1: DRX or non DRX requirements apply according to the conditions described in clause 3.6.1NOTE 2: In EN-DC operation, the parameters, timers and scheduling requests referred to in clause 3.6.1 are for the secondary cell group. The DRX cycle is the DRX cycle of the secondary cell group. |

#### 9.3.10.2 Measurement period

When NCSG are provided for inter frequency measurements, the UE physical layer shall be capable of reporting SS-RSRP, SS-RSRQ and SS-SINR measurements to higher layers with measurement accuracy as specified in clauses 10.1.4, 10.1.5, 10.1.9, 10.1.10, 10.1.14 and 10.1.15, respectively, as shown in table 9.3.10.2-1 and 9.3.10.2-2:

**Table 9.3.10.2-1: Measurement period for inter-frequency measurements with NCSG (FR1)**

|  |  |
| --- | --- |
| **Condition NOTE1,2** | **T SSB\_measurement\_period\_inter** |
| No DRX | Max(200ms, 8 × Max(VIRP, SMTC period)) × CSSFinter |
| DRX cycle ≤ 320ms | Max(200ms, Ceil(8 × 1.5) × Max(VIRP, SMTC period, DRX cycle)) × CSSFinter |
| DRX cycle > 320ms | 8 × DRX cycle × CSSFinter |
| NOTE 1: DRX or non DRX requirements apply according to the conditions described in clause 3.6.1NOTE 2: In EN-DC operation, the parameters, timers and scheduling requests referred to in clause 3.6.1 are for the secondary cell group. The DRX cycle is the DRX cycle of the secondary cell group. |

**Table 9.3.10.2-2: Measurement period for inter-frequency measurements with NCSG (FR2)**

|  |  |
| --- | --- |
| **Condition NOTE1,2** | **T SSB\_measurement\_period\_inter** |
| No DRX | Max(400ms, Mmeas\_period\_inter × Max(VIRP, SMTC period)) × CSSFinter |
| DRX cycle ≤ 320ms | Max(400ms, (1.5 × Mmeas\_period\_inter) × Max(VIRP, SMTC period, DRX cycle)) × CSSFinter |
| DRX cycle > 320ms | Mmeas\_period\_inter × DRX cycle × CSSFinter |
| NOTE 1: DRX or non DRX requirements apply according to the conditions described in clause 3.6.1NOTE 2: In EN-DC operation, the parameters, timers and scheduling requests referred to in clause 3.6.1 are for the secondary cell group. The DRX cycle is the DRX cycle of the secondary cell group. |

#### 9.3.10.3 Scheduling availability during inter-frequency measurement with NCSG

# <End of Change 2>