**3GPP TSG RAN2 #117-e R2-22xx**

**Electronic Meeting, 21 Feb – 03 Mar 2022**

**Agenda Item:**  **7.3**

**Source: Qualcomm Inc. (rapporteur)**

**Title:** **Report on [AT117-e][312][NBIOT R16] PUR Response Window (Qualcomm)**

**Document for: Discussion and decision**

### **1 Introduction**

This is the email discussion report of the following:

* [AT117-e][312][NBIOT R16] PUR Response Window (Qualcomm)

**Status**: Started

**Scope:** Discussion of whether correction is needed, and work on the CRs.

**Intended outcome:** Report in R2-2203574, and revised CR (if needed – Tdocs can be allocated if necessary).

**Deadline:** Thursday 24th February 1200 UTC

#### **1.1 Contact Information**

|  |  |  |
| --- | --- | --- |
| **Company** | **Contact Name** | **Email** |
| Qualcomm | Umesh Phuyal | uphuyal@qti.qualcomm.com |
| Huawei, HiSilicon | Odile Rollinger | odile.rollinger@huawei.com |
| ZTE | Ting Lu | lu.ting@zte.com.cn |
| Sequans | Noam Cayron | noam.cayron@sequans.com |
| Ericsson | Emre A. Yavuz | emre.yavuz@ericsson.com |
| LGE | HyunJung Choe | stella.choe@lge.com |
| MediaTek | Aaron Cai | Aaron.cai@mediatek.com |

### **2 Discussion**

#### **2.1 Background**

The PUR feature is applicable to both NB-IoT and eMTC in Release 16. PUR was mainly discussed in NB-IoT sessions, but developed jointly. There was some inconsistency in timer names during the CR merging. It is proposed to correct it in Release 16 so that the error is not transported to Rel-17 specs.

#### **2.2 Discussion on CR R2-2203724**

The CR under discussion is

[R2-2203724](https://www.3gpp.org/ftp/tsg_ran/WG2_RL2/TSGR2_117-e/Docs/R2-2203724.zip) Correction to pur-ResponseWindowTimer and removal of pur-ResponseWindowSize Qualcomm Incorporated, Huawei, HiSilicon CR Rel-16 36.321 16.6.0 1534 - F NB\_IOTenh3-Core, LTE\_eMTC5-Core Late

There is no ASN.1 field called *pur-ResponseWindowSize* in RRC. The mention of *pur-ResponseWindowSize* in this specification was intended to mean the field *pur-ResponseWindowTimer*. Also, the duration of *pur-ResponseWindowTimer* would be indicated by the field itself. The CR corrects the misalignment.

NOTE: RRC rapporteur’s CR for minor corrections is expected revise to capture a related editorial in the RRC specification (as shown below). So, no separate RRC CR was submitted.

***pur-ResponseWindowTimer***

Duration of the PUR response window in TS 36.321 [6]. Value in PDCCH periods. Value *pp2* corresponds to 2 PDDCH periods, *pp3* corresponds to 3 PDCCH periods, and so on.

The value considered by the UE is: *pur-ResponseWindowTimer* = Min (signaled value x PDCCH period, 10.24s).

**Question 1: Comments on CR R2-2203724.**

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| --- | --- | --- |
| **Company** | **Support/No Support** | **Comments** |
| Huawei, HiSilicon | Support |  |
| Qualcomm | Support | proponent |
| ZTE | Support |  |
| Sequans | Support |  |
| Ericsson | Support |  |
| LGE | Support |  |
| MediaTek | Support |  |

**Summary:**

**TBD**

#### **2.3 Other**

**Question 11: Anything else that is not covered by above questions? Please explain.**

|  |  |
| --- | --- |
| **Company** | **Comments** |
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|  |  |

### **3 Conclusion**

TBD

### **4 References**

[1] [R2-2203724](https://www.3gpp.org/ftp/tsg_ran/WG2_RL2/TSGR2_117-e/Docs/R2-2203724.zip) Correction to pur-ResponseWindowTimer and removal of pur-ResponseWindowSize Qualcomm Incorporated, Huawei, HiSilicon CR Rel-16 36.321 16.6.0 1534 - F NB\_IOTenh3-Core, LTE\_eMTC5-Core Late