**3GPP TSG-RAN WG1 Meeting #107-e *R1-21xxxxx***

**e-Meeting, November 11–19, 2021**

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| *CR-Form-v12.1* | | | | | | | | |
| **DRAFT CHANGE REQUEST** | | | | | | | | |
|  | | | | | | | | |
|  | **38.212** | **CR** |  | **rev** |  | **Current version:** | **16.7.0** |  |
|  | | | | | | | | |
| *For* [***HELP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* | | | | | | | | |
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| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network | **X** | Core Network |  |

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|  | | | | | | | | | | |
| ***Title:*** | Introduction of features to extend current NR operation to 71 GHz | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Source to WG:*** | Huawei | | | | | | | | | |
| ***Source to TSG:*** | R1 | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Work item code:*** | NR\_ext\_to\_71GHz-Core | | | | |  | ***Date:*** | | | 2021-11-29 |
|  |  | | | |  | |  | | |  |
| ***Category:*** | **B** |  | | | | | ***Release:*** | | | Rel-17 |
|  | *Use one of the following categories:* ***F*** *(correction)* ***A*** *(mirror corresponding to a change in an earlier release)* ***B*** *(addition of feature),* ***C*** *(functional modification of feature)* ***D*** *(editorial modification)*  Detailed explanations of the above categories can be found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | | | | | | | | *Use one of the following releases: Rel-8 (Release 8) Rel-9 (Release 9) Rel-10 (Release 10) Rel-11 (Release 11) … Rel-15 (Release 15) Rel-16 (Release 16) Rel-17 (Release 17) Rel-18 (Release 18)* | |
|  |  | | | | | | | | | |
| ***Reason for change:*** | | Introduce features to support NR features in frequency range 2-2 | | | | | | | | |
|  | |  | | | | | | | | |
| ***Summary of change:*** | | Support of NR featues in frequency range 2-2 | | | | | | | | |
|  | |  | | | | | | | | |
| ***Consequences if not approved:*** | | NR featues in frequency range 2-2 will be incomplete | | | | | | | | |
|  | |  | | | | | | | | |
| ***Clauses affected:*** | | 6.3.1.4, 7.3.1, 7.3.1.1.1, 7.3.1.1.2, 7.3.1.2.1, 7.3.1.2.2 | | | | | | | | |
|  | |  | | | | | | | | |
|  | | **Y** | **N** |  | | | |  | | |
| ***Other specs*** | | **X** |  | Other core specifications | | | | TS 38.211, TS 38.213, TS 38.214 | | |
| ***affected:*** | |  | **X** | Test specifications | | | |  | | |
| ***(show related CRs)*** | |  | **X** | O&M Specifications | | | |  | | |
|  | |  | | | | | | | | |
| ***Other comments:*** | |  | | | | | | | | |
|  | |  | | | | | | | | |
| ***This CR's revision history:*** | |  | | | | | | | | |

#### 6.3.1.4 Rate matching

For PUCCH formats 2/3/4, the total rate matching output sequence length  is given by Table 6.3.1.4-1, where  , , and  are the number of symbols carrying UCI for PUCCH formats 2/3/4 respectively; ,and are the number of PRBs that are determined by the UE for PUCCH formats 2/3/4 transmission respectively according to Clause 9.2 of [5, TS38.213]; and , , and  are the spreading factors for PUCCH format 2, PUCCH format 3, and PUCCH format 4, respectively.

Table 6.3.1.4-1: Total rate matching output sequence length 

|  |  |  |
| --- | --- | --- |
| *PUCCH format* | *Modulation order* | |
| QPSK | π/2-BPSK |
| PUCCH format 2 |  | N/A |
| PUCCH format 3 |  |  |
| PUCCH format 4 |  |  |

### 7.3.1 DCI formats

The DCI formats defined in table 7.3.1-1 are supported.

Table 7.3.1-1: DCI formats

|  |  |
| --- | --- |
| **DCI format** | **Usage** |
| 0\_0 | Scheduling of PUSCH in one cell |
| 0\_1 | Scheduling of one or multiple PUSCH in one cell, or indicating downlink feedback information for configured grant PUSCH (CG-DFI) |
| 0\_2 | Scheduling of PUSCH in one cell |
| 1\_0 | Scheduling of PDSCH in one cell |
| 1\_1 | Scheduling of one or multiple PDSCH in one cell, and/or triggering one shot HARQ-ACK codebook feedback |
| 1\_2 | Scheduling of PDSCH in one cell |
| 2\_0 | Notifying a group of UEs of the slot format, available RB sets, COT duration and search space set group switching |
| 2\_1 | Notifying a group of UEs of the PRB(s) and OFDM symbol(s) where UE may assume no transmission is intended for the UE |
| 2\_2 | Transmission of TPC commands for PUCCH and PUSCH |
| 2\_3 | Transmission of a group of TPC commands for SRS transmissions by one or more UEs |
| 2\_4 | Notifying a group of UEs of the PRB(s) and OFDM symbol(s) where UE cancels the corresponding UL transmission from the UE |
| 2\_5 | Notifying the availability of soft resources as defined in Clause 9.3.1 of [10, TS 38.473] |
| 2\_6 | Notifying the power saving information outside DRX Active Time for one or more UEs |
| 3\_0 | Scheduling of NR sidelink in one cell |
| 3\_1 | Scheduling of LTE sidelink in one cell |

< Unchanged parts are omitted >

##### 7.3.1.1.1 Format 0\_0

DCI format 0\_0 is used for the scheduling of PUSCH in one cell.

< Unchanged parts are omitted >

Table 7.3.1.1.1-4A: Channel access type & CP extension if *ChannelAccessMode-r16* = "*semistatic*" is provided in frequency range 1

|  |  |  |
| --- | --- | --- |
| Bit field mapped to index | Channel Access Type | The CP extension T\_"ext" index defined in Clause 5.3.1 of [4, TS 38.211] |
| 0 | No sensing as defined in Clause 4.3 in TS 37.213 | 0 |
| 1 | No sensing as defined in Clause 4.3 in TS 37.213 | 2 |
| 2 | 9us sensing within a 25us interval as defined in Clause 4.3 in TS 37.213 | 0 |
| 3 | - | - |

##### 7.3.1.1.2 Format 0\_1

DCI format 0\_1 is used for the scheduling of one or multiple PUSCH in one cell, or indicating CG downlink feedback information (CG-DFI) to a UE.

< Unchanged parts are omitted >

- Time domain resource assignment – 0, 1, 2, 3, 4, 5, or 6 bits

- If the higher layer parameter *pusch-TimeDomainAllocationListDCI-0-1* is not configured and if the higher layer parameter *pusch-TimeDomainAllocationListForMultiPUSCH* is not configured and if the higher layer parameter *pusch-TimeDomainResourceAllocationListForMultiPUSCH-r17* is not configured and if the higher layer parameter *pusch-TimeDomainAllocationList* is configured, 0, 1, 2, 3, or 4 bits as defined in Clause 6.1.2.1 of [6, TS38.214]. The bitwidth for this field is determined as bits, where *I* is the number of entries in the higher layer parameter *pusch-TimeDomainAllocationList*;

- If the higher layer parameter *pusch-TimeDomainAllocationListDCI-0-1* is configured or if the higher layer parameter *pusch-TimeDomainAllocationListForMultiPUSCH is configured* or if the higher layer parameter *push-TimeDomainResourceAllocationListForMultiPUSCH-r17* is configured, 0, 1, 2, 3, 4, 5 or 6 bits as defined in Clause 6.1.2.1 of [6, TS38.214]. The bitwidth for this field is determined as bits, where *I* is the number of entries in the higher layer parameter *pusch-TimeDomainAllocationListDCI-0-1* or *pusch-TimeDomainAllocationListForMultiPUSCH* or *pusch-TimeDomainResourceAllocationListForMultiPUSCH-r17*;

- otherwise the bitwidth for this field is determined as bits, where *I* is the number of entries in the default table*.*

- Frequency hopping flag – 0 or 1 bit:

- 0 bit if only resource allocation type 0 is configured, or if the higher layer parameter *frequencyHopping* is not configured and the higher layer parameter *pusch-RepTypeIndicatorDCI-0-1* is not configured to *pusch-RepTypeB*, or if the higher layer parameter *frequencyHoppingDCI-0-1* is not configured and *pusch-RepTypeIndicatorDCI-0-1* is configured to *pusch-RepTypeB*, or if only resource allocation type 2 is configured;

- 1 bit according to Table 7.3.1.1.1-3 otherwise, only applicable to resource allocation type 1, as defined in Clause 6.3 of [6, TS 38.214].

- Modulation and coding scheme – 5 bits as defined in Clause 6.1.4.1 of [6, TS 38.214]

- New data indicator – 1 bit if the number of scheduled PUSCH indicated by the Time domain resource assignment field is 1; otherwise 2, 3, 4, 5, 6, 7 or 8 bits determined based on the maximum number of schedulable PUSCH among all entries in the higher layer parameter *pusch-TimeDomainAllocationListForMultiPUSCH* or *pusch-TimeDomainResourceAllocationListForMultiPUSCH-r17*, where each bit corresponds to one scheduled PUSCH as defined in clause 6.1.4 in [6, TS 38.214].

- Redundancy version – – number of bits determined by the following:

- 2 bits as defined in Table 7.3.1.1.1-2 if the number of scheduled PUSCH indicated by the Time domain resource assignment field is 1;

- otherwise 2, 3, 4, 5, 6, 7 or 8 bits determined by the maximum number of schedulable PUSCHs among all entries in the higher layer parameter *pusch-TimeDomainAllocationListForMultiPUSCH* or *pusch-TimeDomainResourceAllocationListForMultiPUSCH-r17*, where each bit corresponds to one scheduled PUSCH as defined in clause 6.1.4 in [6, TS 38.214] and redundancy version is determined according to Table 7.3.1.1.2-34.

< Unchanged parts are omitted >

- ChannelAccess-CPext-CAPC – 0, 1, 2, 3, 4, 5 or 6 bits. The bitwidth for this field is determined as bits, where *I* is the number of entries in the higher layer parameter *ul-AccessConfigListDCI-0-1* or in Table 7.3.1.1.1-4A if *ChannelAccessMode-r16* = "*semistatic*" is provided, for operation in a cell with shared spectrum channel access; otherwise 0 bit. One or more entries from Table 7.3.1.1.2-35 or Table 7.3.1.1.2-35A are configured by the higher layer parameter *ul-AccessConfigListDCI-0-1.*

< Unchanged parts are omitted >

Table 7.3.1.1.2-35: Allowed entries for DCI format 0\_1, configured by higher layer parameter *ul-AccessConfigListDCI-0-1* in frequency range 1

|  |  |  |  |
| --- | --- | --- | --- |
| **Entry index** | **Channel Access Type** | **The CP extension T\_"ext" index defined in Clause 5.3.1 of [4, 38.211]** | **CAPC** |
| 0 | Type2C-ULChannelAccess  defined in [clause 4.2.1.2.3 in 37.213] | 0 | 1 |
| 1 | Type2C-ULChannelAccess  defined in [clause 4.2.1.2.3 in 37.213] | 0 | 2 |
| 2 | Type2C-ULChannelAccess  defined in [clause 4.2.1.2.3 in 37.213] | 0 | 3 |
| 3 | Type2C-ULChannelAccess  defined in [clause 4.2.1.2.3 in 37.213] | 0 | 4 |
| 4 | Type2C-ULChannelAccess  defined in [clause 4.2.1.2.3 in 37.213] | 2 | 1 |
| 5 | Type2C-ULChannelAccess  defined in [clause 4.2.1.2.3 in 37.213] | 2 | 2 |
| 6 | Type2C-ULChannelAccess  defined in [clause 4.2.1.2.3 in 37.213] | 2 | 3 |
| 7 | Type2C-ULChannelAccess  defined in [clause 4.2.1.2.3 in 37.213] | 2 | 4 |
| 8 | Type2B-ULChannelAccess  defined in [clause 4.2.1.2.3 in 37.213] | 0 | 1 |
| 9 | Type2B-ULChannelAccess  defined in [clause 4.2.1.2.3 in 37.213] | 0 | 2 |
| 10 | Type2B-ULChannelAccess  defined in [clause 4.2.1.2.3 in 37.213] | 0 | 3 |
| 11 | Type2B-ULChannelAccess  defined in [clause 4.2.1.2.3 in 37.213] | 0 | 4 |
| 12 | Type2B-ULChannelAccess  defined in [clause 4.2.1.2.3 in 37.213] | 2 | 1 |
| 13 | Type2B-ULChannelAccess  defined in [clause 4.2.1.2.3 in 37.213] | 2 | 2 |
| 14 | Type2B-ULChannelAccess  defined in [clause 4.2.1.2.3 in 37.213] | 2 | 3 |
| 15 | Type2B-ULChannelAccess  defined in [clause 4.2.1.2.3 in 37.213] | 2 | 4 |
| 16 | Type2A-ULChannelAccess defined in [clause 4.2.1.2.1 in 37.213] | 0 | 1 |
| 17 | Type2A-ULChannelAccess defined in [clause 4.2.1.2.1 in 37.213] | 0 | 2 |
| 18 | Type2A-ULChannelAccess defined in [clause 4.2.1.2.1 in 37.213] | 0 | 3 |
| 19 | Type2A-ULChannelAccess defined in [clause 4.2.1.2.1 in 37.213] | 0 | 4 |
| 20 | Type2A-ULChannelAccess defined in [clause 4.2.1.2.1 in 37.213] | 1 | 1 |
| 21 | Type2A-ULChannelAccess defined in [clause 4.2.1.2.1 in 37.213] | 1 | 2 |
| 22 | Type2A-ULChannelAccess defined in [clause 4.2.1.2.1 in 37.213] | 1 | 3 |
| 23 | Type2A-ULChannelAccess defined in [clause 4.2.1.2.1 in 37.213] | 1 | 4 |
| 24 | Type2A-ULChannelAccess defined in [clause 4.2.1.2.1 in 37.213] | 3 | 1 |
| 25 | Type2A-ULChannelAccess defined in [clause 4.2.1.2.1 in 37.213] | 3 | 2 |
| 26 | Type2A-ULChannelAccess defined in [clause 4.2.1.2.1 in 37.213] | 3 | 3 |
| 27 | Type2A-ULChannelAccess defined in [clause 4.2.1.2.1 in 37.213] | 3 | 4 |
| 28 | Type1-ULChannelAccess defined in [clause 4.2.1.1 in 37.213] | 0 | 1 |
| 29 | Type1-ULChannelAccess defined in [clause 4.2.1.1 in 37.213] | 0 | 2 |
| 30 | Type1-ULChannelAccess defined in [clause 4.2.1.1 in 37.213] | 0 | 3 |
| 31 | Type1-ULChannelAccess defined in [clause 4.2.1.1 in 37.213] | 0 | 4 |
| 32 | Type1-ULChannelAccess defined in [clause 4.2.1.1 in 37.213] | 1 | 1 |
| 33 | Type1-ULChannelAccess defined in [clause 4.2.1.1 in 37.213] | 1 | 2 |
| 34 | Type1-ULChannelAccess defined in [clause 4.2.1.1 in 37.213] | 1 | 3 |
| 35 | Type1-ULChannelAccess defined in [clause 4.2.1.1 in 37.213] | 1 | 4 |
| 36 | Type1-ULChannelAccess defined in [clause 4.2.1.1 in 37.213] | 2 | 1 |
| 37 | Type1-ULChannelAccess defined in [clause 4.2.1.1 in 37.213] | 2 | 2 |
| 38 | Type1-ULChannelAccess defined in [clause 4.2.1.1 in 37.213] | 2 | 3 |
| 39 | Type1-ULChannelAccess defined in [clause 4.2.1.1 in 37.213] | 2 | 4 |
| 40 | Type1-ULChannelAccess defined in [clause 4.2.1.1 in 37.213] | 3 | 1 |
| 41 | Type1-ULChannelAccess defined in [clause 4.2.1.1 in 37.213] | 3 | 2 |
| 42 | Type1-ULChannelAccess defined in [clause 4.2.1.1 in 37.213] | 3 | 3 |
| 43 | Type1-ULChannelAccess defined in [clause 4.2.1.1 in 37.213] | 3 | 4 |

Table 7.3.1.1.2-35A: Allowed entries for DCI format 0\_1, configured by higher layer parameter *ul-AccessConfigListDCI-0-1* in frequency range 2-2

|  |  |
| --- | --- |
| **Entry index** | **Channel Access Type** |
| 0 | Type 1 channel access defined in clause 4.4.1 of 37.213 |
| 1 | Type 2 channel access defined in clause 4.4.2 of 37.213 |
| 2 | Type 3 channel access defined in clause 4.4.3 of 37.213 |

7.3.1.2 DCI formats for scheduling of PDSCH

7.3.1.2.1 Format 1\_0

DCI format 1\_0 is used for the scheduling of PDSCH in one DL cell.

The following information is transmitted by means of the DCI format 1\_0 with CRC scrambled by C-RNTI or CS-RNTI or MCS-C-RNTI:

- Identifier for DCI formats – 1 bits

- The value of this bit field is always set to 1, indicating a DL DCI format

- Frequency domain resource assignment –  bits where  is given by clause 7.3.1.0

If the CRC of the DCI format 1\_0 is scrambled by C-RNTI and the "Frequency domain resource assignment" field are of all ones, the DCI format 1\_0 is for random access procedure initiated by a PDCCH order, with all remaining fields set as follows:

- Random Access Preamble index – 6 bits according to *ra-PreambleIndex* in Clause 5.1.2 of [8, TS38.321]

- UL/SUL indicator – 1 bit. If the value of the "Random Access Preamble index" is not all zeros and if the UE is configured with *supplementaryUplink* in *ServingCellConfig* in the cell, this field indicates which UL carrier in the cell to transmit the PRACH according to Table 7.3.1.1.1-1; otherwise, this field is reserved

- SS/PBCH index – 6 bits. If the value of the "Random Access Preamble index" is not all zeros, this field indicates the SS/PBCH that shall be used to determine the RACH occasion for the PRACH transmission; otherwise, this field is reserved.

- PRACH Mask index – 4 bits. If the value of the "Random Access Preamble index" is not all zeros, this field indicates the RACH occasion associated with the SS/PBCH indicated by "SS/PBCH index" for the PRACH transmission, according to Clause 5.1.1 of [8, TS38.321]; otherwise, this field is reserved

- Reserved bits – 12 bits for operation in a cell with shared spectrum channel access in frequency range 1 or when the DCI format is monitored in common search space for operation in a cell in frequency range 2-2; otherwise 10 bits

Otherwise, all remaining fields are set as follows:

- Time domain resource assignment – 4 bits as defined in Clause 5.1.2.1 of [6, TS 38.214]

- VRB-to-PRB mapping – 1 bit according to Table 7.3.1.2.2-5

- Modulation and coding scheme – 5 bits as defined in Clause 5.1.3 of [6, TS 38.214]

- New data indicator – 1 bit

- Redundancy version – 2 bits as defined in Table 7.3.1.1.1-2

- HARQ process number – 4 bits

- Downlink assignment index – 2 bits as defined in Clause 9.1.3 of [5, TS 38.213], as counter DAI

- TPC command for scheduled PUCCH – 2 bits as defined in Clause 7.2.1 of [5, TS 38.213]

- PUCCH resource indicator – 3 bits as defined in Clause 9.2.3 of [5, TS 38.213]

- PDSCH-to-HARQ\_feedback timing indicator – 3 bits as defined in Clause 9.2.3 of [5, TS38.213]

- ChannelAccess-CPext – 2 bits indicating combinations of channel access type and CP extension as defined in Table 7.3.1.1.1-4, or Table 7.3.1.1.1-4A if *ChannelAccessMode-r16* = "*semistatic*" is provided, for operation in a cell with shared spectrum channel access; 0 bits otherwise

- Reserved bits – 2 bits when the DCI format is monitored in common search space for operation in a cell in frequency range 2-2 and the number of bits for the field of 'ChannelAccess-CPext' is 0; 0 bits otherwise

The following information is transmitted by means of the DCI format 1\_0 with CRC scrambled by P-RNTI:

- Short Messages Indicator – 2 bits according to Table 7.3.1.2.1-1.

- Short Messages – 8 bits, according to Clause 6.5 of [9, TS38.331]. If only the scheduling information for Paging is carried, this bit field is reserved.

- Frequency domain resource assignment – bits. If only the short message is carried, this bit field is reserved.

-  is the size of CORESET 0

- Time domain resource assignment – 4 bits as defined in Clause 5.1.2.1 of [6, TS38.214]. If only the short message is carried, this bit field is reserved.

- VRB-to-PRB mapping – 1 bit according to Table 7.3.1.2.2-5. If only the short message is carried, this bit field is reserved.

- Modulation and coding scheme – 5 bits as defined in Clause 5.1.3 of [6, TS38.214], using Table 5.1.3.1-1. If only the short message is carried, this bit field is reserved.

- TB scaling – 2 bits as defined in Clause 5.1.3.2 of [6, TS38.214]. If only the short message is carried, this bit field is reserved.

- Reserved bits – 8 bits for operation in a cell with shared spectrum channel access in frequency range 1 or for operation in a cell in frequency range 2-2; otherwise 6 bits

The following information is transmitted by means of the DCI format 1\_0 with CRC scrambled by SI-RNTI:

- Frequency domain resource assignment – bits

-  is the size of CORESET 0

- Time domain resource assignment – 4 bits as defined in Clause 5.1.2.1 of [6, TS38.214]

- VRB-to-PRB mapping – 1 bit according to Table 7.3.1.2.2-5

- Modulation and coding scheme – 5 bits as defined in Clause 5.1.3 of [6, TS38.214], using Table 5.1.3.1-1

- Redundancy version – 2 bits as defined in Table 7.3.1.1.1-2

- System information indicator – 1 bit as defined in Table 7.3.1.2.1-2

- Reserved bits – 17 bits for operation in a cell with shared spectrum channel access in frequency range 1 or for operation in a cell in frequency range 2-2; otherwise 15 bits

The following information is transmitted by means of the DCI format 1\_0 with CRC scrambled by RA-RNTI or MsgB-RNTI:

- Frequency domain resource assignment – bits

-  is the size of CORESET 0 if CORESET 0 is configured for the cell and  is the size of initial DL bandwidth part if CORESET 0 is not configured for the cell

- Time domain resource assignment – 4 bits as defined in Clause 5.1.2.1 of [6, TS38.214]

- VRB-to-PRB mapping – 1 bit according to Table 7.3.1.2.2-5

- Modulation and coding scheme – 5 bits as defined in Clause 5.1.3 of [6, TS38.214], using Table 5.1.3.1-1

- TB scaling – 2 bits as defined in Clause 5.1.3.2 of [6, TS38.214]

- LSBs of SFN – 2 bits for the DCI format 1\_0 with CRC scrambled by MsgB-RNTI as defined in Clause 8.2A of [5, TS 38.213] if *msgB-responseWindow* is configured to be larger than 10 ms; or 2 bits for the DCI format 1\_0 with CRC scrambled by RA-RNTI as defined in Clause 8.2 of [5, TS 38.213] for operation in a cell with shared spectrum channel access if *ra-ResponseWindow or ra-ResponseWindow-v1610* is configured to be larger than 10 ms; 0 bit otherwise

- Reserved bits – (16 – *A*) bits for operation in a cell without shared spectrum access in frequency range 1 and frequency range 2-1, (18 – *A*) for operation in a cell with shared spectrum access in frequency range 1 or for operation in a cell in frequency range 2-2, where the value of *A* is the number of bits for the field of 'LSBs of SFN' as defined above

The following information is transmitted by means of the DCI format 1\_0 with CRC scrambled by TC-RNTI:

- Identifier for DCI formats – 1 bit

- The value of this bit field is always set to 1, indicating a DL DCI format

- Frequency domain resource assignment – bits

-  is the size of CORESET 0

- Time domain resource assignment – 4 bits as defined in Clause 5.1.2.1 of [6, TS38.214]

- VRB-to-PRB mapping – 1 bit according to Table 7.3.1.2.2-5

- Modulation and coding scheme – 5 bits as defined in Clause 5.1.3 of [6, TS38.214], using Table 5.1.3.1-1

- New data indicator – 1 bit

- Redundancy version – 2 bits as defined in Table 7.3.1.1.1-2

- HARQ process number – 4 bits

- Downlink assignment index – 2 bits, reserved

- TPC command for scheduled PUCCH – 2 bits as defined in Clause 7.2.1 of [5, TS38.213]

- PUCCH resource indicator – 3 bits as defined in Clause 9.2.3 of [5, TS38.213]

- PDSCH-to-HARQ\_feedback timing indicator – 3 bits as defined in Clause 9.2.3 of [5, TS38.213]

- ChannelAccess-CPext – 2 bits indicating combinations of channel access type and CP extension as defined in Table 7.3.1.1.1-4, or Table 7.3.1.1.1.4A, if *ChannelAccessMode-r16* = "*semistatic*" is provided for operation in a cell with shared spectrum channel access; otherwise 0 bit

- Reserved bits – 2 bits when the DCI format is monitored in common search space for operation in a cell in frequency range 2-2 and the number of bits for the field of 'ChannelAccess-CPext' is 0; 0 bits otherwise

< Unchanged parts are omitted >

##### 7.3.1.2.2 Format 1\_1

DCI format 1\_1 is used for the scheduling of one or multiple PDSCH in one cell.

The following information is transmitted by means of the DCI format 1\_1 with CRC scrambled by C-RNTI or CS-RNTI or MCS-C-RNTI:

- Identifier for DCI formats – 1 bits

- The value of this bit field is always set to 1, indicating a DL DCI format

- Carrier indicator – 0 or 3 bits as defined in Clause 10.1 of [5, TS 38.213].

- Bandwidth part indicator – 0, 1 or 2 bits as determined by the number of DL BWPs  configured by higher layers, excluding the initial DL bandwidth part. The bitwidth for this field is determined as bits, where

-  if , in which case the bandwidth part indicator is equivalent to the ascending order of the higher layer parameter *BWP-Id*;

- otherwise , in which case the bandwidth part indicator is defined in Table 7.3.1.1.2-1;

If a UE does not support active BWP change via DCI, the UE ignores this bit field.

< Unchanged parts are omitted >

- Time domain resource assignment – 0, 1, 2, 3, 4, 5 or 6 bits

- If the higher layer parameter *pdsch-TimeDomainResourceAllocationListForMultiPDSCH* is not configured and if the higher layer parameter *pdsch-TimeDomainAllocationList* is configured, 0, 1, 2, 3 or 4 bits as defined in Clause 5.1.2.1 of [6, TS 38.214]. The bitwidth for this field is determined as bits, where *I* is the number of entries in the higher layer parameter *pdsch-TimeDomainAllocationList* if the higher layer parameter is configured;

- If the higher layer parameter *pdsch-TimeDomainResourceAllocationListForMultiPDSCH* is configured, 0, 1, 2, 3, 4, 5 or 6 bits as defined in Clause 5.1.2.1 of [6, TS38.214]. The bitwidth for this field is determined as bits, where *I* is the number of entries in the higher layer parameter *pdsch-TimeDomainResourceAllocationListForMultiPDSCH*;

- Otherwise *I* is the number of entries in the default table.

- VRB-to-PRB mapping – 0 or 1 bit:

- 0 bit if only resource allocation type 0 is configured or if interleaved VRB-to-PRB mapping is not configured by high layers;

- 1 bit according to Table 7.3.1.2.2-5 otherwise, only applicable to resource allocation type 1, as defined in Clause 7.3.1.6 of [4, TS 38.211].

- PRB bundling size indicator – 0 bit if the higher layer parameter *prb-BundlingType* is not configured or is set to 'staticBundling', or 1 bit if the higher layer parameter *prb-BundlingType* is set to 'dynamicBundling' according to Clause 5.1.2.3 of [6, TS 38.214].

- Rate matching indicator – 0, 1, or 2 bits according to higher layer parameters *rateMatchPatternGroup1* and *rateMatchPatternGroup2*, where the MSB is used to indicate *rateMatchPatternGroup1* and the LSB is used to indicate *rateMatchPatternGroup2* when there are two groups.

- ZP CSI-RS trigger – 0, 1, or 2 bits as defined in Clause 5.1.4.2 of [6, TS 38.214]. The bitwidth for this field is determined as bits, where is the number of aperiodic ZP CSI-RS resource sets configured by higher layer.

For transport block 1:

- Modulation and coding scheme – 5 bits as defined in Clause 5.1.3.1 of [6, TS 38.214]

- New data indicator – 1 bit if the number of scheduled PDSCH indicated by the Time domain resource assignment field is 1; otherwise 2, 3, 4, 5, 6, 7 or 8 bits determined based on the maximum number of schedulable PDSCH among all entries in the higher layer parameter *pdsch-TimeDomainResourceAllocationListForMultiPDSCH*, where each bit corresponds to one scheduled PDSCH as defined in clause 5.1.3 in [6, TS 38.214].

- Redundancy version – number of bits determined by the following:

- 2 bits as defined in Table 7.3.1.1.1-2 if the number of scheduled PDSCH indicated by the Time domain resource assignment field is 1;

- otherwise 2, 3, 4, 5, 6, 7 or 8 bits determined by the maximum number of schedulable PDSCHs among all entries in the higher layer parameter *pdsch-TimeDomainResourceAllocationListForMultiPDSCH*, where each bit corresponds to one scheduled PDSCH as defined in clause 5.1.3 in [6, TS 38.214] and redundancy version is determined according to Table 7.3.1.1.2-34.

For transport block 2 (only present if *maxNrofCodeWordsScheduledByDCI* equals 2):

- Modulation and coding scheme – 5 bits as defined in Clause 5.1.3.1 of [6, TS 38.214]

- New data indicator – 1 bit if the number of scheduled PDSCH indicated by the Time domain resource assignment field is 1; otherwise 2, 3, 4, 5, 6, 7 or 8 bits determined based on the maximum number of schedulable PDSCH among all entries in the higher layer parameter *pdsch-TimeDomainResourceAllocationListForMultiPDSCH*, where each bit corresponds to one scheduled PDSCH as defined in clause 5.1.3 in [6, TS 38.214].

- Redundancy version –number of bits determined by the following:

- 2 bits as defined in Table 7.3.1.1.1-2 if the number of scheduled PDSCH indicated by the Time domain resource assignment field is 1;

- otherwise 2, 3, 4, 5, 6, 7 or 8 bits determined by the maximum number of schedulable PDSCHs among all entries in the higher layer parameter *pdsch-TimeDomainResourceAllocationListForMultiPDSCH*, where each bit corresponds to one scheduled PDSCH as defined in clause 5.1.3 in [6, TS 38.214] and redundancy version is determined according to Table 7.3.1.1.2-34.

If "Bandwidth part indicator" field indicates a bandwidth part other than the active bandwidth part and the value of *maxNrofCodeWordsScheduledByDCI* for the indicated bandwidth part equals 2 and the value of *maxNrofCodeWordsScheduledByDCI* for the active bandwidth part equals 1, the UE assumes zeros are padded when interpreting the "Modulation and coding scheme", "New data indicator", and "Redundancy version" fields of transport block 2 according to Clause 12 of [5, TS38.213], and the UE ignores the "Modulation and coding scheme", "New data indicator", and "Redundancy version" fields of transport block 2 for the indicated bandwidth part.

< Unchanged parts are omitted >

- ChannelAccess-CPext – 0, 1, 2, 3 or 4 bits. The bitwidth for this field is determined as bits, where *I* is the number of entries in the higher layer parameter *ul-AccessConfigListDCI-1-1* or in Table 7.3.1.1.1-4A if *ChannelAccessMode-r16* = "*semistatic*" is provided, for operation in a cell with shared spectrum channel access; otherwise 0 bit. One or more entries from Table 7.3.1.2.2-6 or Table 7.3.1.2.2-6A are configured by the higher layer parameter *ul-AccessConfigListDCI-1-1.*

< Unchanged parts are omitted >

Table 7.3.1.2.2-6: Allowed entries for DCI format 1\_1, configured by higher layer parameter *ul-AccessConfigListDCI-1-1* in frequency range 1

|  |  |  |
| --- | --- | --- |
| **Entry index** | **Channel Access Type** | **The CP extension Text index defined in Clause 5.3.1 of [4, TS 38.211]** |
| 0 | Type2C-ULChannelAccess  defined in [clause 4.2.1.2.3 in 37.213] | 0 |
| 1 | Type2C-ULChannelAccess  defined in [clause 4.2.1.2.3 in 37.213] | 2 |
| 2 | Type2B-ULChannelAccess  defined in [clause 4.2.1.2.3 in 37.213] | 0 |
| 3 | Type2B-ULChannelAccess  defined in [clause 4.2.1.2.3 in 37.213] | 2 |
| 4 | Type2A-ULChannelAccess defined in [clause 4.2.1.2.1 in 37.213] | 0 |
| 5 | Type2A-ULChannelAccess defined in [clause 4.2.1.2.1 in 37.213] | 1 |
| 6 | Type2A-ULChannelAccess defined in [clause 4.2.1.2.1 in 37.213] | 3 |
| 7 | Type1-ULChannelAccess defined in [clause 4.2.1.1 in 37.213] | 0 |
| 8 | Type1-ULChannelAccess defined in [clause 4.2.1.1 in 37.213] | 1 |
| 9 | Type1-ULChannelAccess defined in [clause 4.2.1.1 in 37.213] | 2 |
| 10 | Type1-ULChannelAccess defined in [clause 4.2.1.1 in 37.213] | 3 |

Table 7.3.1.2.2-6A: Allowed entries for DCI format 1\_1, configured by higher layer parameter *ul-AccessConfigListDCI-1-1* in frequency range 2-2

|  |  |
| --- | --- |
| **Entry index** | **Channel Access Type** |
| 0 | Type 1 channel access defined in clause 4.4.1 of 37.213 |
| 1 | Type 2 channel access defined in clause 4.4.2 of 37.213 |
| 2 | Type 3 channel access defined in clause 4.4.3 of 37.213 |