**3GPP TSG-RAN WG1 Meeting #107-e *R1-21xxxxx***

**e-Meeting, November 11–19, 2021**

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| *CR-Form-v12.1* |
| **DRAFT CHANGE REQUEST** |
|  |
|  | **38.212** | **CR** |  | **rev** | **-** | **Current version:** | **16.7.0** |  |
|  |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* |
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| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network | **X** | Core Network |  |

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| ***Title:***  | Introduction of NR dynamic spectrum sharing  |
|  |  |
| ***Source to WG:*** | Huawei |
| ***Source to TSG:*** | R1 |
|  |  |
| ***Work item code:*** | NR\_DSS-Core |  | ***Date:*** | 2021-11-29 |
|  |  |  |  |  |
| ***Category:*** | B |  | ***Release:*** | Rel-17 |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)…Rel-15 (Release 15)Rel-16 (Release 16)Rel-17 (Release 17)Rel-18 (Release 18)* |
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| ***Reason for change:*** | Inclusion of NR dynamic spectrum sharing. |
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| ***Summary of change:*** | Support of NR dynamic spectrum sharing. |
|  |  |
| ***Consequences if not approved:*** | NR dynamic spectrum sharing in Rel-17 will be incomplete. |
|  |  |
| ***Clauses affected:*** | 7.3.1.1.2, 7.3.1.1.3, 7.3.1.2.2, 7.3.1.2.3 |
|  |  |
|  | **Y** | **N** |  |  |
| ***Other specs*** | **X** |  |  Other core specifications  | TS 38.213  |
| ***affected:*** |  | **X** |  Test specifications |  |
| ***(show related CRs)*** |  | **X** |  O&M Specifications |  |
|  |  |
| ***Other comments:*** |  |
|  |  |
| ***This CR's revision history:*** |  |

##### 7.3.1.1.2 Format 0\_1

DCI format 0\_1 is used for the scheduling of one or multiple PUSCH in one cell, or indicating CG downlink feedback information (CG-DFI) to a UE.

The following information is transmitted by means of the DCI format 0\_1 with CRC scrambled by C-RNTI or CS-RNTI or SP-CSI-RNTI or MCS-C-RNTI:

- Identifier for DCI formats – 1 bit

- The value of this bit field is always set to 0, indicating an UL DCI format

- Carrier indicator – 0 or 3 bits, as defined in Clause 10.1 of [5, TS38.213]. This field is reserved when this format is carried by PDCCH on the primary cell and the UE is configured for scheduling on the primary cell from an SCell, with the same number of bits as that in this format carried by PDCCH on the SCell for scheduling on the primary cell.

- DFI flag – 0 or 1 bit

- 1 bit if the UE is configured to monitor DCI format 0\_1 with CRC scrambled by CS-RNTI and for operation in a cell with shared spectrum channel access. For a DCI format 0\_1 with CRC scrambled by CS-RNTI, the bit value of 0 indicates activating or releasing type 2 CG transmission and the bit value of 1 indicates CG-DFI. For a DCI format 0\_1 with CRC scrambled by C-RNTI/SP-CSI-RNTI/MCS-C-RNTI and for operation in a cell with shared spectrum channel access, the bit is reserved.

- 0 bit otherwise;

If DCI format 0\_1 is used for indicating CG-DFI, all the remaining fields are set as follows:

- HARQ-ACK bitmap – 16 bits , where the order of the bitmap to HARQ process index mapping is such that HARQ process indices are mapped in ascending order from MSB to LSB of the bitmap. For each bit of the bitmap, value 1 indicates ACK, and value 0 indicates NACK.

- TPC command for scheduled PUSCH – 2 bits as defined in Clause 7.1.1 of [5, TS38.213]

- All the remaining bits in format 0\_1 are set to zero.

Otherwise, all the remaining fields are set as follows:

- UL/SUL indicator – 0 bit for UEs not configured with *supplementaryUplink* in *ServingCellConfig* in the cell or UEs configured with *supplementaryUplink* in *ServingCellConfig* in the cell but only one carrier in the cell is configured for PUSCH transmission; otherwise, 1 bit as defined in Table 7.3.1.1.1-1.

- Bandwidth part indicator – 0, 1 or 2 bits as determined by the number of UL BWPs  configured by higher layers, excluding the initial UL bandwidth part. The bitwidth for this field is determined as bits, where

-  if , in which case the bandwidth part indicator is equivalent to the ascending order of the higher layer parameter *BWP-Id*;

- otherwise , in which case the bandwidth part indicator is defined in Table 7.3.1.1.2-1;

If a UE does not support active BWP change via DCI, the UE ignores this bit field.

< Unchanged parts are omitted >

##### 7.3.1.1.3 Format 0\_2

DCI format 0\_2 is used for the scheduling of PUSCH in one cell.

The following information is transmitted by means of the DCI format 0\_2 with CRC scrambled by C-RNTI or CS-RNTI or SP-CSI-RNTI or MCS-C-RNTI:

- Identifier for DCI formats – 1 bit

- The value of this bit field is always set to 0, indicating an UL DCI format

- Carrier indicator – 0, 1, 2 or 3 bits determined by higher layer parameter *carrierIndicatorSizeDCI-0-2*, as defined in Clause 10.1 of [5, TS38.213]. This field is reserved when this format is carried by PDCCH on the primary cell and the UE is configured for scheduling on the primary cell from an SCell, with the same number of bits as that in this format carried by PDCCH on the SCell for scheduling on the primary cell.

- UL/SUL indicator – 0 bit for UEs not configured with *supplementaryUplink* in *ServingCellConfig* in the cell or UEs configured with *supplementaryUplink* in *ServingCellConfig* in the cell but only one carrier in the cell is configured for PUSCH transmission; otherwise, 1 bit as defined in Table 7.3.1.1.1-1.

- Bandwidth part indicator – 0, 1 or 2 bits as determined by the number of UL BWPs configured by higher layers, excluding the initial UL bandwidth part. The bitwidth for this field is determined as bits, where

- if , in which case the bandwidth part indicator is equivalent to the ascending order of the higher layer parameter *BWP-Id*;

- otherwise , in which case the bandwidth part indicator is defined in Table 7.3.1.1.2-1;

If a UE does not support active BWP change via DCI, the UE ignores this bit field.

- Frequency domain resource assignment – number of bits determined by the following:

- bits if only resource allocation type 0 is configured, where is defined in Clause 6.1.2.2.1 of [6, TS 38.214]

- bits if only resource allocation type 1 is configured, or bits if *resourceAllocationDCI-0-2-r16* is configured as '*dynamicSwitch'*, where is the size of the active UL bandwidth part, is defined as in clause 4.4.4.4 of [4, TS 38.211] and is given by higher layer parameter *resourceAllocationType1GranularityDCI-0-2.* If the higher layer parameter *resourceAllocationType1GranularityDCI-0-2* is not configured, is equal to 1.

- If *resourceAllocationDCI-0-2-r16* is configured as '*dynamicSwitch'*, the MSB bit is used to indicate resource allocation type 0 or resource allocation type 1, where the bit value of 0 indicates resource allocation type 0 and the bit value of 1 indicates resource allocation type 1.

- For resource allocation type 0, the LSBs provide the resource allocation as defined in Clause 6.1.2.2.1 of [6, TS 38.214].

- For resource allocation type 1, the LSBs provide the resource allocation as follows:

- For PUSCH hopping with resource allocation type 1:

- MSB bits are used to indicate the frequency offset according to Clause 6.3 of [6, TS 38.214], where if the higher layer parameter *frequencyHoppingOffsetListsDCI-0-2* contains two offset values and if the higher layer parameter *frequencyHoppingOffsetListsDCI-0-2* contains four offset values

- bits provide the frequency domain resource allocation according to Clause 6.1.2.2.2 of [6, TS 38.214]

- For non-PUSCH hopping with resource allocation type 1:

- bits provide the frequency domain resource allocation according to Clause 6.1.2.2.2 of [6, TS 38.214]

If "Bandwidth part indicator" field indicates a bandwidth part other than the active bandwidth part and if *resourceAllocationDCI-0-2-r16* is configured as '*dynamicSwitch'* for the indicated bandwidth part, the UE assumes resource allocation type 0 for the indicated bandwidth part if the bitwidth of the "Frequency domain resource assignment" field of the active bandwidth part is smaller than the bitwidth of the "Frequency domain resource assignment" field of the indicated bandwidth part.

< Unchanged parts are omitted >

##### 7.3.1.2.2 Format 1\_1

DCI format 1\_1 is used for the scheduling of PDSCH in one cell.

The following information is transmitted by means of the DCI format 1\_1 with CRC scrambled by C-RNTI or CS-RNTI or MCS-C-RNTI:

- Identifier for DCI formats – 1 bits

- The value of this bit field is always set to 1, indicating a DL DCI format

- Carrier indicator – 0 or 3 bits as defined in Clause 10.1 of [5, TS 38.213]. This field is reserved when this format is carried by PDCCH on the primary cell and the UE is configured for scheduling on the primary cell from an SCell, with the same number of bits as that in this format carried by PDCCH on the SCell for scheduling on the primary cell.

- Bandwidth part indicator – 0, 1 or 2 bits as determined by the number of DL BWPs  configured by higher layers, excluding the initial DL bandwidth part. The bitwidth for this field is determined as bits, where

-  if , in which case the bandwidth part indicator is equivalent to the ascending order of the higher layer parameter *BWP-Id*;

- otherwise , in which case the bandwidth part indicator is defined in Table 7.3.1.1.2-1;

If a UE does not support active BWP change via DCI, the UE ignores this bit field.

- Frequency domain resource assignment – number of bits determined by the following, where  is the size of the active DL bandwidth part:

-  bits if only resource allocation type 0 is configured, where  is defined in Clause 5.1.2.2.1 of [6, TS38.214],

- bits if only resource allocation type 1 is configured, or

-  bits if *resourceAllocation* is configured as '*dynamicSwitch'*.

- If *resourceAllocation* is configured as '*dynamicSwitch'*, the MSB bit is used to indicate resource allocation type 0 or resource allocation type 1, where the bit value of 0 indicates resource allocation type 0 and the bit value of 1 indicates resource allocation type 1.

- For resource allocation type 0, the LSBs provide the resource allocation as defined in Clause 5.1.2.2.1 of [6, TS 38.214].

- For resource allocation type 1, the  LSBs provide the resource allocation as defined in Clause 5.1.2.2.2 of [6, TS 38.214]

If "Bandwidth part indicator" field indicates a bandwidth part other than the active bandwidth part and if *resourceAllocation* is configured as '*dynamicSwitch'* for the indicated bandwidth part, the UE assumes resource allocation type 0 for the indicated bandwidth part if the bitwidth of the "Frequency domain resource assignment" field of the active bandwidth part is smaller than the bitwidth of the "Frequency domain resource assignment" field of the indicated bandwidth part.

< Unchanged parts are omitted >

##### 7.3.1.2.3 Format 1\_2

DCI format 1\_2 is used for the scheduling of PDSCH in one cell.

The following information is transmitted by means of the DCI format 1\_2 with CRC scrambled by C-RNTI or CS-RNTI or MCS-C-RNTI:

- Identifier for DCI formats – 1 bits

- The value of this bit field is always set to 1, indicating a DL DCI format.

- Carrier indicator – 0, 1, 2 or 3 bits determined by higher layer parameter *carrierIndicatorSizeDCI-1-2*, as defined in Clause 10.1 of [5, TS38.213]. This field is reserved when this format is carried by PDCCH on the primary cell and the UE is configured for scheduling on the primary cell from an SCell, with the same number of bits as that in this format carried by PDCCH on the SCell for scheduling on the primary cell.

- Bandwidth part indicator – 0, 1 or 2 bits as determined by the number of DL BWPs configured by higher layers, excluding the initial DL bandwidth part. The bitwidth for this field is determined as bits, where

- if , in which case the bandwidth part indicator is equivalent to the ascending order of the higher layer parameter *BWP-Id*;

- otherwise , in which case the bandwidth part indicator is defined in Table 7.3.1.1.2-1;

If a UE does not support active BWP change via DCI, the UE ignores this bit field.

- Frequency domain resource assignment – number of bits determined by the following:

- bits if only resource allocation type 0 is configured, where is defined in Clause 5.1.2.2.1 of [6, TS 38.214];

- bits if only resource allocation type 1 is configured, or bits if *resourceAllocationDCI-1-2-r16* is configured as '*dynamicSwitch'*, where , is the size of the active DL bandwidth part, is defined as in clause 4.4.4.4 of [4, TS 38.211] and is determined by higher layer parameter *resourceAllocationType1GranularityDCI-1-2*. If the higher layer parameter *resourceAllocationType1GranularityDCI-1-2* is not configured, is equal to 1.

- If *resourceAllocationDCI-1-2-r16* is configured as '*dynamicSwitch'*, the MSB bit is used to indicate resource allocation type 0 or resource allocation type 1, where the bit value of 0 indicates resource allocation type 0 and the bit value of 1 indicates resource allocation type 1.

- For resource allocation type 0, the LSBs provide the resource allocation as defined in Clause 5.1.2.2.1 of [6, TS 38.214].

- For resource allocation type 1, the LSBs provide the resource allocation as defined in Clause 5.1.2.2.2 of [6, TS 38.214]

If "Bandwidth part indicator" field indicates a bandwidth part other than the active bandwidth part and if *resourceAllocationDCI-1-2-r16* is configured as '*dynamicSwitch'* for the indicated bandwidth part, the UE assumes resource allocation type 0 for the indicated bandwidth part if the bitwidth of the "Frequency domain resource assignment" field of the active bandwidth part is smaller than the bitwidth of the "Frequency domain resource assignment" field of the indicated bandwidth part.

 < Unchanged parts are omitted >