Companies are to share their inputs on the excel spreadsheet in /tsg\_ran/WG1\_RL1/TSGR1\_106b-e/Inbox/drafts/8.1.2.4/RRC parameters/ herein.

## Inputs on version 00

Please share your inputs, if any, in the following table

Table 1 Inputs: Initial version

|  |  |
| --- | --- |
| **Company** | **Input** |
| ZTE | * One new RRC parameter should be introduced to enable UE identifying two QCL-TypeD properties for multiple overlapping CORESETs * The third row for simultaneousTCI-UpdateList1, simultaneousTCI-UpdateList2 is not needed as we have agreed the Rel-16 ones are reused |
| CATT | Agree with ZTE that if UE reports capability of monitoring two QCL-TypeD properties for multiple overlapping CORESETs, one new RRC parameter should be introduced to enable UE identifying two QCL-TypeD properties. |
| Moderator | RRC list is updated according to current agreements / working assumption.  A few comments:  1. @CATT / ZTE: Since we don’t have agreement on new RRC parameter, could be you please clarify necessity of the new RRC parameter to enable UE identifying two QCL-TypeD properties? It seems indication of the two TCI state for CORESET and UE capability may be sufficient condition to define correspodngin UE behaviour as well.  2. @ZTE, since simultaneousTCI-UpdateList1, simultaneousTCI-UpdateList2 parameters are only described in RAN2 spec, we should include them in the list as the existing parameters, but with new UE behaviour. With this information RAN2 can update the description in their specs to also include CORESETs. |
| ZTE2 | @Moderator We have agreed to introduce a new UE capability for identifying two QCL-TypeD, some UE may not support this feature. For UEs with such new capability, gNB should be able to control UE behavior either is legacy or new behavior.  For our second comment, we are fine with Moderator’s explanation. |
| Moderator | @ZTE  Thanks for clarification. The intention to have better gNB control is understood, but it would be good to clarify the scenario/benefits of supporting scenario when NW inidctaes CORESET with two TCI states, but due to some reasons rely on legacy behaviour even UE is capable of identifying two QCL-TypeD? |
| ZTE3 | @Moderator for example, for UE power saving. If gNB configures single QCL-TypeD, UE can turn off one panel. |
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## Inputs on version xx

Please share your inputs, if any, in the following table

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