Companies are to share their inputs on the excel spreadsheet in /tsg\_ran/WG1\_RL1/TSGR1\_106b-e/Inbox/drafts/8.1.2.2/RRC parameters/ herein.

## Inputs on version 00

Please share your inputs, if any, in the following table

Table 1 Inputs: Initial version

|  |  |
| --- | --- |
| **Company** | **Input** |
| Apple | AdditionalPCIInfo: as commented in last meeting, we suggest we list all that we have agreed.  NumberOfAdditionalPCI: This is not needed. |
| Ericsson | **Most of this is up to RAN2 design, we have already sent them the list of agreement in an LS. So it should be clear that this is for info only and RAN2 is free to design ASN.1and include or exclude parameters. On** NumberOfAdditionalPCI, **it will be a value between 1 and 7 and needs to be signalled.** |
| Huawei, HiSilicon | **AdditionalPCIInfo:** As RAN1 hasn’t agreed on whether the previsouly mentioned information related to SSBs with PCI different from serving cell are explicitly or implicitly provided (e.g., reusing those in configured MO) to UE and the specific design is actually up to RAN2, we suggest putting this parameter in brackets.  **NumberOfAdditionalPCI:** We are not sure if this is really needed. Note that this is RRC parameter, not UE feature. As long as PCI different from serving cell is somehow signaled to UE (also left to RAN2), the UE know how many PCI(s) different from serving cell are configured. We haven’t idenfitied the need to explicitly indicate the number of PCIs different from serving cell. |
| QC | NumberOfAdditionalPCI: We do not think this is needed. The number may not need to be configured as a RRC parameter. |
| ZTE | We are okay with both AdditionalPCIInfo and NumberOfAdditionalPCI.  We suggest to add one parameter of the new indicator/signalling as we have agreed in #106-e.  **Agreement**  Introduce a new RRC indicator/signalling (e.g., re-index the non-serving cell) to indicate the non-serving cell information that a TCI state/QCL information is associated with, where the new indicator/signaling is not the exact PCI value   * Detailed signalling design is up to RAN2 |
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## Inputs on version xx

Please share your inputs, if any, in the following table

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