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Source: WG3

To: TSG RAN WG1

Cc: TSG RAN WG2

Title: Response LS on DSCH TFCI Split Mode

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RAN3 would like to thank RAN1 for the LS (R3-010314_R1-010172) on DSCH TFCI Split Mode.

RAN1 asked RAN3 to confirm RAN1's understanding that

- 1. Hard split supports only up to 32 TFCs for DCHs and DSCHs, separately,
- 2. Logical split cannot support transmitting DSCH over Iur.

RAN3 would like to confirm that RAN1's understanding is correct on both issues 1 and 2.

To avoid any misunderstanding, RAN3 would like to clarify that use of the TFCI logical split over Iur is supported in R99, as long as the DSCH is located in a cell under the control of the SRNC. In the R99 specifications, usage of the logical split in combination with DSCH scheduling under a DRNC is not supported.

RAN1 also asked if it is worth RAN1's studying the enhancement on hard split to support variable bit length TFCI for DCHs and DSCHs as a Rel. 5 issue.

Given the above limitation, the proposed enhancement would enable more flexibility in the TFCI2 lengths in the case of a DSCH scheduled by a DRNS. In this respect, the proposed study would bring some functional benefit.