

Agenda Item: 4
Source: Philips
Title: Correction to TS25.214 (R99) for UL power control in compressed mode
Document for: Decision

Introduction

The attached CR makes a correction to the corrections which were agreed in CR25.214-140 [1] at RAN WG1 #17. The sense of the intention of that CR is not changed, but the terminology is clarified following discussion on the email reflector:

The term Δ_{PILOT} is now only used to indicate the magnitude of a required *step* in uplink transmit power. Δ_{PILOT} is defined every slot and may take a non-zero value at the start and end of each compressed frame (i.e. a frame containing a transmission gap). Δ_{PILOT} would typically take a positive value at the start of a compressed frame and a negative value at the end.

When calculating the SIR target to be used by the Node B during uplink compressed frames, an offset corresponding in magnitude to Δ_{PILOT} has to be applied to the SIR target. However, this is described in terms of an offset which must be applied *throughout* the compressed frame, and not in terms of a step applied at the beginning and end of a compressed frame. Furthermore, this offset is relative to a normal, non-compressed frame, and may not be equal to the size of the Δ_{PILOT} step at the beginning of the frame (e.g. in the case of two consecutive compressed frames containing different slot formats.) This offset is therefore now described in the attached CR as $\Delta_{SIR_{PILOT}}$, and is defined separately from Δ_{PILOT} .

Additionally, in order to make clear when exactly the SIR_{cm_target} applies, the description of compressed mode is improved.

Reference

[1] 3GPP R1-00-1400, "CR 25.214-140: uplink power control in compressed mode", Alcatel, November 2000

5.1.2.3 Transmit power control in compressed mode

In compressed mode, one or more transmission gap patterns are active. Therefore some frames are compressed and contain transmission gaps. The uplink power control procedure is as specified in clause 5.1.2.2, using the same UTRAN supplied parameters for Power Control Algorithm and step size (Δ_{TPC}), but with additional features which aim to recover as rapidly as possible a signal-to-interference ratio (SIR) close to the target SIR after each transmission gap.

The serving cells (cells in the active set) should estimate signal-to-interference ratio SIR_{est} of the received uplink DPCCH. The serving cells should then generate TPC commands and transmit the commands once per slot, except during downlink transmission gaps, according to the following rule: if $\text{SIR}_{\text{est}} > \text{SIR}_{\text{cm_target}}$ then the TPC command to transmit is "0", while if $\text{SIR}_{\text{est}} < \text{SIR}_{\text{cm_target}}$ then the TPC command to transmit is "1".

$\text{SIR}_{\text{cm_target}}$ is the target SIR during compressed mode and fulfils

$$\text{SIR}_{\text{cm_target}} = \text{SIR}_{\text{target}} + \Delta_{\text{SIR_PILOT}} + \Delta_{\text{SIR1_coding}} + \Delta_{\text{SIR2_coding}},$$

where $\Delta_{\text{SIR1_coding}}$ and $\Delta_{\text{SIR2_coding}}$ are computed from uplink parameters DeltaSIR1, DeltaSIR2, DeltaSIRafter1, DeltaSIRafter2 signalled by higher layers as:

- $\Delta_{\text{SIR1_coding}} = \text{DeltaSIR1}$ if the start of the first transmission gap in the transmission gap pattern is within the current uplink frame.
- $\Delta_{\text{SIR1_coding}} = \text{DeltaSIRafter1}$ if the current uplink frame just follows a frame containing the start of the first transmission gap in the transmission gap pattern.
- $\Delta_{\text{SIR2_coding}} = \text{DeltaSIR2}$ if the start of the second transmission gap in the transmission gap pattern is within the current uplink frame.
- $\Delta_{\text{SIR2_coding}} = \text{DeltaSIRafter2}$ if the current uplink frame just follows a frame containing the start of the second transmission gap in the transmission gap pattern.
- $\Delta_{\text{SIR1_coding}} = 0$ dB and $\Delta_{\text{SIR2_coding}} = 0$ dB in all other cases.

And $\Delta_{\text{SIR_PILOT}}$ is defined below as: $\Delta_{\text{SIR_PILOT}} = 10 \log_{10} (N_{\text{pilot,N}} / N_{\text{pilot,curr_frame}})$

where $N_{\text{pilot,curr_frame}}$ is the number of pilot bits per slot in the current uplink frame, and $N_{\text{pilot,N}}$ is the number of pilot bits per slot in a normal uplink frame without a transmission gap.

In the case of several compressed mode patterns are being used simultaneously, $\Delta_{\text{SIR1_coding}}$ and $\Delta_{\text{SIR2_coding}}$ offsets are computed for each compressed mode pattern and all $\Delta_{\text{SIR1_coding}}$ and $\Delta_{\text{SIR2_coding}}$ offsets are summed together.

In compressed mode, compressed frames may occur in either the uplink or the downlink or both. In uplink compressed frames, the transmission of uplink DPDCH(s) and DPCCH shall both be stopped during transmission gaps.

Due to the transmission gaps in compressed frames, there may be missing TPC commands in the downlink. If no downlink TPC command is transmitted, the corresponding TPC_cmd derived by the UE shall be set to zero.

Compressed and non-compressed frames in the uplink DPCCH may have a different number of pilot bits per slot. A change in the transmit power of the uplink DPCCH would be needed in order to compensate for the change in the total pilot energy. Therefore at the start of each slot the UE shall derive the value of a power offset Δ_{PILOT} . If the number of pilot bits per slot in the uplink DPCCH is different from its value in the most recently transmitted slot, Δ_{PILOT} (in dB) shall be given by:

$$\Delta_{\text{PILOT}} = 10 \log_{10} (N_{\text{pilot,prev}} / N_{\text{pilot,curr}});$$

where $N_{\text{pilot,prev}}$ is the number of pilot bits in the most recently transmitted slot, and $N_{\text{pilot,curr}}$ is the number of pilot bits in the current slot. Otherwise, including during transmission gaps in the downlink, Δ_{PILOT} shall be zero.

Unless otherwise specified, in every slot during compressed mode the UE shall adjust the transmit power of the uplink DPCCH with a step of Δ_{DPCCH} (in dB) which is given by:

$$\Delta_{\text{DPCCH}} = \Delta_{\text{TPC}} + \text{TPC_cmd} + \Delta_{\text{PILOT}}.$$

At the start of the first slot after an uplink or downlink transmission gap the UE shall apply a change in the transmit power of the uplink DPCCH by an amount Δ_{DPCCH} (in dB), with respect to the uplink DPCCH power in the most recently transmitted uplink slot, where:

$$\Delta_{\text{DPCCH}} = \Delta_{\text{RESUME}} + \Delta_{\text{PILOT}}$$

The value of Δ_{RESUME} (in dB) shall be determined by the UE according to the Initial Transmit Power mode (ITP). The ITP is a UE specific parameter, which is signalled by the network with the other compressed mode parameters (see [4]). The different modes are summarised in table 1.

Table 1: Initial Transmit Power modes during compressed mode

Initial Transmit Power mode	Description
0	$\Delta_{\text{RESUME}} = \Delta_{\text{TPC}} \Delta_{\text{TPC_cmd_gap}}$
1	$\Delta_{\text{RESUME}} = \Delta_{\text{last}}$

In the case of a transmission gap in the uplink, $\Delta_{\text{TPC_cmd_gap}}$ shall be the value of $\Delta_{\text{TPC_cmd}}$ derived in the first slot of the uplink transmission gap, if a downlink TPC command is transmitted in that slot. Otherwise $\Delta_{\text{TPC_cmd_gap}}$ shall be zero.

Δ_{last} shall be equal to the most recently computed value of Δ_i . Δ_i shall be updated according to the following recursive relations, which shall be executed in all slots in which both the uplink DPCCH and a downlink TPC command are transmitted, and in the first slot of an uplink transmission gap if a downlink TPC command is transmitted in that slot:

$$\Delta_i = 0.9375 \Delta_{i-1} + 0.0625 \Delta_{\text{TPC_cmd}_i} k_{sc}$$

where: $\Delta_{\text{TPC_cmd}_i}$ is the power control command derived by the UE in that slot;

$k_{sc} = 0$ if additional scaling is applied in the current slot and the previous slot as described in sub-clause 5.1.2.6, and $k_{sc} = 1$ otherwise.

Δ_{i-1} is the value of Δ_i computed for the previous slot. The value of Δ_{i-1} shall be initialised to zero when the uplink DPCCH is activated, and also at the end of the first slot after each uplink transmission gap, and also at the end of the first slot after each downlink transmission gap. The value of Δ_i shall be set to zero at the end of the first slot after each uplink transmission gap.

After a transmission gap in either the uplink or the downlink, the period following resumption of simultaneous uplink and downlink DPCCH transmission is called a recovery period. RPL is the recovery period length and is expressed as a number of slots. RPL is equal to the minimum value out of the transmission gap length and 7 slots. If a transmission gap is scheduled to start before RPL slots have elapsed, then the recovery period shall end at the start of the gap, and the value of RPL shall be reduced accordingly.

During the recovery period, 2 modes are possible for the power control algorithm. The Recovery Period Power control mode (RPP) is signalled with the other compressed mode parameters (see [4]). The different modes are summarised in the table 2:

Table 2: Recovery Period Power control modes during compressed mode

Recovery Period power control mode	Description
0	Transmit power control is applied using the algorithm determined by the value of PCA, as in subclause 5.1.2.2 with step size Δ_{TPC} .
1	Transmit power control is applied using algorithm 1 (see subclause 5.1.2.2.2) with step size $\Delta_{\text{RP-TPC}}$ during RPL slots after each transmission gap.

For RPP mode 0, the step size is not changed during the recovery period and ordinary transmit power control is applied (see subclause 5.1.2.2), using the algorithm for processing TPC commands determined by the value of PCA (see subclauses 5.1.2.2.2 and 5.1.2.2.3).

For RPP mode 1, during RPL slots after each transmission gap, power control algorithm 1 is applied with a step size Δ_{RP-TPC} instead of Δ_{TPC} , regardless of the value of PCA. Therefore, the change in uplink DPCCH transmit power at the start of each of the RPL+1 slots immediately following the transmission gap (except for the first slot after the transmission gap) is given by:

$$\Delta_{DPCCH} = \Delta_{RP-TPC} \Delta_{TPC_cmd} + \Delta_{PILOT}$$

Δ_{RP-TPC} is called the recovery power control step size and is expressed in dB. If PCA has the value 1, Δ_{RP-TPC} is equal to the minimum value of 3 dB and $2\Delta_{TPC}$. If PCA has the value 2, Δ_{RP-TPC} is equal to 1 dB.

After the recovery period, ordinary transmit power control resumes using the algorithm specified by the value of PCA and with step size Δ_{TPC} .

If PCA has the value 2, the sets of slots over which the TPC commands are processed shall remain aligned to the frame boundaries in the compressed frame. For both RPP mode 0 and RPP mode 1, if the transmission gap or the recovery period results in any incomplete sets of TPC commands, Δ_{TPC_cmd} shall be zero for those sets of slots which are incomplete.