3GPP TSG RAN WG1#17 Stockholm, Sweden, 21-24 Nov 2000

3GPP TSG RAN WG1#17 Stockholm, Sweden, 21-24 Nov 2000							Document e.g. fc or t	R1-00-1 or 3GPP use the formation SMG, use the formation	446 t TP-99xxx at P-99-xxx
			CHANGE	REQI	JEST	 Please page for 	see embedded help r instructions on ho	o file at the bottom o w to fill in this form	of this correctly.
			25.212	CR	101		Current Vers	sion: 3.4.0	
GSM (AA.BB) or S	3G (J	AA.BBB) specific	ation number ?		?	CR number a	as allocated by MC	C support team	
For submission to: RAN#10 list expected approval meeting # here ?			for approval X for information			is form is availa	strategic (for SMG non-strategic use only)		
Proposed change affects: (U)SIM ME X UTRAN / Radio X Core Network									
<u>Source:</u>		Mitsubishi I	Electric (Trium RD)			Date	<u>21-Nov-20</u>	00
Subject:		Correction	to code block seg	mentatio	n				
Work item:		Release 99							
Category: (only one category shall be marked with an X)	F A B C D	CorrectionXRelease:Phase 2Corresponds to a correction in an earlier releaseRelease 96Release 96Addition of featureRelease 97Release 97Functional modification of featureRelease 98Release 98Editorial modificationRelease 00Release 00							X
<u>Reason for</u> change:		Useless "er	nd if" in the code b	olock seg	gmentati	on algorit	thm		
Clauses affected: 4.2.2.2									
Other specs affected:		Other 3G core specifications Other GSM core specifications MS test specifications BSS test specifications O&M specifications			 List c List c List c List c List c List c 	of CRs: of CRs: of CRs: of CRs: of CRs: of CRs:			
<u>Other</u> comments:									

<----- double-click here for help and instructions on how to create a CR.

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4.2.2.2 Code block segmentation

Segmentation of the bit sequence from transport block concatenation is performed if $X_i > Z$. The code blocks after segmentation are of the same size. The number of code blocks on TrCH *i* is denoted by C_i . If the number of bits input to the segmentation, X_i , is not a multiple of C_i , filler bits are added to the beginning of the first block. If turbo coding is selected and $X_i < 40$, filler bits are added to the beginning of the code block. The filler bits are transmitted and they are always set to 0. The maximum code block sizes are:

- convolutional coding: Z = 504;
- turbo coding: Z = 5114;
- no channel coding: *Z* = *unlimited*.

The bits output from code block segmentation are denoted by $o_{ir1}, o_{ir2}, o_{ir3}$, P_{irK_i} , where *i* is the TrCH number, *r* is the code block number, and K_i is the number of bits.

Number of code blocks: $C_i = ?X_i / Z?$

Number of bits in each code block:

if $X_i < 40$ and Turbo coding is used, then

```
K_i = 40
```

else

```
K_i = ?X_i / C_i?
```

end if

Number of filler bits: $Y_i = C_i K_i - X_i$

for k = 1 to Y_i -- Insertion of filler bits

```
o_{i1k} ? 0
```

end for

for $k = Y_i + 1$ to K_i

 o_{i1k} ? $x_{i,(k?Y_i)}$

end for

```
r = 2
```

-- Segmentation

while $r ? C_i$

for k = 1 to K_i

```
o_{irk} ? x_{i,(k?(r?1)?K_i?Y_i)I}
```

end for

r = r + 1

end while

end if