TSG-RAN Working Group 1 meeting#17

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Agenda Item:AH21Source:CWTS/CATTTo:TSG RAN WG1Title:Coding of transport formats combination indicator (TFCI)for QPSK of 1.28Mcps TDDDocument for:Discussion and Approval

1 Summary

Encoding of TFCI depends on its length and the modulation mode in use. When QPSK modulation is applied in 1.28Mcps TDD, the coding scheme for TFCI is the same æ that of 3.84Mcps TDD. The same TFCI length $(0,1,2,3\sim5,6\sim10)$ like 3.84Mcps TDD are supported, and the corresponding encoded number of bits is (0,4,8,16,32).

2 Proposal

It's proposed to discuss and include the following text proposal into the clause 4.4.1 Coding of transport format combination indicator (TFCI) for QPSK

------ Changes to working CR of 25.222 begin ------

4.4.1 Coding of transport format combination indicator (TFCI) for QPSK

4.4.1.1 Coding of long TFCI lengths

4.4.1.2 Coding of short TFCI lengths

4.4.1.2.1 Coding very short TFCIs by repetition

4.4.1.2.2 Coding short TFCIs using bi-orthogonal codes

The coding of TFCI for 1.28Mcps TDD when QPSK is applied is same as that of 3.84Mcps TDD.Cf.[4.3.1 'Coding of transport format combination indicator'].

4.4.1.13 Mapping of TFCI word

Denote the number of bits in the TFCI word by N_{TFCI} , and denote the code word bits by bk, where $k = 0, ..., N_{TFCI-1}$.

When the number of bits in the TFCI is 8,16,32, the mapping of the TFCI word to the TFCI bit positions shall be as follows.



Figure [X1]: Mapping of TFCI word bits to TFCI position in 1.28 Mcps TDD option. where $N = N_{TFCI}$.

The location of the 1st to 4th parts of TFCI in the timeslot is defined in [7].

When the number of bits in the TFCI is 4, the mapping of TFCI word to the TFCI bit position is that the TFCI bits are equally divided into two part for the consecutive two subframe and mapped onto the end of the first data field respectively. The 4 bits mapping is show as follows:

