TSG RAN WG1 #16 June 10-13, 2000 01129

Pusan, Korea

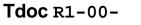
Source: TSG RAN WG1 Chairman

Report from TSG RAN#9 From Hawaii

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All Release -99 CRs Approved

 The CR 25.211-059 which was put on hold in RAN#8 was now rejected, no discussions on any of them



DPCCH gating

- (part of Terminal power saving features WI):
- The WG1 TR 25.840 was show to TSG RAN
 - The milestone was set to 03/01.
 - Guidance was given that focus should be put on the interference reduction aspects as well.
 - Other WGs expected to work on it



Radio Link Performance Enhancements (1)

- WI sheet for DSCH power control improvement in soft handover was agreed with the milestone set to 03/01
- TR 25.841 was presented for information



Radio Link Performance Enhancements (2)

- The study item was made as a permanent study item to be repeated for each Release.
- Other topics not presented for TSG RAN at this stage are for consideration for Release 5.
- Next milestone is 12/01
 - On a individual item conclusions much be reached naturally earlier if the target is Release 5, i.e. latest by RAN#13 (09/01) a work item would need to be created.



TDD Node B Synchronisation

- TDD Node B synchronisation WI
- The TR 25.836 was presented for information)
- The milestone was set to 03/01



Uplink Synchronous Transmission

- Uplink Synchronous Transmission study item
- The milestone for the study report is set for 03/01



1.28 Mcps UTRA TDD

- (Low Chip Rate UTRA TDD Physical Layer WI)
- TR 25.928 Provided for information
 - It was noted in TSG RAN that TR can not be considered finalised with potential issue on the slot/frame structure
 - Strong opinions were raised on the 1.28 Mchips/s UTRA TDD to be better aligned with 3.84 Mchips/s UTRA TDD
- TDD co-existence was discussed in TSG RAN
 - WG4 is tasked to study the issue (adjacent channel at least)
 - WG1 is to wait WG4 outcome before conclusions on the 1.28 Mcps slot/frame structure

Smart Antennas

- (Smart Antennas WI)
- TR 25.842 Provided for information
- The WI was modified to address the TDD specs as so far nothing new has been identified on FDD side



Other Topics

- Hybrid ARQ milestone shifted 06/01 for the TR.
 In WG1 TR is to be considered to cover issues like impacts to the channel coding and multiplexing chain
- Improved cell FACH state
 - Study report milestone set to 03/01, no action expected from WG1 at this point
- Positioning
 - RAN concluded that use of compressed mode with location specific measurements in not Release-99 issue



High Speed Downlink Packet Access (HSDPA) work allocation to RAN WGs

- RAN WG1:
 - **Adaptive Modulation and Coding**
 - **H-ARQ** link performance of different schemes
 - *∝*Frame size
 - Reverse control channel frame formats, need for multiple DPCH.
 - **Mathematical Implications on mobile station requirements.**
 - Simulation assumptions for link and system simulations.
- RAN WG2:
 - **Protocol architecture.**
 - **H-ARQ protocol, messaging, etc.**
 - **Fast cell selection.**

≈RAN WG4

*«***Implementation aspects of higher order modulation**



Annex 1. Coming RAN WG1 meetings

- WG1#17 November 21-24 (Sweden, Host: Ericsson)
- WG1#18 January 16-19 (Boston (tbc), USA, Host T1P1)
- WG1#19 February 27- March 2 (Host needed),
- WG1#20 May 21- 26 (Korea, Host Samsung)
- WG1#21 June 2001
- WG1#22 August 2001
- WG1#23 October 2001
- WG1#24 November 2001
- (potential physical ad hoc in April)

