TSG-RAN Working Group 1 AH21

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Agenda Item: AH21 **Source:** CWTS

To: TSG RAN WG1

Title: Example of multi-frame structure

Document for: Information and discussion

Introduction

In this paper, example of the multi-frame structure for CCPCH is described. The period for the CCPCH of the low chip rate TDD is the same as high chip rate TDD that is 720ms.

Conclusion

It is proposed to discuss and include the following text proposal in Annex B of chapter 7 in TR25.928.

----- changes to TR25.928 begin -----

4 Physical channels and mapping of transport channels onto physical channels

Annex B (Informative): CCPCH Multiframe Structure

Frame #	0 1	23	4 5	67	89	10		14	16		20	22	24	26	28	30	32	34	36	38	40	42	44	46	48	50									68	
						11	13	15	1/	19	21	23	25	21	29	31	33	33	3/	39	41	43	45	4/	49	41	53	55	5/	39	61	63	65	67	69	/1
P-CCPCH1 in TS 0, Code 0																																				
P-CCPCH2 in TS 0, Code 1																																				
BCH 2, 93kbps			I	PCE	I 8,7	9kb	ps			FACH 23,44 kbps																										

Figure B.1: Example for a multiframe structure for P-CCPCHs that is repeated every 72th frame (144 sub-frame)

Frame #	0 1	2 3	4 5	67	89	10 11											38 39					56 57			68 69	
S-CCPCH1 in TS k, Code i												I		I												
S-CCPCH2 in TS k, Code j																										
PCH 11,72kbps		FACH 23,44 kbps																								

Figure B.2: Example for a multiframe structure for S-CCPCHs that is repeated every 72th frame, I,j=1...16 (144 sub-frame)

The two codes at SF 16 which define the two P-(S)CCPCHs, are jointly used to deliver the same higher layer message! this means that it not possible to send contemporarily two different messages (e.g. BCH and PCH) onto the two different codes of the P-(S)CCPCHs.

----- changes to TR25.928 end -----