TSG-RAN Working Group 1 , AH 21   *TSGR1(00)0834*

Espoo, Finland, June 14-15, 2000

Agenda Item: AH21
Source: CWTS
To: TSG RAN WG1
Title: Dedicated Transport Channels
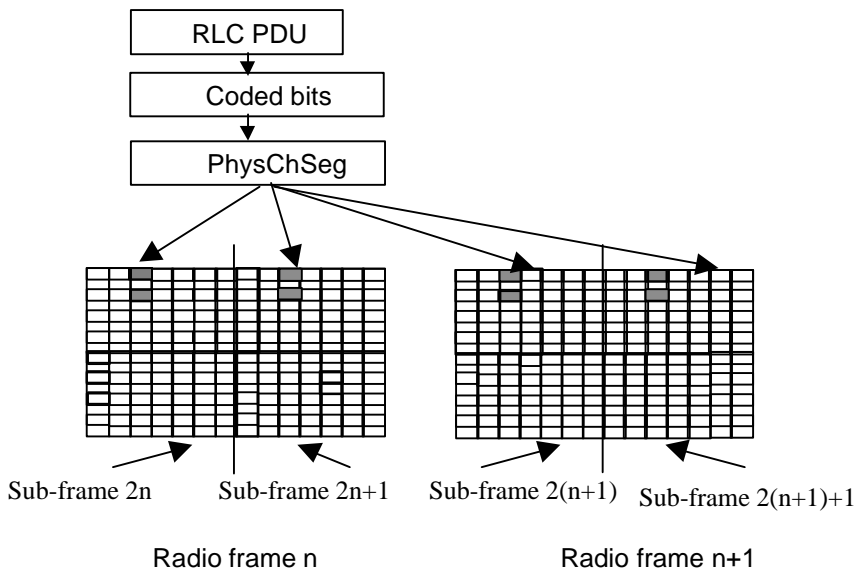**Document for:**   Discussion and Approval
_____

Introduction

This paper describes the dedicated transport channels for low chip rate TDD option.

Conclusion

It's proposed to discuss and include the following text proposal into the clause '7.3.1 Dedicated Transport Channels' in TR25.928.

---------------------------------- changes to TR25.928 begin ------------------------------------

7.3.1 Dedicated Transport Channels

Figure 1: Mapping of PDU onto the physical bearer(TTI= 20ms)

[Description:]

The figure shows the mapping of PDU onto physical bearer.

[Explanation difference:]

The mapping of PDU into dedicated channel is different from that of high chip rate TDD option according to the different frame structure. See separate sections (clause 8.1.11) for further explanation of the segmentation into sub-frames.

---------------------------------- changes to TR25.928 end ------------------------------------