3GPP TSG RAN WG1#11 **Tdoc R1-00-0300**
San Diego, USA
February 29th- March 3rd , 2000 **Meeting No. 11**
Agenda Item: Editorial CRs or AD Hoc 14.

Source: Motorola, Nokia

DSCH clarifications in 3GPP TS 25.213

Introduction.

This document presents two simple clarifications for the DSCH (or PDSCH) for the modulation section and for the scrambling code section.

- In the modulation section the PDSCH is not mentioned while the other channels are, thus as the modulation is identical to the other channels like P-CCPCH, the PDSCH is simply added to the list.
- For the scrambling code section the PDSCH is clarified to be under a single scrambling code as for a CCTrCH in general it has been stated that a CCTrCh may be under more than one scrambling code (with the multicode transmission) As this is not seen necessary for PDSCH with multicode transmission, a clarification is being added to the corresponding section as shown in the attached CR.for 25.213.

3GPP TSG RAN WG1 Meeting #11
San Diego, USA, 29.2-3.3 2000

***Document* R1-00-300**
e.g. for 3GPP use the format TP-99xxx
or for SMG, use the format P-99-xxx

CHANGE REQUEST

Please see embedded help file at the bottom of this page for instructions on how to fill in this form correctly.

25.213 **CR** **029** Current Version: 3.1.0

GSM (AA.BB) or 3G (AA.BBB) specification number ↑ *↑ CR number as allocated by MCC support team*

For submission to: RAN#7
list expected approval meeting # here ↑

for approval		strategic		*(for SMG use only)*
for information		non-strategic		

Form: CR cover sheet, version 2 for 3GPP and SMG *The latest version of this form is available from: ftp://ftp.3gpp.org/Information/CR-Form-v2.doc*

Proposed change affects: (U)SIM [ ] ME [X] UTRAN / Radio [X] Core Network [ ]
(at least one should be marked with an X)

Source: Motorola, Nokia **Date:** 29.2.2000

Subject: Clarifications to DSCH scrambling and modulation in 25.213

Work item:

Category:
(only one category shall be marked with an X)

F	Correction	
A	Corresponds to a correction in an earlier release	
B	Addition of feature	
C	Functional modification of feature	X
D	Editorial modification	

Release:

Phase 2	
Release 96	
Release 97	
Release 98	
Release 99	X
Release 00	

Reason for change: The DSCH is not mentioned in the modulation section of 25.213 and in connection with scrambling the CCTrCH has been given possibility for using several scrambling codes which is not necessary for multicode transmission with DSCH.

Clauses affected:

Other specs affected:

Other 3G core specifications		→ List of CRs:
Other GSM core specifications		→ List of CRs:
MS test specifications		→ List of CRs:
BSS test specifications		→ List of CRs:
O&M specifications		→ List of CRs:

Other comments:

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5 Downlink spreading and modulation

5.1 Spreading

Figure 8 illustrates the spreading operation for all downlink physical channels except SCH, i.e. for P-CCPCH, S-CCPCH, CPICH, AICH, PICH,PDSCH, and downlink DPCH. The non-spread physical channel consists of a sequence of real-valued symbols. For all channels except AICH, the symbols can take the three values +1, -1, and 0, where 0 indicates DTX. For AICH, the symbol values depend on the exact combination of acquisition indicators to be transmitted, compare [2] Section 5.3.3.6.

Each pair of two consecutive symbols is first serial-to-parallel converted and mapped to an I and Q branch. The mapping is such that even and odd numbered symbols are mapped to the I and Q branch respectively. For all channels except AICH, symbol number zero is defined as the first symbol in each frame. For AICH, symbol number zero is defined as the first symbol in each access slot. The I and Q branches are then spread to the chip rate by the same real-valued channelization code $C_{ch,SF,m}$. The sequences of real-valued chips on the I and Q branch are then treated as a single complex-valued sequence of chips. This sequence of chips is scrambled (complex chip-wise multiplication) by a complex-valued scrambling code $S_{dl,n}$. In case of P-CCPCH, the scrambling code is applied aligned with the P-CCPCH frame boundary, i.e. the first complex chip of the spread P-CCPCH frame is multiplied with chip number zero of the scrambling code. In case of other downlink channels, the scrambling code is applied aligned with the scrambling code applied to the P-CCPCH. In this case, the scrambling code is thus not necessarily applied aligned with the frame boundary of the physical channel to be scrambled.

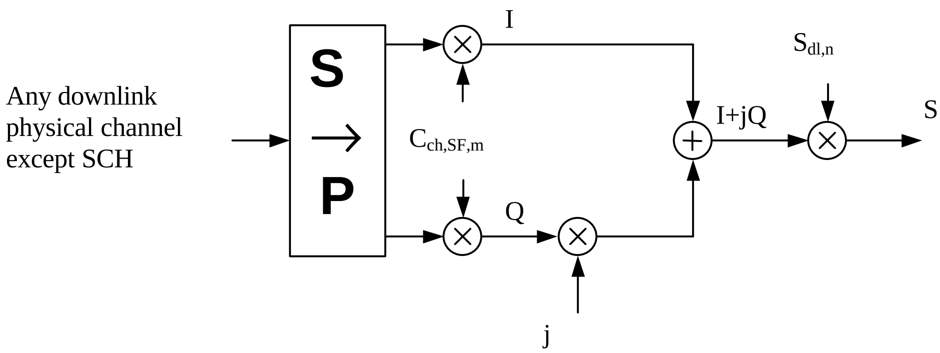

Figure 8: Spreading for all downlink physical channels except SCH

Figure 9 illustrates how different downlink channels are combined. Each complex-valued spread channel, corresponding to point S in Figure 8, is separately weighted by a weight factor G_i. The complex-valued P-SCH and S-SCH, as described in [1], section 5.3.3.4, are separately weighted by weight factors G_p and G_s. All downlink physical channels are then combined using complex addition.

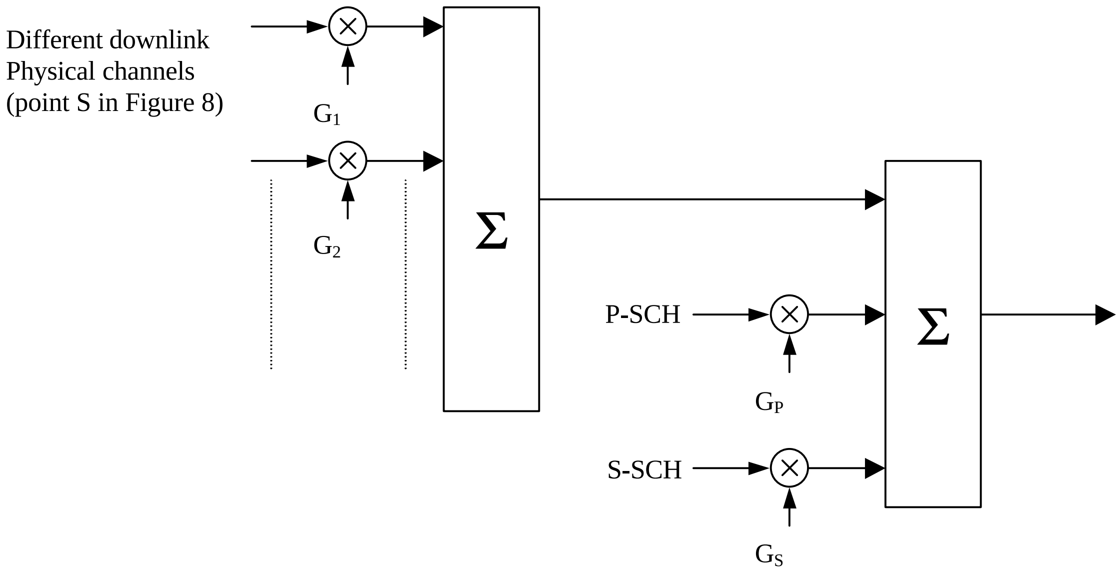

Figure 9: Spreading and modulation for SCH and P-CCPCH

5.2 Code generation and allocation

5.2.1 Channelization codes

The channelization codes of figure 8 are the same codes as used in the uplink, namely Orthogonal Variable Spreading Factor (OVSF) codes that preserve the orthogonality between downlink channels of different rates and spreading factors. The OVSF codes are defined in figure 4 in section 4.3.1.

The channelization code for the Primary CPICH is fixed to $C_{ch,256,0}$ and the channelization code for the Primary CCPCH is fixed to $C_{ch,256,1}$.The channelization codes for all other physical channels are assigned by UTRAN.

With the spreading factor 512 a specific restriction is applied. When the code word $C_{ch,512,n}$, with n=0,2,4….510, is used in soft handover, then the code word $C_{512,n+1}$ is not allocated in the Node Bs where timing adjustment is to be used. Respectively if $C_{ch,512,n}$, with n=1,3,5….511 is used, then the code word $C_{512,n-1}$ is not allocated in the Node B where timing adjustment is to be used. This restriction shall not apply for the softer handover operation or in case UTRAN is synchronised to such a level that timing adjustments in soft handover are not used with spreading factor 512.

When compressed mode is implemented by reducing the spreading factor by 2, the OVSF code used for compressed frames is:

- $C_{ch,SF/2,\lfloor n/2 \rfloor}$ if ordinary scrambling code is used
- $c_{ch,SF/2,n \bmod SF/2}$ if alternative scrambling code is used (see section 5.2.2)

where $c_{ch,SF,n}$ is the channelization code used for non-compressed frames.

In case the OVSF code on the PDSCH varies from frame to frame, the OVSF codes shall be allocated such a way that the OVSF code(s) below the smallest spreading factor will be from the branch of the code tree pointed by the smallest spreading factor used for the connection. This means that all the codes for UE for the PDSCH connection can be generated according to the OVSF code generation principle from smallest spreading factor code used by the UE on PDSCH.

In case of mapping the DSCH to multiple parallel PDSCHs, the same rule applies, but all of the branches identified by the multiple codes, corresponding to the smallest spreading factor, may be used for higher spreading factor allocation.

5.2.2 Scrambling code

A total of 2^{18}-1 = 262,143 scrambling codes, numbered 0…262,142 can be generated. However not all the scrambling codes are used. The scrambling codes are divided into 512 sets each of a primary scrambling code and 15 secondary scrambling codes.

The primary scrambling codes consist of scrambling codes n=16*i where i=0…511. The i:th set of secondary scrambling codes consists of scrambling codes 16*i+k, where k=1…15.

There is a one-to-one mapping between each primary scrambling code and 15 secondary scrambling codes in a set such that i:th primary scrambling code corresponds to i:th set of scrambling codes.

Hence, according to the above, scrambling codes k = 0, 1, …, 8191 are used. Each of these codes are associated with a left alternative scrambling code and a right alternative scrambling code, that may be used for compressed frames. The left alternative scrambling code corresponding to scrambling code k is scrambling code number k + 8192, while the right alternative scrambling code corresponding to scrambling code k is scrambling code number k + 16384. The alternative scrambling codes can be used for compressed frames. In this case, the left alternative scrambling code is used if n<SF/2 and the right alternative scrambling code is used if n≥SF/2, where $c_{ch,SF,n}$ is the channelization code used for non-compressed frames. The usage of alternative scrambling code for compressed frames is signalled by higher layers for each physical channel respectively.

The set of primary scrambling codes is further divided into 64 scrambling code groups, each consisting of 8 primary scrambling codes. The j:th scrambling code group consists of primary scrambling codes 16*8*j+16*k, where j=0..63 and k=0..7.

Each cell is allocated one and only one primary scrambling code. The primary CCPCH and primary CPICH are always transmitted using the primary scrambling code. The other downlink physical channels can be transmitted with either the primary scrambling code or a secondary scrambling code from the set associated with the primary scrambling code of the cell.

The mixture of primary scrambling code and secondary scrambling code for one CCTrCH is allowable. However, in the case of the CCTrCH of type DSCH then all the PDSCH channelisation codes that a single UE may receive shall be under a single scrambling code (either the primary or a secondary scrambling code).

The scrambling code sequences are constructed by combining two real sequences into a complex sequence. Each of the two real sequences are constructed as the position wise modulo 2 sum of 38400 chip segments of two binary *m*-sequences generated by means of two generator polynomials of degree 18. The resulting sequences thus constitute segments of a set of Gold sequences. The scrambling codes are repeated for every 10 ms radio frame. Let *x* and *y* be the two sequences respectively. The *x* sequence is constructed using the primitive (over GF(2)) polynomial $1+X^7+X^{18}$. The y sequence is constructed using the polynomial $1+X^5+X^7+X^{10}+X^{18}$.

The sequence depending on the chosen scrambling code number *n* is denoted z_n, in the sequel. Furthermore, let $x(i)$, $y(i)$ and $z_n(i)$ denote the *i*:th symbol of the sequence *x*, *y*, and z_n, respectively

The *m*-sequences *x*and *y* are constructed as:

Initial conditions:

x is constructed with x (0)=1, x(1)= x(2)=...= x (16)= x (17)=0

y(0)=y(1)= … =y(16)= y(17)=1

Recursive definition of subsequent symbols:

$x(i+18) = x(i+7) + x(i)$ modulo 2, $i=0,\dots,2^{18}-20$,

$y(i+18) = y(i+10)+y(i+7)+y(i+5)+y(i)$ modulo 2, $i=0,\dots,2^{18}-20$.

The n:th Gold code sequence z_n, $n=0,1,2,\dots,2^{18}-2$, is then defined as

$z_n(i) = x((i+n) \text{ modulo } (2^{18}-1)) + y(i)$ modulo 2, $i=0,\dots,2^{18}-2$.

These binary sequences are converted to real valued sequences Z_n by the following transformation:

$$Z_n(i) = \begin{cases} +1 & if \;\; z_n(i) = 0 \\ -1 & if \;\; z_n(i) = 1 \end{cases} \quad for \quad i = 0,1,\ldots,2^{18}-2.$$

Finally, the n:th complex scrambling code sequence $S_{dl,n}$ is defined as:

$S_{dl,n}(i) = Z_n(i) + j\, Z_n((i+131072) \text{ modulo } (2^{18}-1)),\ i=0,1,\ldots,38399.$

Note that the pattern from phase 0 up to the phase of 38399 is repeated.

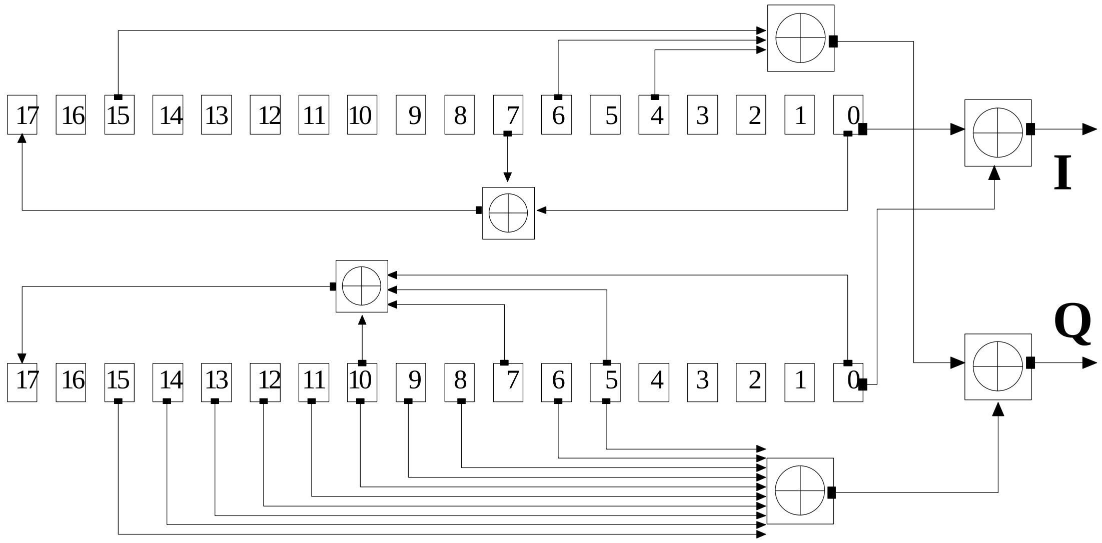

Figure 10: Configuration of downlink scrambling code generator