San Diego, USA, 28 FEB 2000 - 03 MAR 2000					
Agenda Item:	Ad Hoc 1				
Source:	Nokia, Siemens				
Title:	Changes to the section on Timing Advance in TS25.224				
Document for:	Approval				

3GPP TSG-RAN Working Group 1 Meeting No. 11 San Diego, USA, 28 FEB 2000 - 03 MAR 2000

The aim of the CR presented below is to clarify the existing section on the Timing advance in the TS25.224.

The following changes are proposed:

- Removal of the first sub-section (4.3.1) numbering. This is done to clarify that the use of UL synchronisation is strictly optional, and this is also indicated by the change of the first sentence in the current section 4.3.2.
- The last two sentences from the first paragraph are removed as they do not add any value to the physical layer specifications and no such restrictions are currently specified in the higher layer specifications.
- Also, a statement is added which should clarify when the UE is supposed to use the received TA command. This is beneficial information for delay estimation and for location based services. After the last meeting it was pointed out that it would be beneficial if the Node B would also know exactly when to expect the TA change. Since the TA message is generated at the RNC the Node B has no means of knowing that. In view of that a further revision has been made which makes the update moment a function of the SFN.
- Finally, the TA granularity is changed in the section 4.3.2 according to the WG4 requirements.

## 3GPP TSG RAN WG1 Meeting #11 San Diego, USA, 28 FEB 2000 - 03 MAR 2000

Document R1-00-0291 e.g. for 3GPP use the format TP-99xxx or for SMG, use the format P-99-xxx

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Form: CR cover sheet, version 2 for 3GPP and SMG The latest version of this form : <u>ftp://ftp.3gpp.org/Information/CR-Form-v2.doc</u>									
Proposed change affects: (U)SIM ME X UTRAN / Radio X Core Network   (at least one should be marked with an X) (U)SIM ME X UTRAN / Radio X Core Network									
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Subject:	Clarification	<mark>s on the UL sync</mark> h	nronisati	<mark>on and Tir</mark>	ning advanc	e.			
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<u>Reason for</u> change:	Clarification	s to the current de	escriptio	n on the u	se of the Tir	ning Adva	ance are neede	ed.	
Clauses affecte	<u>d:</u> 4.3								
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## 4.3 Timing Advance

## 4.3.1 Without UL Synchronization

<u>UTRAN may adjust the UE transmission timing with timing advance. The timing of transmissions from the UE is</u> adjusted according to timing advance values received from the UTRAN. The initial value for timing advance will be determined in the UTRAN by measurement of the timing of the PRACH. The required timing advance will be represented as an <u>68</u> bit number (0-<u>63</u><del>255</del>) being the multiple of 4 chips which is nearest to the required timing advance. The maximum allowed value may be limited by the operator to a value lower than 255, if required or the function may be disabled. A UE cannot operate beyond the range set by the maximum value of timing advance.

When Timing Advance is used the UTRAN will continuously measure the timing of a transmission from the UE and send the necessary timing advance value. On receipt of this value the UE will shall adjust the timing of its transmissions accordingly in steps of ±4chips. The transmission of TA values is done by means of higher layer messages. Upon receiving the TA command the UE shall adjust its transmission timing according to the timing advance command at the beginning of the next frame that fulfils the SFN Mod20 = 0 criteria and which does not occur sooner than 10 frames after the TTI period for the DCCH carrying the timing advance command ended.

When TDD to TDD handover takes place the UE shall transmit in the new cell with timing advance TA adjusted by the relative timing difference  $\Delta t$  between the new and the old cell:

 $TA_{new} = TA_{old} + 2\Delta t$ 

## 4.3.12 <u>Timing advance w</u>With UL Synchronization

With-If\_UL Synchronization is used, the timing advance is sub-chip granular and with high accuracy in order to enable synchronous CDMA in the UL. The required timing advance will be represented as a multiple of 1/48 chips.

The UTRAN will continuously measure the timing of a transmission from the UE and send the necessary timing advance value. On receipt of this value the UE will adjust the timing of its transmissions accordingly in steps of  $\pm 1/48$  chips. The transmission of TA values is ffs.

Support of UL synchronisation is optional for the UE.