**[104-e-NR-5G\_V2X-02]: Clarification on the S-SSB slot**

* **SY-4: Determination of slots including S-SSB transmission**
* **SY-6: Restriction of S-SSB slot**
* **Corrections for SY-1 Correction on SL-BCH, SY-2 Corrections/clarifications on S-SSB and SL-SSID can be discussed in the CR preparation.**

**till 1/28, with potential CRs till 2/2 – Teng (CATT)**

For the discussion on Rel-16 SL SYNC in this meeting, companies are encouraged to follow the steps:

* 1/25-1/26: 1st round to discuss the technical details.
* 1/26-1/27: 2nd round to discuss the proposal/potential TPs based on the discussion and contributions.
* 1/27-1/28: Conclusion for consensus.
* Till 2/2: All CRs discussion/checking.

**Issue SY 4 Determination of slots including S-SSB transmission**

Based on current specification description in TS 38.331 and 38.213, the value of the interval parameter is provided by *sl-timeInterval*, i.e., *sl-TimeInterval-r16* in IE *SL-SyncConfig-r16 in* TS 38.331. If *sl-TimeInterval-r16* is configured with value 0, it means there would be no interval between neighboring sidelink SSBs and  S-SSBs which transmit in the same slot corresponding to the value of . But based on RAN1’s agreement, it is not intended to transmit multiple S-SSBs in a slot, which have no obvious difference with transmitting one S-SSB in a slot since the multiple S-SSBs are same and the Tx power is restricted by the max power in a slot. So it is should be avoided.

RAN2 has not agreed the corresponding CR during last meeting, but the issue is still there from the system design perspective. This issue can be admitted by companies in RAN1. A solution can be discussed/solved and reflected in RAN1 spec, or RAN1 can send LS to RAN2 for spec change request/triggering RAN2’s discussion again.

**Round 1 comments 1/25-1/26**

**Q1: Do you think this issue exists in current specification? (Yes/No)**

**Q2: If Yes, do you think this issue can be solved in RAN1 spec or RAN2 spec? How to solve it/potential TPs?**

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| **Company** | **Q1**  **(Y/N)** | **Q2**  **(R1/R2)** | **TPs recommendation / Views** |
| LGE | Y | R1 | Ok with ZTE/Sanechips’ text proposal below.  is a slot interval between S-SS/PSBCH blocks, which is equal to , where,  is provided by *sl-timeInterval* |
| NTT DOCOMO | Y | R2 (or R1) | Multiple S-SSBs within a slot is impossible since one S-SSB uses all symbols of a slot. 0 is removed from sl-timeInterval if possible; otherwise, prohibition of 0 can be added in the corresponding field description.  Update of RAN1 spec is also fine for us. |
| ZTE,Sanechips | Y | R1 | The following TP in the clause 16.1 in 38.213:   |  | | --- | | 16.1 Synchronization procedures ......  ------------------- < Unchanged parts are omitted > --------------------------  A UE is provided, by *sl-NumSSB-WithinPeriod*, a number of S-SS/PSBCH blocks in a period of 16 frames. The UE assumes that a transmission of the S-SS/PSBCH blocks in the period is with a periodicity of 16 frames. The UE determines indexes of slots that include S-SS/PSBCH block as +, where  - index 0 corresponds to a first slot in a frame with SFN satisfying  - is a S-SS/PSBCH block index within the number of S-SS/PSBCH blocks in the period, with  - is a slot offset from a start of the period to the first slot including S-SS/PSBCH block, provided by *sl-TimeOffsetSSB*  - is a slot interval between S-SS/PSBCH blocks, which is equal to , where,  is provided by *sl-timeInterval*  ------------------- < Unchanged parts are omitted > --------------------------  ...... | |
| NEC | N/not sure |  | RRC parameters are cited below for reference.   |  | | --- | | ***sl-NumSSB-WithinPeriod***  Indicates the number of sidelink SSB transmissions within one sidelink SSB period. The applicable values are related to the subcarrier spacing and frequency as follows:  FR1, SCS = 15 kHz: 1, 2  FR1, SCS = 30 kHz: 1, 2, 4  FR1, SCS = 60 kHz: 1, 2, 4, 8  FR2, SCS = 60 kHz: 1, 2, 4, 8, 16, 32  FR2, SCS = 120 kHz: 1, 2, 4, 8, 16, 32, 64 | | ***sl-TimeOffsetSSB***  Indicates the slot offset from the start of sidelink SSB period to the first sidelink SSB. | | ***sl-TimeInterval***  Indicates the slot interval between neighboring sidelink SSBs. This value is applicable when there are more than one sidelink SSBs within one sidelink SSB period. |   We understand that multiple SSBs within one slot in not expected but we may need more clarification.  ***sl-TimeInterval*** indicates the slot **interval** between neighbouring SSBs, from our understanding, value 1 will be the smallest valid value. Then value 0 means SSB overlap and this error case can be avoided by proper higher layer configuration.  Follow the same logical, in case of 15KHz, there are only 160 slots within 160ms period. High layer will also avoid to configure *sl-TimeInterval*with value 639 which is within the current value range (0…639). As a consequence, we don't have to change the value range (0…639) of *sl-TimeInterval* for 15kHz. Situation are same in 30kHz case.  So, we think high layer can avoid to configure value 0 follow the same issues exist in value 639 for 15kHz and 30 kHz. |
| Ericsson | Y | R1 (or R2) | We are OK with the text proposal indicated by ZTE. Additionally, it could be possible to modify the R2 specification modifying the values of sl-TimeInterval-r16 to INTEGER (1..640) |
| Huawei/HiSilicon | No need to change | N/A | We don’t think this issue needs to be corrected at this stage at least for RAN1.   * First the value ‘0’ can be avoided by the network configuration. So even it is within the RRC parameter list, no harm at all. * Second, since the periodicity is 160ms for all SCS, the time interval values which are larger than 160 or 320 will be meaningless for 15kHz or 30kHz SCS respectively as well. But we can rely on the network to do the right configuration for these cases. * Third, if 639 is eventually changed to 640 for the time interval, even for 60kHz SCS, the largest interval between two adjacent S-SSB will be 638 (assume two S-SSBs are configured), the value 639 and 640 will be invalid as well. RAN1 have no necessity to correct all these invalid cases.   And furthermore, although in Rel-16 the value ‘0’ may not be used, while the value ‘0’ it may be used for future. For example, multiple S-SSBs can be transmitted in the same slot with different antenna ports by different direction or beam. |
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**Issue SY 6 Restriction of S-SSB slot**

An agreement in RAN1#101-e meeting is missed in the latest TS 38.213. The agreement is copied as follows.

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| RAN1#101-e  Agreements:   * S-SSB transmission/reception slots are in cell-specific UL resources in Uu. |

**Round 1 comments 1/25-1/26**

**Q1: Do you think the agreement above is missed in current specification? (Yes/No)**

**Q2: If Yes, do you have recommendation on the TPs? If No, please input your views.**

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| --- | --- | --- |
| **Company** | **Q1**  **(Y/N)** | **TPs recommendation / Views** |
| LGE | Y | OK with Docomo’s text proposal below.  A S-SS/PSBCH block can be transmitted/received in a slot of which all OFDM symbols are semi-statically configured as UL as per the higher layer parameter *tdd-UL-DL-ConfigurationCommon-r16* of the serving cell if providedor *sl-TDD-Configuration-r16* if provided or *sl-TDD-Config-r16* of the received PSBCH if provided. |
| NTT DOCOMO | Y | TP in our contribution (R1-2101582).  Any other clarification is fine for us if it describes the above agreement. |
| ZTE,Sanechips | Y | the following finetuning based on DCM's contribution to the clause 16.1 in 38.213:   |  | | --- | | 16.1 Synchronization procedures ......  ---------------------------- < Unchanged parts are omitted > ----------------------------  A UE is provided, by *numSSBwithinPeriod-SL*, a number of S-SS/PSBCH blocks in a period of 16 frames. The UE assumes that a transmission of the S-SS/PSBCH blocks in the period is with a periodicity of 16 frames. The UE determines indexes of slots that include S-SS/PSBCH block as +, where  - index 0 corresponds to a first slot in a frame with SFN satisfying  - is a S-SS/PSBCH block index within the number of S-SS/PSBCH blocks in the period, with  - is a slot offset from a start of the period to the first slot including S-SS/PSBCH block, provided by *timeOffsetSSB-SL*  - is a slot interval between S-SS/PSBCH blocks, provided by *timeIntervalSSB-SL*  The UE does not expect the set of symbols of the slot configured for S-SSB transmission and reception to be indicated in a cell-specific way as downlink/flexible by *TDD-UL-DL-ConfigCommon* which is configured or by *sl-TDD-Configuration* which is pre-configured, or by *sl-TDD-Config* which is indicated in the received PSBCH payload.  ---------------------------- < Unchanged parts are omitted > ----------------------------  ...... | |
| NEC | Y | DCM's proposal is ok. |
| Ericsson | Y | The text provided by DCM looks fine. |
| Huawei/HiSilicon | Y | The dedicated ITS band case also need to be captured. At this case, the suggested TP will need to be updated as: 16.1 Synchronization procedures ......  ---------------------------- < Unchanged parts are omitted > ----------------------------  A UE is provided, by *numSSBwithinPeriod-SL*, a number of S-SS/PSBCH blocks in a period of 16 frames. The UE assumes that a transmission of the S-SS/PSBCH blocks in the period is with a periodicity of 16 frames. The UE determines indexes of slots that include S-SS/PSBCH block as +, where  - index 0 corresponds to a first slot in a frame with SFN satisfying  - is a S-SS/PSBCH block index within the number of S-SS/PSBCH blocks in the period, with  - is a slot offset from a start of the period to the first slot including S-SS/PSBCH block, provided by *timeOffsetSSB-SL*  - is a slot interval between S-SS/PSBCH blocks, provided by *timeIntervalSSB-SL*  A S-SS/PSBCH block can be transmitted/received in a slot of which all OFDM symbols are semi-statically configured as UL as per the higher layer parameter *tdd-UL-DL-ConfigurationCommon* of the serving cell if providedor *sl-TDD-Configuration* if provided or *sl-TDD-Config* of the received PSBCH if provided or if *tdd-UL-DL-ConfigurationCommon* and *sl-TDD-Configuration* are not provided for a spectrum indicated with only PC5 interface in Table 5.2E.1-1 in [TS 38.101-1].  ---------------------------- < Unchanged parts are omitted > ---------------------------- |
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