TSG-RAN WG1#10

TSGR1#10(00)0155

Beijing, China, Jan. 18 – 21, 2000

Source: Ad hoc 5

Title: Ad hoc 5 meeting report on 1-20-00

The ad hoc received and discussed the following contributions"

Tdoc 14

Tdoc 17

Tdoc 095

Tdoc 140

Tdoc 152

These contributions address the following three issues:

- 1. Proposed text change for internal Turbo code interleaver.
- 2. Shortened tail bits for small size convolutional code.
- 3. Small size Turbo code interleaver.

Ad hoc's Recommendation:

- 1. Proposed text change on Turbo code internal interleaver will be further reviewed off-line by NTT Docomo and Nokia and others:
- 2. Proposal on shortened tail bits for convolutional code will not be accepted at this stage.
- 3. 40-bits is recommended to be the minimal size internal Turbo interleaver and for smaller size, it will be padded to 40 bits. Therefore, from WG2's perspective, Turbo codes of all lengths are supported.

- 4. Nortel and NTT Docomo's proposed Turbo internal interleaver for short frame is adopted with necessary changes.
- 5. Optional and mandatory: Current understanding is that the short size Turbo code is optional for both UE and basestation. However, it is believed that from technical point of view, it should be mandatory for UE if it supports Turbo coding. The group's suggestion is to postpone the decision to next meeting such that each company will be able to know whether they can accommodate the change for release 99.