
Title: Complexity of Nortel Turbo Code Puncturing Scheme

Source: Nortel Networks¹

0.0 Summary

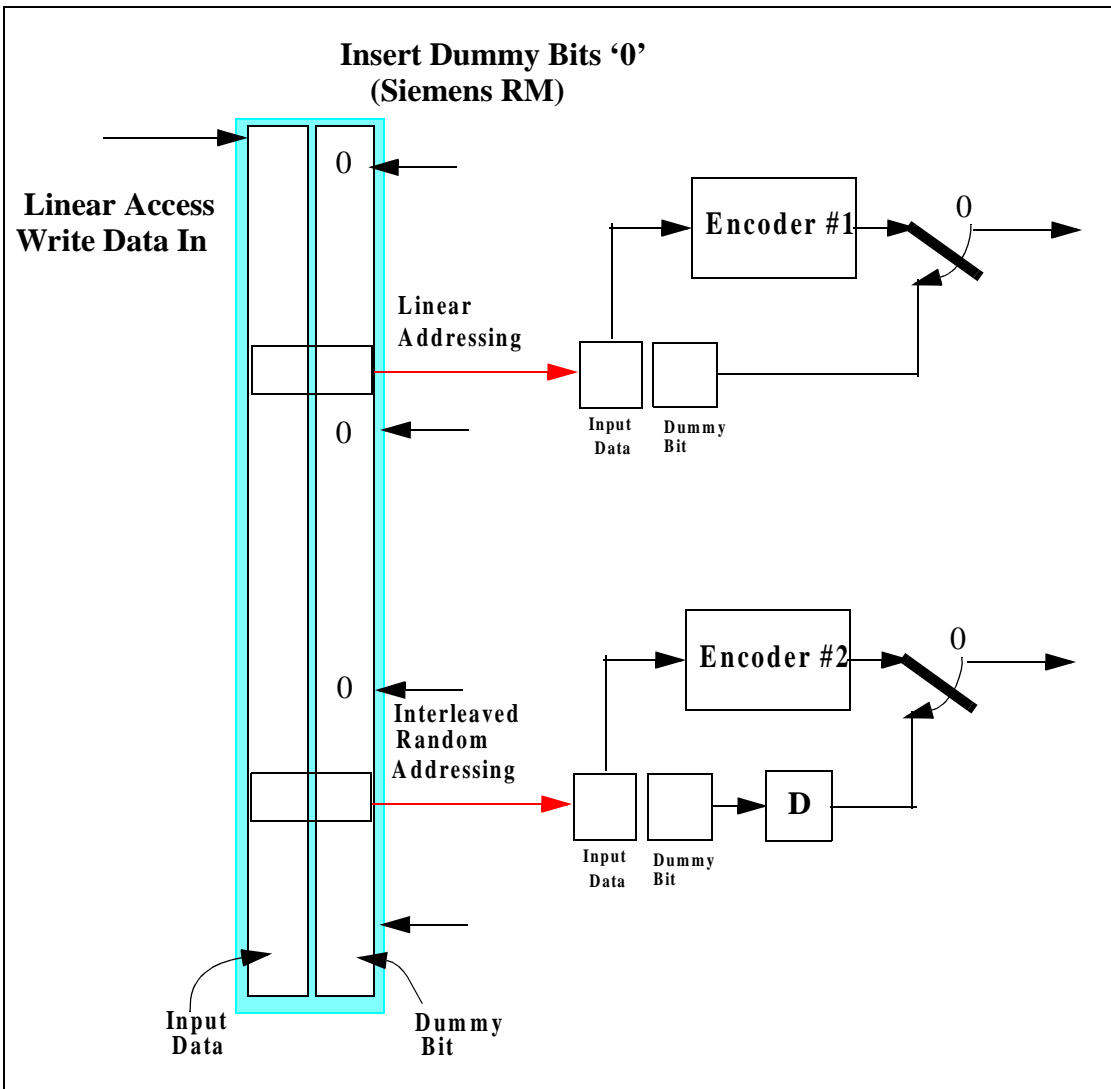
We present the a low complexity implementation of PIL interleaver based Turbo code puncturing scheme.

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1.0 PIL Interleaving Based Puncturing Scheme

The implementation is based on the single-pass PIL interleaving and dummy bits technique is shown in Figure 1. for the proposal in Ref[1].

FIGURE 1. Implementation of PIL Based Puncturing for Turbo Code



Reference:

- [1] Nortel: "Rate Matching Puncturing for 8-PCCC and Convolutional Code"
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