TSG-RAN Working Group 1(Radio) meeting #3 NYNÄSHAMN, Sweden 22-26, March 1999

# Agenda Item:

Source: Ad Hoc 12

Title:Report from Ad Hoc 12: Cell search (rev.)

# Document for:

## **1.** Issues to be discussed in Ad Hoc 12

1) CPM proposed by TTA

2) A new cyclic hierarchical sequences for Secondary SCH proposed by Nortel

3) A new hierarchical correlation sequence for Primary SCH proposed by Siemens

is a proposal to alter 1<sup>st</sup> and 2<sup>nd</sup> stage of cell search scheme. 2) is a proposal to alter codes used for the Secondary SCH. 3) is a proposal to alter codes used for Primary SCH.
cannot apply 1). 3) can apply both of 1) and 2).

# 2. Discussion Status

CPM and Cyclic hierarchical sequences (Nortel proposal) have been discussed in parallel. A new hierarchical correlation sequence was proposed lately and has not been discussed on e-mail.

# 2.1 CPM

### 2.1.1 E-mail discussion

From complexity, required memory size and performance point of view, there were many discussions to compare CPM with the current cell search scheme on e-mail.

Regarding performance, Shinsegi, Ericsson, and TI showed their simulation results of both CPM and the current scheme. Ericsson and TI showed that current scheme is superior to CPM. Shinsegi showed that CPM is superior to the current scheme.

Complexity and required memory size are depends on optimal period of each stage. Optimal period of each stage is not clear, now. Therefore, comparison of complexity and required memory size has not been completed.

In order to proceed discussion in this Ad Hoc, common simulation parameter set and common simulation conditions/scenarios were determined toward same simulation results among Shinsegi, Ericsson, and TI (see annex A of this report). Considering remaining time before WG1#3, link level simulation was adopted for our study. Shinsegi requested to do system level simulation after the link level simulations.

Mr. Furuya pointed out that optimization of power allocation to P-SCH and S-SCH should be considered for the current scheme. This point will be reflected to the next stage of simulation investigation.

### 2.1.2 Simulation results

In the physical Ad Hoc, Ericsson, TI, and Shinsegi reported their simulation results. Simulation results were different among the companies since they used different simulation models, and the simulation parameters were not same completely among companies. And Shinsegi proposed revised procedure of second and third stages and did simulations based on the new procedure. Ericsson and TI did not use the new procedure. Followings are overview of the simulation results.

#### Ericsson

• Two schemes have similar performance for hand over case at SCH loading factor of 10 % and velocity of 5 and 60 km.

ΤI

- Optimal periods of each stage for two schemes were shown.
- Two schemes have similar performance for initial acquisition case.
- Current scheme has significantly better performance over CPM for hand over case at appropriate reset time.
- Optimal PSCH and SSCH power ratio was shown for current scheme.
- It was pointed out that CPM need complex control rather than current scheme. But this was not clear for other colleagues. Further explanation is needed.
- It was pointed out that the advantage of the time multiplexed SCH are lost for CPM.

Shinsegi

- At high SNR, CPM has similar performance or better performance than current scheme.
- At low SNR, CPM has significantly better performance.

### 2.1.3 Future action

In order to get reliable simulation results among members, common simulation scenario, models and parameters are determined toward consensus before the next WG1 meeting.

Simulation method: Considering remaining time before the next WG1 meeting, link level simulation is selected.

Simulation model: Modified model of Ericsson and TI is selected. Shinsegi insisted the nessesity of additional BS in the model. However, other member could not understand the necessity at the moment. If other member can understand it in this week, the simulation model can be change.

Simulation parameter: Based on simulation results of TI and Shinsegi, simulation parameters were determined (see annex B of this report).

# 2.2 A new cyclic hierarchical sequences for Secondary SCH

Notel pointed out problem of cross correlation between PSC and SSC in current SCH, and proposed new code for Secondary SCH.

TI mentioned the problem pointed out by Nortel is not recognized in their study. Future plan

Nortel, TI, and Shinsegi will do simulation in order to investigate performance using the parameters and models similar to that of CPM investigation (see annex B). Furthermore, required memory size and complexity will also discussed on e-mail. Simulation results will be sent out from each company in 2 weeks, and will be discuss on e-mail.

### 3. A new hierarchical correlation sequence for Primary SCH

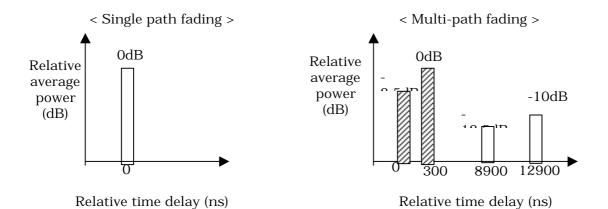
Siemens proposed a new Primary Synchronization code which can lower degradation due to frequency error.

This proposal will discuss on e-mail.

Slots per 10 msec frame16Minislots per slot-20Alphabet size of R-S code17Hopping codeRefer to [1,2]Refer to [3,4]Number of scrambling codes512Number of code groups32Chip rate (Mcps) $4.096/8.192/16.384$ (for complexity comparison)Number of samples per chip1, 2Data rate64 ks/secData modulationQPSKDownlink PN spreadingComplex spreading with shortened Gold sequenceNumber of users per cell20Number of cells7Channel model1-path, 2-paths (optionally 4-paths)*Location of mobileInitial cell searchUniform distribution in the cell searchLocation of mobileTarget cell searchCell edgeLog-normal shadowing standard deviation0SCH power ratio (pscH)5, 10, 15%1st SCH codeHadamard code multiplied by 1st code2nd SCH codeHadamard code multiplied by 1st code2nd SCH codeOptimized parameters of proponentProbability of error for 3rd stage0ptimized parameters of proponentProbability of error for 3rd stage10-4Parameter for coherent detection11			h parameters before the pr	5
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$ \begin{array}{ c c c c } \hline Channel model & 1-path, 2-paths (optionally 4-paths)* \\ \hline Initial cell search & Uniform distribution in the cell \\ \hline search & Cell edge \\ \hline Iarget cell search & Cell edge \\ \hline Log-normal shadowing standard deviation & 10 dB \\ \hline Path loss decay factor & 3.8 \\ \hline Mobile speed & 3, 60 km/h \\ \hline Thermal noise & 0 \\ \hline SCH power ratio (psch) & 5, 10, 15\% \\ \hline 1st SCH code & 16x16 hierarchical code \\ \hline 2nd SCH code & Hadamard code multiplied by 1st code \\ \hline Cell search parameters (e.g. frame lengths of 3 stages) \\ \hline Probability of error for 3rd stage & 10^{-4} \\ \hline L & - & 4 \\ \hline Parameter for coherent detection & 11 \\ \hline \end{array} $	Number of users per cell		20	
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	Period of each stage		Optimized in each scheme	

Table 1 Simulation parameters before the physical Ad Hoc.

Channel model



- Only when the algorithm c atches the first or second path signal, the cell searc h is declared to be succes sful. When the algorithm c atches the third or fourth path signal, the cell searc h is declared to fail.
- If 2-path model is adopte d, the first two path signal s (shaded) are used.

Parameter	3GPP	СРМ
Channel model	Vehicular B and	
	1 path	
Velosity	5, 60, and 250 km/h	
	Total SCH power is 5 and	
	10 % of BS power.	5 and 10 % of BS power
SCH power	Power allocation between	
	PSCH and SSCH is 0.40.6	
	and 0.60.4, respectively <sup>*1</sup> .	
PCCPCH power	5 and 10 % of BS power corresponding to Total SCH power	
PCCPCH transmission	Discontinuous	Continuous
Side information	10 BSs in the candidate list, whose groues are different and	
Side information	known. Path information of primary BS is known.	
Primary Cell/Target cell power	3 and 6 dB	
$(P_A/P_B)$		
Primary cell power/I <sub>oc</sub>	Variable, -8 to -2 dB	
Stage 1 for initial acquisition $\frac{*2}{}$	5 ms	<u>+10-5</u> ms
Stage 2 for initial acquisition <sup>*2</sup>	10 ms	<del>10</del> 5+x ms
Stage 3 for initial acquisition <sup>*2</sup>	5 ms	<del>10-5</del> ms
Reset time for initial acquisition $\frac{*2}{}$	20 ms	-
Stage 1 for Hand over <sup>*2</sup>	10 ms	10 ms
Stage 2 for Hand over $*^2$	20 ms	10+x ms
Stage 3 for Hand over <sup>*2</sup>	10 ms	10 ms
Reset time for Hand over <sup>*2</sup>	200 ms	-
Sampling rate	1 <del>, 2</del> /chip <u>(2/chip is optional)</u>	

Table 2 Simulation parameters determined in the Physical Ad Hoc

Note

\*1 Power allocations stated above are mandatory for the simulation. Other values can be selected optionally.

\*2 Periods of each stage stated above are mandatory for the simulations. Other values can be selected optionally.

All parameters other than above are selected based on table 1.