**3GPP TSG-RAN Meeting #91e *RP-21xxxx***

**2021-03-16 to 2021-03-26 Revision of** [**RP-210772**](http://www.3gpp.org/ftp/tsg_ran/TSG_RAN//TSGR_91e/Docs//RP-210772.zip)

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| *CR-Form-v12.1* |
| **CHANGE REQUEST** |
|  |
|  | **38.101-3** | **CR** | **0507** | **rev** | **2** | **Current version:** | **17.0.0** |  |
|  |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* |
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| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network | **X** | Core Network |  |

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| --- |
|  |
| ***Title:***  | Correcting FR1-FR2 BCS ambiguity – Interpretation B |
|  |  |
| ***Source to WG:*** | n.a. |
| ***Source to TSG:*** | Ericsson |
|  |  |
| ***Work item code:*** | NR\_newRAT-Core |  | ***Date:*** | 2021-03-25 |
|  |  |  |  |  |
| ***Category:*** | **A** |  | ***Release:*** | Rel-17 |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)…Rel-15 (Release 15)Rel-16 (Release 16)Rel-17 (Release 17)Rel-18 (Release 18)* |
|  |  |
| ***Reason for change:*** | 1) In the current version of the specification it is unclear which Bandwidth Combination Sets (BCS) apply for **FR1-FR2 Dual Connectivity**.Notes in tables in section 5.5B.7 say "*NOTE 1: NR configuration for FR1 and FR2 are defined in TS 38.101-1 [2] and TS 38.101-2 [3] respectively*". To lookup the two parts of the BCS in two tables, one would need two BCS IDs. However, the ANS.1 signalling provides currently only one such ID (*supportedBandwidthCombinationSet*). Furthermore, the BCS definitions for FR1-FR2 DC would be different from the BCS definitions for FR1-FR2 CA (which are defined explicitly in 38.101-3 and which use different BCS IDs for the same bandwidth combinations). |
|  |  |
| ***Summary of change:*** | 1) Replace the notes in the tables in section 5.5B.7 by a statement in the beginning of that section clarifying that BCSs for FR1-FR2 DC are defined in the FR1-FR2 CA tables in section 5.5A.1. |
|  |  |
| ***Consequences if not approved:*** | It remains unclear how the BCSs for FR1-FR2 DC are defined. This prevents implementaiton and launch of FR1-FR2 DC products (UE and NW). |
|  |  |
| ***Clauses affected:*** |  |
|  |  |
|  | **Y** | **N** |  |  |
| ***Other specs*** |  |  |  Other core specifications  | TS/TR ... CR ...  |
| ***affected:*** |  |  |  Test specifications | TS/TR ... CR ...  |
| ***(show related CRs)*** |  |  |  O&M Specifications | TS/TR ... CR ...  |
|  |  |
| ***Other comments:*** |  |
|  |  |
| ***This CR's revision history:*** | Rev. 1: - Category changed from F to A since CR was back-ported to Rel-15- In table “Table 5.5B.7-3” the change marks reflect now that the entire note in this table is new (unlike the other tables where the note was only corrected)Rev. 2:- Replace note in the table by text in the beginning of the seciton.  |

---Start of changes---

### 5.5B.7 Inter-band NR-DC between FR1 and FR2

The configurations and bandwidth combination sets for the FR1-FR2 NR-DC combinations in the following sub-sections are defined in the tables for FR1-FR2 carrier aggregation in section 5.5A.1.

#### 5.5B.7.1 Inter-band NR-DC configurations between FR1 and FR2 (two bands)

Table 5.5B.7-1: Inter-band NR-DC configurations between FR1 and FR2 (two bands)

| Downlink NR DCconfiguration | Uplink NR DCconfiguration |
| --- | --- |
| DC\_n1A-n257ADC\_n1A-n257GDC\_n1A-n257HDC\_n1A-n257I | DC\_n1A-n257ADC\_n1A-n257GDC\_n1A-n257HDC\_n1A-n257I |
| DC\_n1A-n258ADC\_n1A-n258GDC\_n1A-n258HDC\_n1A-n258IDC\_n1A-n258JDC\_n1A-n258KDC\_n1A-n258LDC\_n1A-n258M | DC\_n1A-n258A |
| DC\_n2A-n260ADC\_n2A-n260GDC\_n2A-n260HDC\_n2A-n260IDC\_n2A-n260JDC\_n2A-n260KDC\_n2A-n260LDC\_n2A-n260M | DC\_n2A-n260ADC\_n2A-n260GDC\_n2A-n260HDC\_n2A-n260IDC\_n2A-n260KDC\_n2A-n260LDC\_n2A-n260M |
| DC\_n2A-n261ADC\_n2A-n261GDC\_n2A-n261HDC\_n2A-n261IDC\_n2A-n261JDC\_n2A-n261KDC\_n2A-n261LDC\_n2A-n261M | DC\_n2A-n261ADC\_n2A-n261GDC\_n2A-n261HDC\_n2A-n261I |
| DC\_n2A-n261(2A)DC\_n2A-n261(3A)DC\_n2A-n261(4A) | DC\_n2A-n261A |
| DC\_n2A-n261(2G)DC\_n2A-n261(2H)DC\_n2A-n261(2I)DC\_n2A-n261(A-G)DC\_n2A-n261(A-H)DC\_n2A-n261(A-I)DC\_n2A-n261(A-J)DC\_n2A-n261(A-K)DC\_n2A-n261(A-L)DC\_n2A-n261(G-H)DC\_n2A-n261(H-I)DC\_n2A-n261(G-I)DC\_n2A-n261(A-G-H)DC\_n2A-n261(A-G-I)DC\_n2A-n261(2A-H)DC\_n2A-n261(2A-G)DC\_n2A-n261(2A-I)DC\_n2A-n261(A-2G) | DC\_n2A-n261ADC\_n2A-n261GDC\_n2A-n261HDC\_n2A-n261I |
| DC\_n3A-n257ADC\_n3A-n257DDC\_n3A-n257GDC\_n3A-n257HDC\_n3A-n257I | DC\_n3A-n257ADC\_n3A-n257DDC\_n3A-n257GDC\_n3A-n257HDC\_n3A-n257I |
| DC\_n5A-n260ADC\_n5A-n260GDC\_n5A-n260HDC\_n5A-n260IDC\_n5A-n260JDC\_n5A-n260KDC\_n5A-n260LDC\_n5A-n260M | DC\_n5A-n260A,DC\_n5A-n260G,DC\_n5A-n260H,DC\_n5A-n260IDC\_n5A-n260K,DC\_n5A-n260L,DC\_n5A-n260M |
| DC\_n40A-n258ADC\_n40A-n258GDC\_n40A-n258HDC\_n40A-n258IDC\_n40A-n258JDC\_n40A-n258KDC\_n40A-n258LDC\_n40A-n258M | DC\_n40A-n258A |
| DC\_n5A-n261ADC\_n5A-n261GDC\_n5A-n261HDC\_n5A-n261IDC\_n5A-n261JDC\_n5A-n261KDC\_n5A-n261LDC\_n5A-n261M | DC\_n5A-n261ADC\_n5A-n261GDC\_n5A-n261HDC\_n5A-n261I |
| DC\_n5A-n261(2A)DC\_n5A-n261(3A)DC\_n5A-n261(4A) | DC\_n5A-n261A |
| DC\_n5A-n261(2G)DC\_n5A-n261(2H)DC\_n5A-n261(2I)DC\_n5A-n261(A-G)DC\_n5A-n261(A-H)DC\_n5A-n261(A-I)DC\_n5A-n261(A-J)DC\_n5A-n261(A-K)DC\_n5A-n261(A-L)DC\_n5A-n261(G-H)DC\_n5A-n261(H-I)DC\_n5A-n261(G-I)DC\_n5A-n261(A-G-H)DC\_n5A-n261(A-G-I)DC\_n5A-n261(2A-H)DC\_n5A-n261(2A-G)DC\_n5A-n261(2A-I)DC\_n5A-n261(A-2G) | DC\_n5A-n261ADC\_n5A-n261GDC\_n5A-n261HDC\_n5A-n261I |
| DC\_n25A-n258ADC\_n25A-n258(2A) DC\_n25A-n258(3A) DC\_n25A-n258(4A) DC\_n25A-n258(5A) | DC\_n25A-n258A |
| DC\_n25A-n260ADC\_n25A-n260GDC\_n25A-n260HDC\_n25A-n260IDC\_n25A-n260JDC\_n25A-n260KDC\_n25A-n260LDC\_n25A-n260M | DC\_n25A-n260A |
| DC\_n25A-n260(2A)DC\_n25A-n260(3A)DC\_n25A-n260(4A)DC\_n25A-n260(5A)DC\_n25A-n260(6A)DC\_n25A-n260(7A)DC\_n25A-n260(8A) | DC\_n25A-n260A |
| DC\_n25A-n261ADC\_n25A-n261(2A) | DC\_n25A-n261A |
| DC\_n28A-n257ADC\_n28A-n257DDC\_n28A-n257GDC\_n28A-n257HDC\_n28A-n257I | DC\_n28A-n257ADC\_n28A-n257DDC\_n28A-n257GDC\_n28A-n257HDC\_n28A-n257I |
| DC\_n40A-n258ADC\_n40A-n258GDC\_n40A-n258HDC\_n40A-n258IDC\_n40A-n258JDC\_n40A-n258KDC\_n40A-n258LDC\_n40A-n258M | DC\_n40A-n258A |
| DC\_n41A-n258ADC\_n41C-n258A | DC\_n41A-n258A |
| DC\_n41A-n258(2A)DC\_n41A-n258(3A)DC\_n41A-n258(4A)DC\_n41A-n258(5A)DC\_n41C-n258(2A)DC\_n41C-n258(3A)DC\_n41C-n258(4A)DC\_n41C-n258(5A)DC\_n41(2A)-n258ADC\_n41(2A)-n258(2A)DC\_n41(2A)-n258(3A)DC\_n41(2A)-n258(4A)DC\_n41(2A)-n258(5A) | DC\_n41A-n258A |
| DC\_n41A-n260ADC\_n41A-n260(2A)DC\_n41A-n260(3A)DC\_n41A-n260(4A)DC\_n41A-n260(5A)DC\_n41A-n260(6A)DC\_n41A-n260(7A)DC\_n41A-n260(8A)DC\_n41A-n260GDC\_n41A-n260HDC\_n41A-n260IDC\_n41A-n260JDC\_n41A-n260KDC\_n41A-n260LDC\_n41A-n260M | DC\_n41A-n260A |
| DC\_n41(2A)-n260ADC\_n41(2A)-n260(2A)DC\_n41(2A)-n260(3A)DC\_n41(2A)-n260(4A)DC\_n41(2A)-n260(5A)DC\_n41(2A)-n260(6A)DC\_n41(2A)-n260(7A)DC\_n41(2A)-n260(8A)DC\_n41(2A)-n260GDC\_n41(2A)-n260HDC\_n41(2A)-n260IDC\_n41(2A)-n260JDC\_n41(2A)-n260KDC\_n41(2A)-n260LDC\_n41(2A)-n260M | DC\_n41A-n260A |
| DC\_n41C-n260ADC\_n41C-n260(2A)DC\_n41C-n260(3A)DC\_n41C-n260(4A)DC\_n41C-n260(5A)DC\_n41C-n260(6A)DC\_n41C-n260(7A)DC\_n41C-n260(8A)DC\_n41C-n260GDC\_n41C-n260HDC\_n41C-n260IDC\_n41C-n260JDC\_n41C-n260KDC\_n41C-n260LDC\_n41C-n260M | DC\_n41A-n260A |
| DC\_n41A-n261ADC\_n41C-n261A | DC\_n41A-n261A |
| DC\_n41A-n261(2A)DC\_n41C-n261(2A)DC\_n41(2A)-n261ADC\_n41(2A)-n261(2A) | DC\_n41A-n261A |
| DC\_n66A-n258ADC\_n66A-n258(2A)DC\_n66A-n258(3A)DC\_n66A-n258(4A)DC\_n66A-n258(5A) | DC\_n66A-n258A |
| DC\_n66A-n260ADC\_n66A-n260GDC\_n66A-n260HDC\_n66A-n260IDC\_n66A-n260JDC\_n66A-n260KDC\_n66A-n260LDC\_n66A-n260M | DC\_n66A-n260ADC\_n66A-n260GDC\_n66A-n260HDC\_n66A-n260IDC\_n66A-n260KDC\_n66A-n260LDC\_n66A-n260M |
| DC\_n66A-n260(2A)DC\_n66A-n260(3A)DC\_n66A-n260(4A)DC\_n66A-n260(5A)DC\_n66A-n260(6A)DC\_n66A-n260(7A)DC\_n66A-n260(8A) | DC\_n66A-n260A |
| DC\_n66A-n261ADC\_n66A-n261GDC\_n66A-n261HDC\_n66A-n261IDC\_n66A-n261JDC\_n66A-n261KDC\_n66A-n261LDC\_n66A-n261MDC\_n66A-n261ODC\_n66A-n261PDC\_n66A-n261Q | DC\_n66A-n261ADC\_n66A\_n261GDC\_n66A\_n261HDC\_n66A\_n261I |
| DC\_n66A-n261(2A)DC\_n66A-n261(3A)DC\_n66A-n261(4A) | DC\_n66A-n261A |
| DC\_n66A-n261(2G)DC\_n66A-n261(2H)DC\_n66A-n261(2I)DC\_n66A-n261(A-G)DC\_n66A-n261(A-H)DC\_n66A-n261(A-I)DC\_n66A-n261(A-J)DC\_n66A-n261(A-K)DC\_n66A-n261(A-L)DC\_n66A-n261(G-H)DC\_n66A-n261(H-I)DC\_n66A-n261(G-I)DC\_n66A-n261(A-G-H)DC\_n66A-n261(A-G-I)DC\_n66A-n261(2A-H)DC\_n66A-n261(2A-G)DC\_n66A-n261(2A-I)DC\_n66A-n261(A-2G) | DC\_n66A-n261ADC\_n66A-n261GDC\_n66A-n261HDC\_n66A-n261I |
| DC\_n77A-n257ADC\_n77A-n257DDC\_n77A-n257EDC\_n77A-n257FDC\_n77A-n257GDC\_n77A-n257HDC\_n77A-n257IDC\_n77A-n257JDC\_n77A-n257KDC\_n77A-n257LDC\_n77A-n257MDC\_n77C-n257ADC\_n77C-n257DDC\_n77C-n257EDC\_n77C-n257F | DC\_n77A-n257ADC\_n77A-n257GDC\_n77A-n257HDC\_n77A-n257IDC\_n77A-n257JDC\_n77A-n257KDC\_n77A-n257LDC\_n77A-n257M |
| DC\_n77(2A)-n257ADC\_n77(2A)-n257GDC\_n77(2A)-n257HDC\_n77(2A)-n257IDC\_n77(2A)-n257JDC\_n77(2A)-n257KDC\_n77(2A)-n257LDC\_n77(2A)-n257M | DC\_n77A-n257ADC\_n77A-n257GDC\_n77A-n257HDC\_n77A-n257IDC\_n77A-n257JDC\_n77A-n257KDC\_n77A-n257LDC\_n77A-n257M |
| DC\_n77A-n260ADC\_n77A-n260GDC\_n77A-n260HDC\_n77A-n260IDC\_n77A-n260JDC\_n77A-n260KDC\_n77A-n260LDC\_n77A-n260M | DC\_n77A-n260ADC\_n77A-n260GDC\_n77A-n260HDC\_n77A-n260I |
| DC\_n77A-n261ADC\_n77A-n261GDC\_n77A-n261HDC\_n77A-n261IDC\_n77A-n261JDC\_n77A-n261KDC\_n77A-n261LDC\_n77A-n261M | DC\_n77A-n261ADC\_n77A-n261GDC\_n77A-n261HDC\_n77A-n261IDC\_n77A-n261JDC\_n77A-n261KDC\_n77A-n261LDC\_n77A-n261M |
| DC\_n77A-n261(2A)DC\_n77A-n261(2G)DC\_n77A-n261(2H)DC\_n77A-n261(2I)DC\_n77A-n261(3A)DC\_n77A-n261(4A) | DC\_n77A-n261A |
| DC\_n77A-n261(A-G)DC\_n77A-n261(A-H)DC\_n77A-n261(A-I)DC\_n77A-n261(G-H)DC\_n77A-n261(G-I)DC\_n77A-n261(H-I)DC\_n77A-n261(A-J)DC\_n77A-n261(A-K)DC\_n77A-n261(A-L)DC\_n77A-n261(A-G-H)DC\_n77A-n261(A-G-I)DC\_n77A-n261(2A-H)DC\_n77A-n261(2A-G)DC\_n77A-n261(2A-I)DC\_n77A-n261(A-2G | DC\_n77A-n261ADC\_n77A-n261GDC\_n77A-n261HDC\_n77A-n261I |
| DC\_n78A-n257ADC\_n78A-n257DDC\_n78A-n257EDC\_n78A-n257FDC\_n78A-n257GDC\_n78A-n257HDC\_n78A-n257IDC\_n78A-n257JDC\_n78A-n257KDC\_n78A-n257LDC\_n78A-n257MDC\_n78C-n257ADC\_n78C-n257DDC\_n78C-n257EDC\_n78C-n257F | DC\_n78A-n257ADC\_n78A-n257GDC\_n78A-n257HDC\_n78A-n257I |
| DC\_n78A-n258ADC\_n78A-n258GDC\_n78A-n258HDC\_n78A-n258IDC\_n78A-n258JDC\_n78A-n258KDC\_n78A-n258LDC\_n78A-n258M | DC\_n78A-n258A |
| DC\_n79A-n257ADC\_n79A-n257DDC\_n79A-n257EDC\_n79A-n257FDC\_n79A-n257GDC\_n79A-n257HDC\_n79A-n257IDC\_n79A-n257JDC\_n79A-n257KDC\_n79A-n257LDC\_n79A-n257MDC\_n79C-n257ADC\_n79C-n257DDC\_n79C-n257EDC\_n79C-n257F | DC\_n79A-n257ADC\_n79A-n257GDC\_n79A-n257HDC\_n79A-n257I |
|  |

#### 5.5B.7.2 Inter-band NR-DC configurations between FR1 and FR2 (three bands)

Table 5.5B.7-2: Inter-band NR-DC configurations between FR1 and FR2 (three bands)

| Downlink NR DCconfiguration | Uplink NR DCconfiguration |
| --- | --- |
| DC\_n1A-n77A-n257ADC\_n1A-n77A-n257GDC\_n1A-n77A-n257HDC\_n1A-n77A-n257I | DC\_n1A-n257ADC\_n1A-n257GDC\_n1A-n257HDC\_n1A-n257IDC\_n77A-n257ADC\_n77A-n257GDC\_n77A-n257HDC\_n77A-n257I |
| DC\_n1A-n78A-n257ADC\_n1A-n78A-n257GDC\_n1A-n78A-n257HDC\_n1A-n78A-n257I | DC\_n1A-n257ADC\_n1A-n257GDC\_n1A-n257HDC\_n1A-n257IDC\_n78A-n257ADC\_n78A-n257GDC\_n78A-n257HDC\_n78A-n257I |
| DC\_n1A-n79A-n257ADC\_n1A-n79A-n257GDC\_n1A-n79A-n257HDC\_n1A-n79A-n257I | DC\_n1A-n257ADC\_n1A-n257GDC\_n1A-n257HDC\_n1A-n257IDC\_n79A-n257ADC\_n79A-n257GDC\_n79A-n257HDC\_n79A-n257I |
| DC\_n3A-n28A-n257ADC\_n3A-n28A-n257GDC\_n3A-n28A-n257HDC\_n3A-n28A-n257I | DC\_n3A-n28ADC\_n3A-n257ADC\_n3A-n257GDC\_n3A-n257HDC\_n3A-n257IDC\_n28A-n257ADC\_n28A-n257GDC\_n28A-n257HDC\_n28A-n257I |
| DC\_n3A-n77A-n257ADC\_n3A-n77A-n257GDC\_n3A-n77A-n257HDC\_n3A-n77A-n257I | DC\_n3A-n77ADC\_n3A-n257ADC\_n3A-n257GDC\_n3A-n257HDC\_n3A-n257IDC\_n77A-n257ADC\_n77A-n257GDC\_n77A-n257HDC\_n77A-n257I |
| DC\_n3A-n77(2A)-n257ADC\_n3A-n77(2A)-n257GDC\_n3A-n77(2A)-n257HDC\_n3A-n77(2A)-n257I | DC\_n3A-n77ADC\_n3A-n257ADC\_n3A-n257GDC\_n3A-n257HDC\_n3A-n257IDC\_n77A-n257ADC\_n77A-n257GDC\_n77A-n257HDC\_n77A-n257I |
| DC\_n3A-n78A-n257ADC\_n3A-n78A-n257GDC\_n3A-n78A-n257HDC\_n3A-n78A-n257I | DC\_n3A-n78ADC\_n3A-n257ADC\_n3A-n257GDC\_n3A-n257HDC\_n3A-n257IDC\_n78A-n257ADC\_n78A-n257GDC\_n78A-n257HDC\_n78A-n257I |
| DC\_n28A-n77A-n257ADC\_n28A-n77A-n257GDC\_n28A-n77A-n257HDC\_n28A-n77A-n257I | DC\_n28A-n77ADC\_n28A-n257ADC\_n28A-n257GDC\_n28A-n257HDC\_n28A-n257IDC\_n77A-n257ADC\_n77A-n257GDC\_n77A-n257HDC\_n77A-n257I |
| DC\_n28A-n78A-n257ADC\_n28A-n78A-n257GDC\_n28A-n78A-n257HDC\_n28A-n78A-n257I | DC\_n28A-n78ADC\_n28A-n257ADC\_n28A-n257GDC\_n28A-n257HDC\_n28A-n257IDC\_n78A-n257ADC\_n78A-n257GDC\_n78A-n257HDC\_n78A-n257I |
| DC\_n77A-n79A-n257ADC\_n77A-n79A-n257GDC\_n77A-n79A-n257HDC\_n77A-n79A-n257I | DC\_n77A-n257ADC\_n77A-n257GDC\_n77A-n257HDC\_n77A-n257IDC\_n79A-n257ADC\_n79A-n257GDC\_n79A-n257HDC\_n79A-n257I |
| DC\_n78A-n79A-n257ADC\_n78A-n79A-n257GDC\_n78A-n79A-n257HDC\_n78A-n79A-n257I | DC\_n78A-n257ADC\_n78A-n257GDC\_n78A-n257HDC\_n78A-n257IDC\_n79A-n257ADC\_n79A-n257GDC\_n79A-n257HDC\_n79A-n257I |
|  |

---End of changes---

#### 5.5B.7.3 Inter-band NR-DC configurations between FR1 and FR2 (four bands)

Table 5.5B.7-3: Inter-band NR-DC configurations between FR1 and FR2 (four bands)

| Downlink NR DCconfiguration | Uplink NR DCconfiguration |
| --- | --- |
| DC\_n1A-n77A-n79A-n257ADC\_n1A-n77A-n79A-n257GDC\_n1A-n77A-n79A-n257HDC\_n1A-n77A-n79A-n257I | DC\_n1A-n257ADC\_n1A-n257GDC\_n1A-n257HDC\_n1A-n257IDC\_n77A-n257ADC\_n77A-n257GDC\_n77A-n257HDC\_n77A-n257IDC\_n79A-n257ADC\_n79A-n257GDC\_n79A-n257HDC\_n79A-n257I |
| DC\_n1A-n78A-n79A-n257ADC\_n1A-n78A-n79A-n257GDC\_n1A-n78A-n79A-n257HDC\_n1A-n78A-n79A-n257I | DC\_n1A-n257ADC\_n1A-n257GDC\_n1A-n257HDC\_n1A-n257IDC\_n78A-n257ADC\_n78A-n257GDC\_n78A-n257HDC\_n78A-n257IDC\_n79A-n257ADC\_n79A-n257GDC\_n79A-n257HDC\_n79A-n257I |
| DC\_n3A-n28A-n77A-n257A | DC\_n3A-n257ADC\_n28A-n257ADC\_n77A-n257A |
| DC\_n3A-n28A-n77A-n257G | DC\_n3A-n257ADC\_n28A-n257ADC\_n77A-n257ADC\_n3A-n257GDC\_n28A-n257GDC\_n77A-n257G |
| DC\_n3A-n28A-n77A-n257H | DC\_n3A-n257ADC\_n28A-n257ADC\_n77A-n257ADC\_n3A-n257GDC\_n28A-n257GDC\_n77A-n257GDC\_n3A-n257HDC\_n28A-n257HDC\_n77A-n257H |
| DC\_n3A-n28A-n77A-n257I | DC\_n3A-n257ADC\_n28A-n257ADC\_n77A-n257ADC\_n3A-n257GDC\_n28A-n257GDC\_n77A-n257GDC\_n3A-n257HDC\_n28A-n257HDC\_n77A-n257HDC\_n3A-n257IDC\_n28A-n257IDC\_n77A-n257I |
| DC\_n3A-n28A-n78A-n257A | DC\_n3A-n257ADC\_n28A-n257ADC\_n78A-n257A |
| DC\_n3A-n28A-n78A-n257G | DC\_n3A-n257ADC\_n28A-n257ADC\_n78A-n257ADC\_n3A-n257GDC\_n28A-n257GDC\_n78A-n257G |
| DC\_n3A-n28A-n78A-n257H | DC\_n3A-n257ADC\_n28A-n257ADC\_n78A-n257ADC\_n3A-n257GDC\_n28A-n257GDC\_n78A-n257GDC\_n3A-n257HDC\_n28A-n257HDC\_n78A-n257H |
| DC\_n3A-n28A-n78A-n257I | DC\_n3A-n257ADC\_n28A-n257ADC\_n78A-n257ADC\_n3A-n257GDC\_n28A-n257GDC\_n78A-n257GDC\_n3A-n257HDC\_n28A-n257HDC\_n78A-n257HDC\_n3A-n257IDC\_n28A-n257IDC\_n78A-n257I |

## 5.5C Void