TSG-RAN Meeting #26 Athen, Greece, 08-10 December 2004 RP-040481 Agenda item 7.3.5

Source: TSG-RAN WG2.

Title: CRs to 25.331 Rel-5 (1) (and Rel-6)

The following CRs are in RP-040481:

Spec	CR	Rev	Phase	Subject	Cat	Version-Current	Version-New	Doc-2nd-Level	Workitem
25.331	2433	-	Rel-5	Correction to measured results on RACH	F	5.10.0	5.11.0	R2-042214	TEI5
25.331	2434	-	Rel-6	Correction to measured results on RACH	А	6.3.0	6.4.0	R2-042215	TEI5
25.331	2435	-	Rel-5	T305 handling upon a state transition	F	5.10.0	5.11.0	R2-042253	TEI5
25.331	2436	-	Rel-6	T305 handling upon a state transition	А	6.3.0	6.4.0	R2-042254	TEI5
25.331	2437	-	Rel-5	Handling of pending AM RLC unrecoverable errors signalled by cell update	F	5.10.0	5.11.0	R2-042268	TEI5
25.331	2438	-	Rel-6	Handling of pending AM RLC unrecoverable errors signalled by cell update	А	6.3.0	6.4.0	R2-042269	TEI5
25.331	2443	-	Rel-5	TPC step size in default configurations	F	5.10.0	5.11.0	R2-042585	TEI5
25.331	2444	-	Rel-6	TPC step size in default configurations	А	6.3.0	6.4.0	R2-042586	TEI5
25.331	2452	-	Rel-5	Correction to HS-DSCH reception conditions	F	5.10.0	5.11.0	R2-042595	HSDPA-L23
25.331	2453	-	Rel-6	Correction to HS-DSCH reception conditions	А	6.3.0	6.4.0	R2-042596	HSDPA-L23
25.331	2454	1	Rel-5	MAC-hs Reset procedure	F	5.10.0	5.11.0	R2-042685	HSDPA-L23
25.331	2455	1	Rel-6	MAC-hs Reset procedure	А	6.3.0	6.4.0	R2-042686	HSDPA-L23

3GPP TSG RAN2#44 Sophia Antipolis, France, France, 4-8 October 2004

R2-042214

	CHANG	E REQUEST	CR-Form-v7.1						
[#] 2	5.331 CR 2433	жrev – ^ж	Current version: 5.a.0 [#]						
For <u>HELP</u> on using	g this form, see bottom of th	nis page or look at the	e pop-up text over the						
Drange and change offe									
Proposed change ane									
Title: ೫ Co	orrection to measured resu	Its on RACH							
Source: ೫ R	AN WG2								
Work item code: ℜ <mark>⊺</mark> [EI5		Date: # September 2004						
Use Det be t	e <u>one</u> of the following categori F (correction) A (corresponds to a correct B (addition of feature), C (functional modification of D (editorial modification) tailed explanations of the above found in 3GPP <u>TR 21.900</u> .	es: ion in an earlier release f feature) ve categories can	Use <u>one</u> of the following releases: Ph2 (GSM Phase 2) Ph2 (Release 1996) R97 (Release 1997) R98 (Release 1998) R99 (Release 1999) Rel-4 (Release 4) Rel-5 (Release 5) Rel-6 (Release 6) Rel-7 (Release 7)						
Reason for change: 🤀	 The following inconsist detected in 25.331: Section 8.1.1.6.1 quantity", "Intra-f "Maximum numb for state CELL_E 	stency related to the " 2: read and use the I requency reporting quer of reported cells o DCH" in SIB 11 if not i	'measured results on RACH" was IE "Intra-frequency measurement uantity for RACH reporting", n RACH" or "Reporting information included in SIB12.						
	 Section 8.5.23: u 	ise SIB 11 values onl	y if SIB 12 is not broadcast.						
Summary of change: ¥	It is assumed that the understood to be corr section 8.1.1.6.12.	It is assumed that the behaviour from section 8.1.1.6.12 is generally understood to be correct. Therefore section 8.5.23 is proposed to be aligned to section 8.1.1.6.12.							
Consequences if and the sequences if the sequences of the sequences if the sequences of the	functions istency remains	s in the specification.							

Clauses affected:	ж	8.5.23
	Y	
Other specs	ж	Other core specifications %
affected:		O&M Specifications
Other comments:	ж	

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at <u>http://www.3gpp.org/specs/CR.htm</u>. Below is a brief summary:

- 1) Fill out the above form. The symbols above marked # contain pop-up help information about the field that they are closest to.
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under <u>ftp://ftp.3gpp.org/specs/</u> For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.
- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

8.5.23 Measured results on RACH

When transmitting an uplink RRC message, the UE shall:

- 1> if the uplink RRC message is an RRC CONNECTION REQUEST message:
 - 2> if the IE "Intra-frequency reporting quantity for RACH reporting" and the IE "Maximum number of reported cells on RACH" are included in System Information Block type 11:
 - 3> include a measurement report in the IE "Measured results on RACH", as specified in the IE "Intrafrequency reporting quantity for RACH reporting" and the IE "Maximum number of reported cells on RACH" in System Information Block type 11.
- 1> for any other uplink RRC message which optionally includes the IE "Measured results on RACH":
 - 2> if the IE "Intra-frequency reporting quantity for RACH reporting" and the IE "Maximum number of reported cells on RACH" are included in System Information Block type 12 (or "System Information Block Type 11", if these IEs are not included in the broadcast "System Information Block Type 12" or "System Information Block Type 12" is not being broadcast).
 - 3> include a measurement report in the IE "Measured results on RACH", as specified in the IE "Intrafrequency reporting quantity for RACH reporting" and the IE "Maximum number of reported cells on RACH" in System Information Block type 12 (or "System Information Block Type 11" if <u>these IEs are</u> <u>not included in the broadcast</u> "System Information Block Type 12" or "System Information Block Type <u>12</u> is not being broadcast).
- 1> include in the IE "Measured results on RACH" all requested reporting quantities for cells for which measurements are reported.
 - NOTE: The UE only includes measurement results for neighbour cells for which valid measurements are available at the time the message is sent. At cell access following selection or reselection to a cell, the UE may not have had sufficient time to obtain valid measurement results for neighbour cells.
- 1> for messages transmitted on CCCH, take care that the maximum allowed message size is not exceeded when forming the IE "Measured results on RACH", i.e. limit the number of included neighbour cells or if required omit the IE "Measured results on RACH" altogether.

If the IE "Measured results on RACH" is present in the message, the UTRAN should extract the contents to be used for radio resource control.

3GPP TSG RAN2#44 Sophia Antipolis, France, France, 4-8 October 2004

R2-042215

									C	R-Form-v71
	CHANGE REQUEST									
ж	25.33	<mark>1</mark> CR	2434	жrev	-	Ħ	Current versi	ion: 6.3	.0	ж
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Proposed change affects: UICC apps# ME X Radio Access Network X Core Network										
Title: ដ	Correct	ion to m	easured resul	ts on RAC	Η					
Source: ೫	RAN W	G2								
Work item code: %	TEI5						<i>Date:</i> ೫	Septemb	er 2	004
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Reason for change	: # T	Fhe follo detected Sec qua "Ma for s Sec	wing inconsist in 25.331: tion 8.1.1.6.12 ntity", "Intra-fr ximum numbe state CELL_D tion 8.5.23: us	ency relat 2: read and equency r er of repor CH" in SIE se SIB 11	d use eportin ted ce 11 if	the f ng q ells o not i s onl	fmeasured res E "Intra-frequu uantity for RA n RACH" or " included in SI y if SIB 12 is	sults on R/ lency mea CH report Reporting B12. not broado	ACH sure ng", infor	" was ment mation
Summary of chang	' е: Ж І	t is assu	med that the l	oehaviour	from s	secti	on 8.1.1.6.12	is general	ly	

 understood to be correct. Therefore section 8.5.23 is proposed to be aligned to section 8.1.1.6.12.

 Consequences if not approved:

Clauses affected:	¥ 8.5.23
Other specs affected:	Y N % Other core specifications % Test specifications % Q&M Specifications %
Other comments:	* · · · · · · · · · · · · · · · · · · ·

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- 1> for any other uplink RRC message which optionally includes the IE "Measured results on RACH":
 - 2> if the IE "Intra-frequency reporting quantity for RACH reporting" and the IE "Maximum number of reported cells on RACH" are included in System Information Block type 12 (or "System Information Block Type 11", if these IEs are not included in the broadcast "System Information Block Type 12" or "System Information Block Type 12" is not being broadcast).
 - 3> include a measurement report in the IE "Measured results on RACH", as specified in the IE "Intrafrequency reporting quantity for RACH reporting" and the IE "Maximum number of reported cells on RACH" in System Information Block type 12 (or "System Information Block Type 11" if <u>these IEs are</u> <u>not included in the broadcast</u> "System Information Block Type 12" or "System Information Block Type <u>12</u> is not being broadcast).
- 1> include in the IE "Measured results on RACH" all requested reporting quantities for cells for which measurements are reported.
 - NOTE: The UE only includes measurement results for neighbour cells for which valid measurements are available at the time the message is sent. At cell access following selection or reselection to a cell, the UE may not have had sufficient time to obtain valid measurement results for neighbour cells.
- 1> for messages transmitted on CCCH, take care that the maximum allowed message size is not exceeded when forming the IE "Measured results on RACH", i.e. limit the number of included neighbour cells or if required omit the IE "Measured results on RACH" altogether.

If the IE "Measured results on RACH" is present in the message, the UTRAN should extract the contents to be used for radio resource control.

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ж		25.331	CR 2	2 <mark>435</mark>	ж ге \	· -	ж	Current vers	sion:	5.a.0	ж
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Proposed chang	ye a	affects:	UICC ap	ps¥	ME[X R	adio A	Access Netwo	rk	Core Ne	etwork
Title:	Ж	T305 har	<mark>dling up</mark>	on a state tra	ansition						
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Reason for change: ೫	In the current specification it is not completely clear whether the UE should re- start T305 in case of a state transition from CELL_FACH state to CELL_PCH/URA_PCH state. In section 13.1 it is indicated that the UE should stop T305 when "entering another state". Section 8 only mentions the UE stopping of T305 at the initiation
	of CELL UPDATE/URA UPDATE procedure.
	See R2-042030 for more detail.
Summary of change: #	It is proposed to specify that the UE shall stop T305 when it leaves CELL_FACH state.
	Implementation of this CR by a R99/Rel-4 UE will not cause compatibility issues.
0	
not approved:	Two possibilites of UE implementation remain in the specification.
	Isolated impact analysis:
	This CR only affects UE implementation.
	Impact on test specifications:
	No impact. The test case 8.3.1.4 in 34.123-1 already assumes the UE re-starts
	T305 upon a state transition from CELL_FACH to CELL_PCH.

Clauses affected: % 8.2.2.3

Other specs affected:	Ħ	Y	N X X X	Other core specifications Test specifications O&M Specifications	Ħ	
Other comments:	ж					

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8.2.2.3 Reception of RADIO BEARER SETUP or RADIO BEARER RECONFIGURATION or RADIO BEARER RELEASE or TRANSPORT CHANNEL RECONFIGURATION or PHYSICAL CHANNEL RECONFIGURATION message by the UE

The UE shall:

- 1> be able to receive any of the following messages:
 - 2> RADIO BEARER SETUP message; or
 - 2> RADIO BEARER RECONFIGURATION message; or
 - 2> RADIO BEARER RELEASE message; or
 - 2> TRANSPORT CHANNEL RECONFIGURATION message; or
 - 2> PHYSICAL CHANNEL RECONFIGURATION message;
- 1> be able to perform a hard handover and apply physical layer synchronisation procedure A as specified in [29], even if no prior UE measurements have been performed on the target cell and/or frequency.

In case the reconfiguration procedure is used to remove all existing RL(s) in the active set while new RL(s) are established the UE shall:

1> if the UE has a pending "TGPS reconfiguration CFN" at the activation time received in the reconfiguration message and the reconfiguration requests a timing re-initialised hard handover (see subclause 8.3.5.1), the UE may:

2> abort the pending CM activation;

- 2> set the CM_PATTERN_ACTIVATION_ABORTED to TRUE.
- 1> otherwise:
 - 2> set the CM_PATTERN_ACTIVATION_ABORTED to FALSE.

If the UE receives:

- a RADIO BEARER SETUP message; or
- a RADIO BEARER RECONFIGURATION message; or
- a RADIO BEARER RELEASE message; or
- a TRANSPORT CHANNEL RECONFIGURATION message; or
- a PHYSICAL CHANNEL RECONFIGURATION message:

it shall:

- 1> set the variable ORDERED_RECONFIGURATION to TRUE;
- 1> if the UE will enter the CELL_DCH state from any state other than CELL_DCH state at the conclusion of this procedure:
 - 2> perform the physical layer synchronisation procedure A as specified in [29] (FDD only).
- 1> act upon all received information elements as specified in subclause 8.6, unless specified in the following and perform the actions below.

The UE may:

1> maintain a list of the set of cells to which the UE has Radio Links if the IE "Cell ID" is present.

The UE may first release the physical channel configuration used at reception of the reconfiguration message. The UE shall then:

- 1> in FDD, if the IE "PDSCH code mapping" is included but the IE "PDSCH with SHO DCH Info" is not included and if the DCH has only one link in its active set:
 - 2> act upon the IE "PDSCH code mapping" as specified in subclause 8.6; and
 - 2> infer that the PDSCH will be transmitted from the cell from which the downlink DPCH is transmitted.
- 1> enter a state according to subclause 8.6.3.3.

In case the UE receives a RADIO BEARER RECONFIGURATION message including the IE "RB information to reconfigure" that only includes the IE "RB identity", the UE shall:

- 1> handle the message as if IE "RB information to reconfigure" was absent.
- NOTE: The RADIO BEARER RECONFIGURATION message always includes the IE "RB information to reconfigure". UTRAN has to include it even if it does not require the reconfiguration of any RB.

If after state transition the UE enters CELL_DCH state, the UE shall, after the state transition:

- 1> in FDD; or
- 1> in TDD when "Primary CCPCH Info" is included indicating a new target cell and "New C-RNTI" is not specified:
 - 2> remove any C-RNTI from MAC;
 - 2> clear the variable C_RNTI.

If after state transition the UE leaves CELL_DCH state, the UE shall, after the state transition:

- 1> clear any stored IE "Downlink HS-PDSCH information";
- 1> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25.

In FDD, if after state transition the UE leaves CELL_DCH state, the UE shall, after the state transition:

- 1> remove any DSCH-RNTI from MAC;
- 1> clear the variable DSCH_RNTI.

If the UE was in CELL_DCH state upon reception of the reconfiguration message and remains in CELL_DCH state, the UE shall:

1> if the IE "Uplink DPCH Info" is absent, not change its current UL Physical channel configuration;

1> in TDD:

2> if "Primary CCPCH Info" is included indicating a new target cell and "New C-RNTI" is not specified:

- 3> remove any C-RNTI from MAC;
- 3> clear the variable C_RNTI.
- 1> if "DPCH frame offset" is included for one or more RLs in the active set:
 - 2> use its value to determine the beginning of the DPCH frame in accordance with the following:
 - 3> if the received IE "DPCH frame offset" is across the value range border compared to the DPCH frame offset currently used by the UE:
 - 4> consider it to be a request to adjust the timing with 256 chips across the frame border (e.g. if the UE receives value 0 while the value currently used is 38144 consider this as a request to adjust the timing with +256 chips).

- 3> if after taking into account value range borders, the received IE "DPCH frame offset" corresponds to a request to adjust the timing with a step exceeding 256 chips:
 - 4> set the variable INVALID_CONFIGURATION to TRUE.
- 3> and the procedure ends.
- 2> adjust the radio link timing accordingly.

If after state transition the UE enters CELL_FACH state, the UE shall, after the state transition:

- 1> if the IE "Frequency info" is included in the received reconfiguration message:
 - 2> select a suitable UTRA cell according to [4] on that frequency;
 - 2> if the UE finds a suitable UTRA cell on that frequency:
 - 3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selects another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):
 - 4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 4> when the cell update procedure completed successfully:
 - 5> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.
 - 2> else, if the UE can not find a suitable UTRA cell on that frequency but it finds a suitable UTRA cell on another frequency:
 - 3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 3> when the cell update procedure completed successfully:
 - 4> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.
- 1> if the IE "Frequency info" is not included in the received reconfiguration message:
 - 2> select a suitable UTRA cell according to [4];
 - 2> if the UE finds a suitable UTRA cell on the current frequency:
 - 3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selects another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):
 - 4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 4> when the cell update procedure completed successfully:
 - 5> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.
 - 2> else, if the UE can not find a suitable UTRA cell on the current frequency but it finds a suitable UTRA cell on another frequency:
 - 3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 3> when the cell update procedure completed successfully:
 - 4> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.

- 1> start timer T305 using its initial value if timer T305 is not running and if periodical update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity" in the variable TIMERS_AND_CONSTANTS;
- 1> select PRACH according to subclause 8.5.17;
- 1> select Secondary CCPCH according to subclause 8.5.19;
- 1> use the transport format set given in system information;
- 1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:
 - 2> ignore that IE and stop using DRX.
- 1> if the contents of the variable C_RNTI is empty:
 - 2> perform a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 2> when the cell update procedure completed successfully:
 - 3> if the UE is in CELL_PCH or URA_PCH state:
 - 4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission";
 - 4> proceed as below.

If the UE was in CELL_FACH state upon reception of the reconfiguration message and remains in CELL_FACH state, the UE shall:

- 1> if the IE "Frequency info" is included in the received reconfiguration message:
 - 2> select a suitable UTRA cell according to [4] on that frequency;
 - 2> if the UE finds a suitable UTRA cell on that frequency:
 - 3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selected another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):
 - 4> initiate a cell update procedure according to subclause 8.3.1 using the cause "cell reselection";
 - 4> when the cell update procedure completed successfully:
 - 5> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.
 - 2> else, if the UE can not find a suitable UTRA cell on that frequency but it finds a suitable UTRA cell on another frequency:
 - 3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 3> when the cell update procedure completed successfully:
 - 4> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.
- 1> if the IE "Frequency info" is not included in the received reconfiguration message:
 - 2> if the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD) is included the UE shall either:
 - 3> ignore the content of the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD) and proceed as below;

2> or:

- 3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CPCH info" (for TDD), and it is different from the current cell:
 - 4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 4> when the cell update procedure completed successfully:
 - 5> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.

If after state transition the UE leaves CELL_FACH state, the UE shall:

1> stop timer T305;

If after state transition the UE enters CELL_PCH or URA_PCH state, the UE shall:

- 1> if the IE "UTRAN DRX cycle length coefficient" is not included in the same message:
 - 2> set the variable INVALID_CONFIGURATION to TRUE.

The UE shall transmit a response message as specified in subclause 8.2.2.4, setting the information elements as specified below. The UE shall:

- 1> if the received reconfiguration message included the IE "Downlink counter synchronisation info"; or
- 1> if the received reconfiguration message is a RADIO BEARER RECONFIGURATION and the IE "New U-RNTI" is included:
 - 2> if the variable PDCP_SN_INFO is empty:
 - 3> configure the corresponding RLC entity for all AM and UM radio bearers and AM and UM signalling radio bearers except RB2 to "stop".
 - 2> else:
 - 3> configure the RLC entity for signalling radio bearers RB1, RB3 and RB4 to "stop";
 - 3> configure the RLC entity for UM and AM radio bearers for which the IE "PDCP SN Info" is not included to "stop".
 - 2> re-establish the RLC entity for RB2;
 - 2> for the downlink and the uplink, apply the ciphering configuration as follows:
 - 3> if the received re-configuation message included the IE "Ciphering Mode Info":
 - 4> use the ciphering configuration in the received message when transmitting the response message.
 - 3> if the ciphering configuration for RB2 from a previously received SECURITY MODE COMMAND has not yet been applied because the activation times not having been reached:
 - 4> if the previous SECURITY MODE COMMAND was received due to new keys being received:
 - 5> consider the new ciphering configuration to include the received new keys;
 - 5> initialise the HFN component of the uplink COUNT-C and downlink COUNT-C of SRB2 as indicated in subclause 8.1.12.3.1.
 - 4> if the ciphering configuration for RB2 from a previously received SECURITY MODE COMMAND has not yet been applied because of the corresponding activation times not having been reached and the previous SECURITY MODE COMMAND caused a change in LATEST_CONFIGURED_CN_DOMAIN:
 - 5> consider the new ciphering configuration to include the keys associated with the LATEST_CONFIGURED_CN_DOMAIN;

5> initialise the HFN component of the uplink COUNT-C and downlink COUNT-C of SRB2 to the most recently transmitted IE "START list" or IE "START" for the

LATEST_CONFIGURED_CN_DOMAIN at the reception of the previous SECURITY MODE COMMAND.

4> apply the new ciphering configuration immediately following RLC re-establishment.

3> else:

4> continue using the current ciphering configuration.

- 2> set the new uplink and downlink HFN component of COUNT-C of RB2 to MAX(uplink HFN component of COUNT-C of RB2, downlink HFN component of COUNT-C of RB2);
- 2> increment by one the downlink and uplink values of the HFN of COUNT-C for RB2;
- 2> calculate the START value according to subclause 8.5.9;
- 2> include the calculated START values for each CN domain in the IE "START list" in the IE "Uplink counter synchronisation info".
- 1> if the received reconfiguration message did not include the IE "Downlink counter synchronisation info":
 - 2> if the variable START_VALUE_TO_TRANSMIT is set:

3> include and set the IE "START" to the value of that variable.

- 2> if the variable START_VALUE_TO_TRANSMIT is not set and the IE "New U-RNTI" is included:
 - 3> calculate the START value according to subclause 8.5.9;
 - 3> include the calculated START values for each CN domain in the IE "START list" in the IE "Uplink counter synchronisation info".
- 2> if the received reconfiguration message caused a change in the RLC size for any RB using RLC-AM:
 - 3> calculate the START value according to subclause 8.5.9;
 - 3> include the calculated START values for the CN domain associated with the corresponding RB identity in the IE "START list" in the IE "Uplink counter synchronisation info".
- 1> if the received reconfiguration message contained the IE "Ciphering mode info" or contained the IE "Integrity protection mode info":
 - 2> set the IE "Status" in the variable SECURITY_MODIFICATION for all the CN domains in the variable SECURITY_MODIFICATION to "Affected".
- 1> if the received reconfiguration message contained the IE "Ciphering mode info":
 - 2> if the reconfiguration message is not used to perform SRNS relocation with change of ciphering algorithm:

3> the UE behaviour is not specified.

- 2> if the message is used to perform a timing re-initialised hard handover:
 - 3> if IE "Ciphering activation time for DPCH" is included:

4> the UE behaviour is not specified.

- 2> else:
 - 3> if the reconfiguration message is used to setup radio bearer(s) using RLC-TM; or
 - 3> if radio bearer(s) using RLC-TM already exist:
 - 4> if IE "Ciphering activation time for DPCH" is not included:
 - 5> the UE behaviour is not specified.

- 2> include and set the IE "Radio bearer uplink ciphering activation time info" to the value of the variable RB_UPLINK_CIPHERING_ACTIVATION_TIME_INFO.
- 1> if the received reconfiguration message did not contain the IE "Ciphering activation time for DPCH" in IE "Ciphering mode info":
 - 2> if prior to this procedure there exist no transparent mode RLC radio bearers:
 - 3> if, at the conclusion of this procedure, the UE will be in CELL_DCH state; and
 - 3> if, at the conclusion of this procedure, at least one transparent mode RLC radio bearer exists:
 - 4> include the IE "COUNT-C activation time" and specify a CFN value for this IE that is a multiple of 8 frames (CFN mod 8 = 0) and lies at least 200 frames ahead of the CFN in which the response message is first transmitted.
- NOTE: UTRAN should not include the IE "Ciphering mode info" in any reconfiguration message unless it is also used to perform an SRNS relocation with change of ciphering algorithm.
- 1> set the IE "RRC transaction identifier" to the value of "RRC transaction identifier" in the entry for the received message in the table "Accepted transactions" in the variable TRANSACTIONS; and
- 1> clear that entry;
- 1> if the variable PDCP_SN_INFO is not empty:

2> include the IE "RB with PDCP information list" and set it to the value of the variable PDCP_SN_INFO.

1> in TDD, if the procedure is used to perform a handover to a cell where timing advance is enabled, and the UE can calculate the timing advance value in the new cell (i.e. in a synchronous TDD network):

2> set the IE "Uplink Timing Advance" according to subclause 8.6.6.26.

- 1> if the IE "Integrity protection mode info" was present in the received reconfiguration message:
 - 2> start applying the new integrity protection configuration in the uplink for signalling radio bearer RB2 from and including the transmitted response message.

If after state transition the UE enters URA_PCH state, the UE shall, after the state transition and transmission of the response message:

- 1> if the IE "Frequency info" is included in the received reconfiguration message:
 - 2> select a suitable UTRA cell according to [4] on that frequency.
 - 2> if the UE can not find a suitable UTRA cell on that frequency but it finds a suitable UTRA cell on another frequency:

3> proceed as below.

- 1> if the IE "Frequency info" is not included in the received reconfiguration message:
 - 2> select a suitable UTRA cell according to [4].
- 1> prohibit periodical status transmission in RLC;
- 1> remove any C-RNTI from MAC;
- 1> clear the variable C_RNTI;
- 1> start timer T305 using its initial value if timer T305 is not running and if periodical update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity" in the variable TIMERS_AND_CONSTANTS;
- 1> select Secondary CCPCH according to subclause 8.5.19;
- 1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:

- 2> use the value in the IE "UTRAN DRX Cycle length coefficient" for calculating Paging occasion and PICH Monitoring Occasion as specified in subclause 8.6.3.2.
- 1> if the criteria for URA update caused by "URA reselection" according to subclause 8.3.1 are fulfilled after cell selection:
 - 2> initiate a URA update procedure according to subclause 8.3.1 using the cause "URA reselection";
 - 2> when the URA update procedure is successfully completed:
 - 3> the procedure ends.

If after state transition the UE enters CELL_PCH state from CELL_DCH state, the UE shall, after the state transition and transmission of the response message:

- 1> if the IE "Frequency info" is included in the received reconfiguration message:
 - 2> select a suitable UTRA cell according to [4] on that frequency.
 - 2> if the UE finds a suitable UTRA cell on that frequency:
 - 3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selects another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):
 - 4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 4> proceed as below.
 - 2> else, if the UE can not find a suitable UTRA cell on that frequency but it finds a suitable UTRA cell on another frequency:
 - 3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 3> proceed as below.
- 1> if the IE "Frequency info" is not included in the received reconfiguration message:
 - 2> select a suitable UTRA cell according to [4].
 - 2> if the UE finds a suitable UTRA cell on the current frequency:
 - 3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selects another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):
 - 4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 4> proceed as below.
 - 2> else, if the UE can not find a suitable UTRA cell on the current frequency but it finds a suitable UTRA cell on another frequency:
 - 3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 3> proceed as below.
- 1> prohibit periodical status transmission in RLC;
- 1> remove any C-RNTI from MAC;
- 1> clear the variable C_RNTI;
- 1> start timer T305 using its initial value if timer T305 is not running and if periodical update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity" in the variable TIMERS_AND_CONSTANTS;

- 1> select Secondary CCPCH according to subclause 8.5.19;
- 1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:
 - 2> use the value in the IE "UTRAN DRX Cycle length coefficient" for calculating Paging occasion and PICH Monitoring Occasion as specified in subclause 8.6.3.2.
- 1> the procedure ends.

If after state transition the UE enters CELL_PCH state from CELL_FACH state, the UE shall, after the state transition and transmission of the response message:

- 1> if the IE "Frequency info" is included in the received reconfiguration message:
 - 2> select a suitable UTRA cell according to [4] on that frequency.
 - 2> if the UE finds a suitable UTRA cell on that frequency:
 - 3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selected another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):
 - 4> initiate a cell update procedure according to subclause 8.3.1 using the cause "cell reselection";
 - 4> proceed as below.
 - 2> else, if the UE can not find a suitable UTRA cell on that frequency but it finds a suitable UTRA cell on another frequency:
 - 3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 3> proceed as below.
- 1> if the IE "Frequency info" is not included in the received reconfiguration message:
 - 2> if the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD) is included the UE shall either:
 - 3> ignore the content of the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD) and proceed as below;

2> or:

- 3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CPCH info" (for TDD), and it is different from the current cell:
 - 4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 4> proceed as below.
- 1> prohibit periodical status transmission in RLC;
- 1> remove any C-RNTI from MAC;
- 1> clear the variable C_RNTI;
- 1> start timer T305 using its initial value if timer T305 is not running and if periodical update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity" in the variable TIMERS_AND_CONSTANTS;
- 1> select Secondary CCPCH according to subclause 8.5.19;
- 1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:
 - 2> use the value in the IE "UTRAN DRX Cycle length coefficient" for calculating Paging occasion and PICH Monitoring Occasion as specified in subclause 8.6.3.2.

1> the procedure ends.

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Reason for change: ℜ	In the current specification it is not completely clear whether the UE should re- start T305 in case of a state transition from CELL_FACH state to CELL_PCH/URA_PCH state. In section 13.1 it is indicated that the UE should stop T305 when "entering another state". Section 8 only mentions the UE stopping of T305 at the initiation of CELL UPDATE/URA UPDATE procedure.
	See R2-042030 for more detail.
Summary of change: #	It is proposed to specify that the UE shall stop T305 when it leaves CELL_FACH state.
Consequences if % not approved:	Two possibilites of UE implementation remain in the specification.
	Isolated impact analysis:
	This CR only affects UE implementation.
	Impact on test specifications:
	No impact. The test case 8.3.1.4 in 34.123-1 already assumes the UE re-starts
	T305 upon a state transition from CELL_FACH to CELL_PCH.
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Clauses affected: % 8.2.2.3

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Other specs affected:	ж	X X X	Other core specifications Test specifications O&M Specifications	Ħ	
Other comments:	ж				

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at <u>http://www.3gpp.org/specs/CR.htm</u>. Below is a brief summary:

- 1) Fill out the above form. The symbols above marked **#** contain pop-up help information about the field that they are closest to.
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under <u>ftp://ftp.3gpp.org/specs/</u> For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.
- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

8.2.2.3 Reception of RADIO BEARER SETUP or RADIO BEARER RECONFIGURATION or RADIO BEARER RELEASE or TRANSPORT CHANNEL RECONFIGURATION or PHYSICAL CHANNEL RECONFIGURATION message by the UE

The UE shall:

- 1> be able to receive any of the following messages:
 - 2> RADIO BEARER SETUP message; or
 - 2> RADIO BEARER RECONFIGURATION message; or
 - 2> RADIO BEARER RELEASE message; or
 - 2> TRANSPORT CHANNEL RECONFIGURATION message; or
 - 2> PHYSICAL CHANNEL RECONFIGURATION message;
- 1> be able to perform a hard handover and apply physical layer synchronisation procedure A as specified in [29], even if no prior UE measurements have been performed on the target cell and/or frequency.

In case the reconfiguration procedure is used to remove all existing RL(s) in the active set while new RL(s) are established the UE shall:

1> if the UE has a pending "TGPS reconfiguration CFN" at the activation time received in the reconfiguration message and the reconfiguration requests a timing re-initialised hard handover (see subclause 8.3.5.1), the UE may:

2> abort the pending CM activation;

- 2> set the CM_PATTERN_ACTIVATION_ABORTED to TRUE.
- 1> otherwise:
 - 2> set the CM_PATTERN_ACTIVATION_ABORTED to FALSE.

If the UE receives:

- a RADIO BEARER SETUP message; or
- a RADIO BEARER RECONFIGURATION message; or
- a RADIO BEARER RELEASE message; or
- a TRANSPORT CHANNEL RECONFIGURATION message; or
- a PHYSICAL CHANNEL RECONFIGURATION message:

it shall:

- 1> set the variable ORDERED_RECONFIGURATION to TRUE;
- 1> if the UE will enter the CELL_DCH state from any state other than CELL_DCH state at the conclusion of this procedure:
 - 2> perform the physical layer synchronisation procedure A as specified in [29] (FDD only).
- 1> act upon all received information elements as specified in subclause 8.6, unless specified in the following and perform the actions below.

The UE may:

1> maintain a list of the set of cells to which the UE has Radio Links if the IE "Cell ID" is present.

The UE may first release the physical channel configuration used at reception of the reconfiguration message. The UE shall then:

- 1> in FDD, if the IE "PDSCH code mapping" is included but the IE "PDSCH with SHO DCH Info" is not included and if the DCH has only one link in its active set:
 - 2> act upon the IE "PDSCH code mapping" as specified in subclause 8.6; and
 - 2> infer that the PDSCH will be transmitted from the cell from which the downlink DPCH is transmitted.
- 1> enter a state according to subclause 8.6.3.3.

In case the UE receives a RADIO BEARER RECONFIGURATION message including the IE "RB information to reconfigure" that only includes the IE "RB identity", the UE shall:

- 1> handle the message as if IE "RB information to reconfigure" was absent.
- NOTE: The RADIO BEARER RECONFIGURATION message always includes the IE "RB information to reconfigure". UTRAN has to include it even if it does not require the reconfiguration of any RB.

If after state transition the UE enters CELL_DCH state, the UE shall, after the state transition:

- 1> in FDD; or
- 1> in TDD when "Primary CCPCH Info" is included indicating a new target cell and "New C-RNTI" is not specified:

2> remove any C-RNTI from MAC;

2> clear the variable C_RNTI.

If after state transition the UE leaves CELL_DCH state, the UE shall, after the state transition:

- 1> clear any stored IE "Downlink HS-PDSCH information";
- 1> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25.

In FDD, if after state transition the UE leaves CELL_DCH state, the UE shall, after the state transition:

- 1> remove any DSCH-RNTI from MAC;
- 1> clear the variable DSCH_RNTI.

If the UE was in CELL_DCH state upon reception of the reconfiguration message and remains in CELL_DCH state, the UE shall:

1> if the IE "Uplink DPCH Info" is absent, not change its current UL Physical channel configuration;

- 1> in TDD:
 - 2> if "Primary CCPCH Info" is included indicating a new target cell and "New C-RNTI" is not specified:
 - 3> remove any C-RNTI from MAC;
 - 3> clear the variable C_RNTI.
- 1> if "DPCH frame offset" is included for one or more RLs in the active set:
 - 2> use its value to determine the beginning of the DPCH frame in accordance with the following:
 - 3> if the received IE "DPCH frame offset" is across the value range border compared to the DPCH frame offset currently used by the UE:
 - 4> consider it to be a request to adjust the timing with 256 chips across the frame border (e.g. if the UE receives value 0 while the value currently used is 38144 consider this as a request to adjust the timing with +256 chips).

3> if after taking into account value range borders, the received IE "DPCH frame offset" corresponds to a request to adjust the timing with a step exceeding 256 chips:

4> set the variable INVALID_CONFIGURATION to TRUE.

- 3> and the procedure ends.
- 2> adjust the radio link timing accordingly.

If after state transition the UE enters CELL_FACH state, the UE shall, after the state transition:

- 1> if the IE "Frequency info" is included in the received reconfiguration message:
 - 2> select a suitable UTRA cell according to [4] on that frequency;
 - 2> if the UE finds a suitable UTRA cell on that frequency:
 - 3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selects another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):
 - 4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 4> when the cell update procedure completed successfully:
 - 5> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.
 - 2> else, if the UE can not find a suitable UTRA cell on that frequency but it finds a suitable UTRA cell on another frequency:
 - 3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 3> when the cell update procedure completed successfully:
 - 4> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.
- 1> if the IE "Frequency info" is not included in the received reconfiguration message:
 - 2> select a suitable UTRA cell according to [4];
 - 2> if the UE finds a suitable UTRA cell on the current frequency:
 - 3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selects another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):
 - 4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 4> when the cell update procedure completed successfully:
 - 5> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.
 - 2> else, if the UE can not find a suitable UTRA cell on the current frequency but it finds a suitable UTRA cell on another frequency:
 - 3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 3> when the cell update procedure completed successfully:
 - 4> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.

- 1> start timer T305 using its initial value if timer T305 is not running and if periodical update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity" in the variable TIMERS_AND_CONSTANTS;
- 1> select PRACH according to subclause 8.5.17;
- 1> select Secondary CCPCH according to subclause 8.5.19;
- 1> use the transport format set given in system information;
- 1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:
 - 2> ignore that IE and stop using DRX.
- 1> if the contents of the variable C_RNTI is empty:
 - 2> perform a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 2> when the cell update procedure completed successfully:
 - 3> if the UE is in CELL_PCH or URA_PCH state:
 - 4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission";
 - 4> proceed as below.

If the UE was in CELL_FACH state upon reception of the reconfiguration message and remains in CELL_FACH state, the UE shall:

- 1> if the IE "Frequency info" is included in the received reconfiguration message:
 - 2> select a suitable UTRA cell according to [4] on that frequency;
 - 2> if the UE finds a suitable UTRA cell on that frequency:
 - 3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selected another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):
 - 4> initiate a cell update procedure according to subclause 8.3.1 using the cause "cell reselection";
 - 4> when the cell update procedure completed successfully:
 - 5> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.
 - 2> else, if the UE can not find a suitable UTRA cell on that frequency but it finds a suitable UTRA cell on another frequency:
 - 3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 3> when the cell update procedure completed successfully:
 - 4> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.
- 1> if the IE "Frequency info" is not included in the received reconfiguration message:
 - 2> if the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD) is included the UE shall either:
 - 3> ignore the content of the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD) and proceed as below;

2> or:

- 3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CPCH info" (for TDD), and it is different from the current cell:
 - 4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 4> when the cell update procedure completed successfully:
 - 5> if the UE is in CELL_PCH or URA_PCH state, initiate a cell update procedure according to subclause 8.3.1 using the cause "Uplink data transmission" and proceed as below.

If after state transition the UE leaves CELL_FACH state, the UE shall:

1> stop timer T305;

If after state transition the UE enters CELL_PCH or URA_PCH state, the UE shall:

- 1> if the IE "UTRAN DRX cycle length coefficient" is not included in the same message:
 - 2> set the variable INVALID_CONFIGURATION to TRUE.

The UE shall transmit a response message as specified in subclause 8.2.2.4, setting the information elements as specified below. The UE shall:

- 1> if the received reconfiguration message included the IE "Downlink counter synchronisation info"; or
- 1> if the received reconfiguration message is a RADIO BEARER RECONFIGURATION and the IE "New U-RNTI" is included:
 - 2> if the variable PDCP_SN_INFO is empty:
 - 3> configure the corresponding RLC entity for all AM and UM radio bearers and AM and UM signalling radio bearers except RB2 to "stop".
 - 2> else:
 - 3> configure the RLC entity for signalling radio bearers RB1, RB3 and RB4 to "stop";
 - 3> configure the RLC entity for UM and AM radio bearers for which the IE "PDCP SN Info" is not included to "stop".
 - 2> re-establish the RLC entity for RB2;
 - 2> for the downlink and the uplink, apply the ciphering configuration as follows:
 - 3> if the received re-configuation message included the IE "Ciphering Mode Info":
 - 4> use the ciphering configuration in the received message when transmitting the response message.
 - 3> if the ciphering configuration for RB2 from a previously received SECURITY MODE COMMAND has not yet been applied because the activation times not having been reached:
 - 4> if the previous SECURITY MODE COMMAND was received due to new keys being received:
 - 5> consider the new ciphering configuration to include the received new keys;
 - 5> initialise the HFN component of the uplink COUNT-C and downlink COUNT-C of SRB2 as indicated in subclause 8.1.12.3.1.
 - 4> if the ciphering configuration for RB2 from a previously received SECURITY MODE COMMAND has not yet been applied because of the corresponding activation times not having been reached and the previous SECURITY MODE COMMAND caused a change in LATEST_CONFIGURED_CN_DOMAIN:
 - 5> consider the new ciphering configuration to include the keys associated with the LATEST_CONFIGURED_CN_DOMAIN;

5> initialise the HFN component of the uplink COUNT-C and downlink COUNT-C of SRB2 to the most recently transmitted IE "START list" or IE "START" for the

LATEST_CONFIGURED_CN_DOMAIN at the reception of the previous SECURITY MODE COMMAND.

4> apply the new ciphering configuration immediately following RLC re-establishment.

3> else:

4> continue using the current ciphering configuration.

- 2> set the new uplink and downlink HFN component of COUNT-C of RB2 to MAX(uplink HFN component of COUNT-C of RB2, downlink HFN component of COUNT-C of RB2);
- 2> increment by one the downlink and uplink values of the HFN of COUNT-C for RB2;
- 2> calculate the START value according to subclause 8.5.9;
- 2> include the calculated START values for each CN domain in the IE "START list" in the IE "Uplink counter synchronisation info".
- 1> if the received reconfiguration message did not include the IE "Downlink counter synchronisation info":
 - 2> if the variable START_VALUE_TO_TRANSMIT is set:

3> include and set the IE "START" to the value of that variable.

- 2> if the variable START_VALUE_TO_TRANSMIT is not set and the IE "New U-RNTI" is included:
 - 3> calculate the START value according to subclause 8.5.9;
 - 3> include the calculated START values for each CN domain in the IE "START list" in the IE "Uplink counter synchronisation info".
- 2> if the received reconfiguration message caused a change in the RLC size for any RB using RLC-AM:
 - 3> calculate the START value according to subclause 8.5.9;
 - 3> include the calculated START values for the CN domain associated with the corresponding RB identity in the IE "START list" in the IE "Uplink counter synchronisation info".
- 1> if the received reconfiguration message contained the IE "Ciphering mode info" or contained the IE "Integrity protection mode info":
 - 2> set the IE "Status" in the variable SECURITY_MODIFICATION for all the CN domains in the variable SECURITY_MODIFICATION to "Affected".
- 1> if the received reconfiguration message contained the IE "Ciphering mode info":
 - 2> if the reconfiguration message is not used to perform SRNS relocation with change of ciphering algorithm:

3> the UE behaviour is not specified.

- 2> if the message is used to perform a timing re-initialised hard handover:
 - 3> if IE "Ciphering activation time for DPCH" is included:

4> the UE behaviour is not specified.

- 2> else:
 - 3> if the reconfiguration message is used to setup radio bearer(s) using RLC-TM; or
 - 3> if radio bearer(s) using RLC-TM already exist:
 - 4> if IE "Ciphering activation time for DPCH" is not included:
 - 5> the UE behaviour is not specified.

- 2> include and set the IE "Radio bearer uplink ciphering activation time info" to the value of the variable RB_UPLINK_CIPHERING_ACTIVATION_TIME_INFO.
- 1> if the received reconfiguration message did not contain the IE "Ciphering activation time for DPCH" in IE "Ciphering mode info":

2> if prior to this procedure there exist no transparent mode RLC radio bearers:

- 3> if, at the conclusion of this procedure, the UE will be in CELL_DCH state; and
- 3> if, at the conclusion of this procedure, at least one transparent mode RLC radio bearer exists:
 - 4> include the IE "COUNT-C activation time" and specify a CFN value for this IE that is a multiple of 8 frames (CFN mod 8 = 0) and lies at least 200 frames ahead of the CFN in which the response message is first transmitted.
- NOTE: UTRAN should not include the IE "Ciphering mode info" in any reconfiguration message unless it is also used to perform an SRNS relocation with change of ciphering algorithm.
- 1> set the IE "RRC transaction identifier" to the value of "RRC transaction identifier" in the entry for the received message in the table "Accepted transactions" in the variable TRANSACTIONS; and

1> clear that entry;

1> if the variable PDCP_SN_INFO is not empty:

2> include the IE "RB with PDCP information list" and set it to the value of the variable PDCP_SN_INFO.

1> in TDD, if the procedure is used to perform a handover to a cell where timing advance is enabled, and the UE can calculate the timing advance value in the new cell (i.e. in a synchronous TDD network):

2> set the IE "Uplink Timing Advance" according to subclause 8.6.6.26.

- 1> if the IE "Integrity protection mode info" was present in the received reconfiguration message:
 - 2> start applying the new integrity protection configuration in the uplink for signalling radio bearer RB2 from and including the transmitted response message.

If after state transition the UE enters URA_PCH state, the UE shall, after the state transition and transmission of the response message:

- 1> if the IE "Frequency info" is included in the received reconfiguration message:
 - 2> select a suitable UTRA cell according to [4] on that frequency.
 - 2> if the UE can not find a suitable UTRA cell on that frequency but it finds a suitable UTRA cell on another frequency:

3> proceed as below.

- 1> if the IE "Frequency info" is not included in the received reconfiguration message:
 - 2> select a suitable UTRA cell according to [4].
- 1> prohibit periodical status transmission in RLC;
- 1> remove any C-RNTI from MAC;
- 1> clear the variable C_RNTI;
- 1> start timer T305 using its initial value if timer T305 is not running and if periodical update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity" in the variable TIMERS_AND_CONSTANTS;
- 1> select Secondary CCPCH according to subclause 8.5.19;
- 1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:

- 2> use the value in the IE "UTRAN DRX Cycle length coefficient" for calculating Paging occasion and PICH Monitoring Occasion as specified in subclause 8.6.3.2.
- 1> if the criteria for URA update caused by "URA reselection" according to subclause 8.3.1 are fulfilled after cell selection:
 - 2> initiate a URA update procedure according to subclause 8.3.1 using the cause "URA reselection";
 - 2> when the URA update procedure is successfully completed:
 - 3> the procedure ends.

If after state transition the UE enters CELL_PCH state from CELL_DCH state, the UE shall, after the state transition and transmission of the response message:

- 1> if the IE "Frequency info" is included in the received reconfiguration message:
 - 2> select a suitable UTRA cell according to [4] on that frequency.
 - 2> if the UE finds a suitable UTRA cell on that frequency:
 - 3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selects another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):
 - 4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 4> proceed as below.
 - 2> else, if the UE can not find a suitable UTRA cell on that frequency but it finds a suitable UTRA cell on another frequency:
 - 3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 3> proceed as below.
- 1> if the IE "Frequency info" is not included in the received reconfiguration message:
 - 2> select a suitable UTRA cell according to [4].
 - 2> if the UE finds a suitable UTRA cell on the current frequency:
 - 3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selects another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):
 - 4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 4> proceed as below.
 - 2> else, if the UE can not find a suitable UTRA cell on the current frequency but it finds a suitable UTRA cell on another frequency:
 - 3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 3> proceed as below.
- 1> prohibit periodical status transmission in RLC;
- 1> remove any C-RNTI from MAC;
- 1> clear the variable C_RNTI;
- 1> start timer T305 using its initial value if timer T305 is not running and if periodical update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity" in the variable TIMERS_AND_CONSTANTS;

- 1> select Secondary CCPCH according to subclause 8.5.19;
- 1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:
 - 2> use the value in the IE "UTRAN DRX Cycle length coefficient" for calculating Paging occasion and PICH Monitoring Occasion as specified in subclause 8.6.3.2.
- 1> the procedure ends.

If after state transition the UE enters CELL_PCH state from CELL_FACH state, the UE shall, after the state transition and transmission of the response message:

- 1> if the IE "Frequency info" is included in the received reconfiguration message:
 - 2> select a suitable UTRA cell according to [4] on that frequency.
 - 2> if the UE finds a suitable UTRA cell on that frequency:
 - 3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD), and the UE selected another cell than indicated by this IE or the received reconfiguration message did not include the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD):
 - 4> initiate a cell update procedure according to subclause 8.3.1 using the cause "cell reselection";
 - 4> proceed as below.
 - 2> else, if the UE can not find a suitable UTRA cell on that frequency but it finds a suitable UTRA cell on another frequency:
 - 3> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 3> proceed as below.
- 1> if the IE "Frequency info" is not included in the received reconfiguration message:
 - 2> if the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD) is included the UE shall either:
 - 3> ignore the content of the IE "Primary CPICH info" (for FDD) or "Primary CCPCH info" (for TDD) and proceed as below;

2> or:

- 3> if the received reconfiguration message included the IE "Primary CPICH info" (for FDD) or "Primary CPCH info" (for TDD), and it is different from the current cell:
 - 4> initiate a cell update procedure according to subclause 8.3.1 using the cause "Cell reselection";
 - 4> proceed as below.
- 1> prohibit periodical status transmission in RLC;
- 1> remove any C-RNTI from MAC;
- 1> clear the variable C_RNTI;
- 1> start timer T305 using its initial value if timer T305 is not running and if periodical update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity" in the variable TIMERS_AND_CONSTANTS;
- 1> select Secondary CCPCH according to subclause 8.5.19;
- 1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:
 - 2> use the value in the IE "UTRAN DRX Cycle length coefficient" for calculating Paging occasion and PICH Monitoring Occasion as specified in subclause 8.6.3.2.

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1> the procedure ends.

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	CR	-Form-v7							
	CHANGE REQUEST								
[≆] 25	5.331 CR 2437 # rev - ^{# Current version:} 5.10.0	>							
For <u>HELP</u> on using	g this form, see bottom of this page or look at the pop-up text over the $lpha$ symbol	ols.							
Proposed change affe	ects: UICC apps# MEX Radio Access Network Core Netwo	ork							
/ <i>Itle:</i> ж <mark>н</mark> а	andling of pending AM RLC unrecoverable errors signalled by cell update								
Source: ೫ R/	AN WG2								
Work item code: ೫ TE	EI5 Date: # October 7, 2004	1							
Category: % F Use Deta be f	Release: # Rel-5 e one of the following categories: Use one of the following release F (correction) 2 (GSM Phase 2) A (corresponds to a correction in an earlier release) R96 (Release 1996) B (addition of feature), R97 (Release 1997) C (functional modification of feature) R98 (Release 1998) D (editorial modification) R99 (Release 1999) tailed explanations of the above categories can Rel-4 (Release 4) found in 3GPP TR 21.900. Rel-5 (Release 5) K If the first CELL UPDATE message indicating an AM RLC unrecoverable error lost, the UE should continue indicating the error in a subsequent CELL UPD/ message. This behaviour is not clearly specified in the current version of the specification.	es: or is ATE							
Summary of change: ₩	 New RRC variables AM_RLC_ERROR_PENDING_RB234 and AM_RLC_ERROR_PENDING_RB5_AND_UP created to indicate whether ar unrecoverable AM RLC error should be signalled in future CELL UPDATE messages. CELL UPDATE message contents updated to include error indication if these variables are set to TRUE. Added requirement for UE to set the new variables to FALSE on reception of CELL UPDATE CONFIRM message. 	n e f a							
Consequences if # not approved:	Connection could be lost due to UE failing to notify UTRAN of the RLC error.								

Clauses affected:	策 8.3.1.3, 8.3.1.6, 13.4.new1, 13.4.new2
Other specs affected:	YN%XAOther core specifications%XATest specificationsXO&M Specifications
Other comments:	器 Revision of (part of) R2-042037.

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at <u>http://www.3gpp.org/specs/CR.htm</u>. Below is a brief summary:

- 1) Fill out the above form. The symbols above marked # contain pop-up help information about the field that they are closest to.
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under <u>ftp://ftp.3gpp.org/specs/</u> For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.
- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

8.3.1.3 CELL UPDATE / URA UPDATE message contents to set

In case of cell update procedure the UE shall transmit a CELL UPDATE message.

In case of URA update procedure the UE shall transmit a URA UPDATE message.

The UE shall set the IEs in the CELL UPDATE message as follows:

- 1> set the IE "Cell update cause" corresponding to the cause specified in subclause 8.3.1.2 that is valid when the CELL UPDATE message is submitted to lower layers for transmission;
- NOTE: During the time period starting from when a cell update procedure is initiated by the UE until when the procedure ends, additional CELL UPDATE messages may be transmitted by the UE with different causes.
- 1> set the IE "U-RNTI" to the value of the variable U_RNTI;
- 1> if the value of the variable PROTOCOL_ERROR_INDICATOR is TRUE:
 - 2> include the IE "RRC transaction identifier"; and
 - 3> set it to the value of "RRC transaction identifier" in the entry for the CELL UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS.
 - 2> include and set the IE "failure cause" to the cause value "protocol error";

2> set the IE "Protocol error information" set to the value of the variable PROTOCOL_ERROR_INFORMATION.

1> if the value of the variable FAILURE_INDICATOR is TRUE:

2> include the IE "RRC transaction identifier"; and

3> set it to the value of "RRC transaction identifier" in the entry for the CELL UPDATE CONFIRM message in the table "Accepted transactions" in the variable TRANSACTIONS.

2> include and set the IE "failure cause" to the value of the variable FAILURE_CAUSE.

- 1> include the START values for each CN domain, calculated according to subclause 8.5.9;
- 1> if an unrecoverable error [16] in any of the AM RLC entities for the signalling radio bearers RB2, RB3 or RB4 is detected:

2> set the IE "AM_RLC error indication (RB2, RB3 or RB4)" to TRUE:

2> set the variable AM_RLC_ERROR_PENDING_RB234 to TRUE.

1> otherwise:

2> if the value of the variable AM RLC ERROR PENDING RB234 is TRUE:

3> set the IE "AM_RLC error indication (RB2, RB3 or RB4)" to TRUE.

2> otherwise:

23 set the IE "AM_RLC error indication (RB2, RB3 or RB4)" to FALSE.

1> if an unrecoverable error [16] in any of the AM RLC entities for the RB5 or upward is detected:

2> set the IE "AM_RLC error indication (RB>4)" to TRUE:-

2> set the variable AM RLC ERROR PENDING RB5 AND UP to TRUE.

- 1> otherwise:
 - 2> if the value of the variable AM RLC ERROR PENDING RB5 AND UP is TRUE:

3> set the IE "AM RLC error indication (RB>4)" to TRUE.

2> otherwise:

- $\underline{32}$ set the IE "AM_RLC error indication (RB>4)" to FALSE.
- 1> set the IE "RB Timer indicator" to the value of the variable RB_TIMER_INDICATOR;
- 1> if the variable ESTABLISHMENT_CAUSE is initialised:

2> include the IE "Establishment cause" and set it to the value of the variable ESTABLISHMENT_CAUSE.

The UE shall set the IEs in the URA UPDATE message as follows:

- 1> set the IE "U-RNTI" to the value of the variable U_RNTI;
- 1> set the IE "URA update cause" corresponding to which cause as specified in subclause 8.3.1.2 that is valid when the URA UPDATE message is submitted to lower layers for transmission;
- NOTE: During the time period starting from when a URA update procedure is initiated by the UE until when the procedure ends, additional URA UPDATE messages may be transmitted by the UE with different causes, depending on which causes are valid for the respective URA UPDATE message.
 - 2> if the value of the variable PROTOCOL_ERROR_INDICATOR is TRUE:
 - 3> include the IE "RRC transaction identifier"; and
 - 4> set it to the value of "RRC transaction identifier" in the entry for the URA UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS;
 - 3> set the IE "Protocol error indicator" to TRUE;
 - 3> include the IE "Protocol error information" set to the value of the variable PROTOCOL_ERROR_INFORMATION.
 - 2> if the value of the variable PROTOCOL_ERROR_INDICATOR is FALSE:
 - 3> if the value of the variable INVALID_CONFIGURATION is TRUE:
 - 4> include the IE "RRC transaction identifier"; and
 - 4> set it to the value of "RRC transaction identifier" in the entry for the URA UPDATE CONFIRM message in the table "Accepted transactions" in the variable TRANSACTIONS;
 - 4> set the IE "Protocol error indicator" to TRUE;
 - 4> include the IE "Protocol error information" set to "Information element value not comprehended";
 - 3> if the value of the variable INVALID_CONFIGURATION is FALSE:
 - 4> set the IE "Protocol error indicator" to FALSE.

[...]

8.3.1.6 Reception of the CELL UPDATE CONFIRM/URA UPDATE CONFIRM message by the UE

When the UE receives a CELL UPDATE CONFIRM/URA UPDATE CONFIRM message; and

- if the message is received on the CCCH, and IE "U-RNTI" is present and has the same value as the variable U_RNTI; or
- if the message is received on DCCH:
- the UE may:

1> maintain a list of the set of cells to which the UE has Radio Links if the IE "Cell ID" is present.

the UE shall:

- 1> stop timer T302;
- 1> in case of a cell update procedure and the CELL UPDATE CONFIRM message:
 - 2> includes "RB information elements"; and/or
 - 2> includes "Transport channel information elements"; and/or
 - 2> includes "Physical channel information elements"; and
 - 2> if the variable ORDERED_RECONFIGURATION is set to FALSE:

3> set the variable ORDERED_RECONFIGURATION to TRUE.

- 1> act upon all received information elements as specified in subclause 8.6, unless specified otherwise in the following:
 - 2> if the IE "Frequency info" is included in the message:
 - 3> if the IE "RRC State Indicator" is set to the value "CELL_FACH" or "CELL_PCH" or URA_PCH":
 - 4> select a suitable UTRA cell according to [4] on that frequency;
 - 4> act as specified in subclause 8.3.1.12.
 - 3> if the IE "RRC State Indicator" is set to the value "CELL_DCH":

4> act on the IE "Frequency info" as specified in subclause 8.6.6.1.

- 2> use the transport channel(s) applicable for the physical channel types that is used; and
- 2> if the IE "TFS" is neither included nor previously stored in the UE for that transport channel(s):

3> use the TFS given in system information.

- 2> if none of the TFS stored is compatible with the physical channel:
 - 3> delete the stored TFS;
 - 3> use the TFS given in system information.
- 2> if the IE "RLC re-establish indicator (RB2, RB3 and RB4)" in the CELL UPDATE CONFIRM message is set to TRUE:
 - 3> re-establish the RLC entities for signalling radio bearer RB2, signalling radio bearer RB3 and signalling radio bearer RB4 (if established);
 - 3> if the value of the IE "Status" in the variable CIPHERING_STATUS of the CN domain stored in the variable LATEST_CONFIGURED_CN_DOMAIN is set to "Started":
 - 4> set the HFN component of the respective COUNT-C values for AM RLC entities with RB identity 2,RB identity 3 and RB identity 4 (if established) equal to the START value included in the latest transmitted CELL UPDATE message for the CN domain stored in the variable LATEST_CONFIGURED_CN_DOMAIN.
- 2> if the IE "RLC re-establish indicator (RB5 and upwards)" in the CELL UPDATE CONFIRM message is set to TRUE:
 - 3> for radio bearers with RB identity 5 and upwards:
 - 4> re-establish the AM RLC entities;
 - 4> if the value of the IE "Status" in the variable CIPHERING_STATUS of the CN domain as indicated in the IE "CN domain identity" in the IE "RAB info" in the variable ESTABLISHED_RABS is set to "Started":
 - 5> set the HFN component of the respective COUNT-C values for AM RLC entities equal to the START value included in this CELL UPDATE message for the CN domain as indicated in the IE "CN domain identity" in the IE "RAB info" in the variable ESTABLISHED_RABS.
- NOTE: UE actions, in case IE "Downlink counter synchronisation info" is included and either IE "RLC re-establish indicator (RB2, RB3 and RB4)" or IE "RLC re-establish indicator (RB5 and upwards)" are set to TRUE, are not defined.
- 1> if the CELL UPDATE CONFIRM / URA UPDATE CONFIRM message contained the IE "Ciphering mode info" or contained the IE "Integrity protection mode info":
 - 2> set the IE "Status" in the variable SECURITY_MODIFICATION for all the CN domains in the variable SECURITY_MODIFICATION to "Affected".
- 1> if the variable ESTABLISHMENT_CAUSE is set:

2> clear the variable ESTABLISHMENT_CAUSE.

1> enter a state according to subclause 8.6.3.3 applied on the CELL UPDATE CONFIRM / URA UPDATE CONFIRM message.
If the UE after state transition enters CELL_DCH state, it shall:

- 1> perform the physical layer synchronisation procedure A as specified in [29] (FDD only);
- 1> not prohibit periodical status transmission in RLC.

If the UE after state transition remains in CELL_FACH state, it shall

- 1> start the timer T305 using its initial value if timer T305 is not running and periodical cell update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity";
- 1> select PRACH according to subclause 8.5.17;
- 1> select Secondary CCPCH according to subclause 8.5.19;
- 1> not prohibit periodical status transmission in RLC;
- 1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:
 - 2> ignore that IE and stop using DRX.

If the UE after state transition enters URA_PCH or CELL_PCH state, it shall:

- 1> prohibit periodical status transmission in RLC;
- 1> clear the variable C_RNTI;
- 1> stop using that C_RNTI just cleared from the variable C_RNTI in MAC;
- 1> start the timer T305 using its initial value if timer T305 is not running and periodical update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity";
- 1> select Secondary CCPCH according to subclause 8.5.19;
- 1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:
 - 2> use the value in the IE "UTRAN DRX Cycle length coefficient" for calculating Paging Occasion and PICH Monitoring Occasion as specified in subclause 8.6.3.2.
- 1> if the IE "UTRAN DRX cycle length coefficient" is not included in the same message:

2> set the variable INVALID_CONFIGURATION to TRUE.

If the UE after the state transition remains in CELL_FACH state; and

1> the contents of the variable C_RNTI are empty:

it shall check the value of V302; and:

- 1> if V302 is equal to or smaller than N302:
 - 2> if, caused by the received CELL UPDATE CONFIRM or URA UPDATE CONFIRM message:
 - 3> the IE "Reconfiguration" in the variable CIPHERING_STATUS is set to TRUE; and/or
 - 3> the IE "Reconfiguration" in the variable INTEGRITY_PROTECTION_INFO is set to TRUE:
 - 4> abort the ongoing integrity and/or ciphering reconfiguration;
 - 4> if the received CELL UPDATE CONFIRM or URA UPDATE CONFIRM message contained the IE "Ciphering mode info":
 - 5> set the IE "Reconfiguration" in the variable CIPHERING_STATUS to FALSE; and
 - 5> clear the variable RB_UPLINK_CIPHERING_ACTIVATION_TIME_INFO.
 - 4> if the received CELL UPDATE CONFIRM or URA UPDATE CONFIRM message contained the IE "Integrity protection mode info":
 - 5> set the IE "Reconfiguration" in the variable INTEGRITY_PROTECTION_INFO to FALSE; and
 - 5> clear the variable INTEGRITY_PROTECTION_ACTIVATION_INFO.
 - 2> in case of a URA update procedure:

3> stop the URA update procedure;

- 3> clear any entry for the URA UPDATE CONFIRM message in the table "Accepted transactions" in the variable TRANSACTIONS; and
- 3> continue with a cell update procedure.
- 2> set the contents of the CELL UPDATE message according to subclause 8.3.1.3, except for the IE "Cell update cause" which shall be set to "cell reselection";
- 2> submit the CELL UPDATE message for transmission on the uplink CCCH;
- 2> increment counter V302;
- 2> restart timer T302 when the MAC layer indicates success or failure to transmit the message.
- 1> if V302 is greater than N302:
 - 2> clear the variable RB_UPLINK_CIPHERING_ACTIVATION_TIME_INFO;
 - 2> clear the variable INTEGRITY_PROTECTION_ACTIVATION_INFO;
 - 2> in case of a cell update procedure:
 - 3> clear the entry for the CELL UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS.
 - 2> in case of a URA update procedure:
 - 3> clear the entry for the URA UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS.
 - 2> release all its radio resources;
 - 2> indicate release (abort) of the established signalling connections (as stored in the variable ESTABLISHED_SIGNALLING_CONNECTIONS) and established radio access bearers (as stored in the variable ESTABLISHED_RABS) to upper layers;
 - 2> clear the variable ESTABLISHED_SIGNALLING_CONNECTIONS;
 - 2> clear the variable ESTABLISHED_RABS;
 - 2> enter idle mode;
 - 2> other actions the UE shall perform when entering idle mode from connected mode are specified in subclause 8.5.2;
 - 2> and the procedure ends.

If the UE after the state transition remains in CELL_FACH state; and

- a C-RNTI is stored in the variable C_RNTI;

or

the UE after the state transition moves to another state than the CELL_FACH state:

the UE shall:

- 1> if the CELL UPDATE CONFIRM / URA UPDATE CONFIRM message contained the IE "Ciphering mode info":
 - 2> include and set the IE "Radio bearer uplink ciphering activation time info" in any response message transmitted below to the value of the variable RB_UPLINK_CIPHERING_ACTIVATION_TIME_INFO.
- 1> in case cell reselection interrupted an ongoing cell update procedure and a CELL UPDATE CONFIRM/URA UPDATE CONFIRM was received with the IE "Downlink counter synchronisation info" present and the response to which was not submitted to the lower layers due to the cell re-selection:
 - 2> include the IE "START list" in the response message transmitted according to subclause 8.3.1.7;
 - 2> if the CELL UPDATE CONFIRM/URA UPDATE CONFIRM, the response to which was not delivered to the lower layers, due to the cell re-selection, included the IE "RB with PDCP information list":
 - 3> include the IE "RB with PDCP information list" in the response message transmitted according to subclause 8.3.1.7.

- 1> in case of a cell update procedure:
 - 2> set the IE "RRC transaction identifier" in any response message transmitted below to the value of "RRC transaction identifier" in the entry for the CELL UPDATE CONFIRM message in the table "Accepted transactions" in the variable TRANSACTIONS; and

2> clear that entry.

- 1> in case of a URA update procedure:
 - 2> set the IE "RRC transaction identifier" in any response message transmitted below to the value of "RRC transaction identifier" in the entry for the URA UPDATE CONFIRM message in the table "Accepted transactions" in the variable TRANSACTIONS; and

2> clear that entry;

- 1> if the variable PDCP_SN_INFO is non-empty:
 - 2> include the IE "RB with PDCP information list" in any response message transmitted below and set it to the value of the variable PDCP_SN_INFO.
- 1> if the received CELL UPDATE CONFIRM or URA UPDATE CONFIRM message included the IE "Downlink counter synchronisation info":
 - 2> if the variable PDCP_SN_INFO is empty:
 - 3> configure the corresponding RLC entity for all AM and UM radio bearers and AM and UM signalling radio bearers except RB2 to "stop".

2> else:

- 3> configure the RLC entity for signalling radio bearers RB1, RB3 and RB4 to "stop";
- 3> configure the RLC entity for UM and AM radio bearers for which the IE "PDCP SN Info" is not included to "stop".
- 2> re-establish the RLC entity for RB2;
- 2> for the downlink and the uplink, apply the ciphering configuration as follows:
 - 3> if the received re-configuation message included the IE "Ciphering Mode Info":
 - 4> use the ciphering configuration in the received message when transmitting the response message.
 - 3> if the ciphering configuration for RB2 from a previously received SECURITY MODE COMMAND has not yet been applied because the activation times not having been reached:
 - 4> if the previous SECURITY MODE COMMAND was received due to new keys being received:
 - 5> consider the new ciphering configuration to include the received new keys;
 - 5> initialise the HFN component of the uplink COUNT-C and downlink COUNT-C of SRB2 as indicated in subclause 8.1.12.3.1.
 - 4> if the ciphering configuration for RB2 from a previously received SECURITY MODE COMMAND has not yet been applied because of the corresponding activation times not having been reached and the previous SECURITY MODE COMMAND caused a change in LATEST_CONFIGURED_CN_DOMAIN:
 - 5> consider the new ciphering configuration to include the keys associated with the LATEST_CONFIGURED_CN_DOMAIN;
 - 5> initialise the HFN component of the uplink COUNT-C and downlink COUNT-C of SRB2 to the most recently transmitted IE "START list" or IE "START" for the LATEST_CONFIGURED_CN_DOMAIN at the reception of the previous SECURITY MODE COMMAND.
 - 4> apply the new ciphering configuration immediately following RLC re-establishment.

3> else:

4> continue using the current ciphering configuration.

2> set the new uplink and downlink HFN component of the COUNT-C of RB2 to MAX(uplink HFN component of the COUNT-C of RB2, downlink HFN component of the COUNT-C of RB2);

- 2> increment by one the downlink and uplink values of the HFN component of the COUNT-C for RB2;
- 2> calculate the START value according to subclause 8.5.9;
- 2> include the calculated START values for each CN domain in the IE "START list" in the IE "Uplink counter synchronisation info" in any response message transmitted below.
- 1> transmit a response message as specified in subclause 8.3.1.7;
- 1> if the IE "Integrity protection mode info" was present in the CELL UPDATE CONFIRM or URA UPDATE CONFIRM message:
 - 2> start applying the new integrity protection configuration in the uplink for signalling radio bearer RB2 from and including the transmitted response message.
- 1> if the variable ORDERED_RECONFIGURATION is set to TRUE caused by the received CELL UPDATE CONFIRM message in case of a cell update procedure:
 - 2> set the variable ORDERED_RECONFIGURATION to FALSE.
- 1> clear the variable PDCP_SN_INFO;
- 1> when the response message transmitted per subclause 8.3.1.7 to the UTRAN has been confirmed by RLC:
 - 2> if the CELL UPDATE CONFIRM / URA UPDATE CONFIRM message contained the IE "Ciphering mode info":
 - 3> resume data transmission on any suspended radio bearer and signalling radio bearer mapped on RLC-AM or RLC-UM;
 - 3> set the IE "Reconfiguration" in the variable CIPHERING_STATUS to FALSE; and
 - 3> clear the variable RB_UPLINK_CIPHERING_ACTIVATION_TIME_INFO.
 - 2> if the CELL UPDATE CONFIRM / URA UPDATE CONFIRM message contained the IE "Integrity protection mode info":
 - 3> set "Uplink RRC Message sequence number" for signalling radio bearer RB0 in the variable INTEGRITY_PROTECTION_INFO to a value such that next RRC message to be sent on uplink RB0 will use the new integrity protection configuration;
 - 3> allow the transmission of RRC messages on all signalling radio bearers with any RRC SN;
 - 3> set the IE "Reconfiguration" in the variable INTEGRITY_PROTECTION_INFO to FALSE.
 - 2> clear the variable INTEGRITY_PROTECTION_ACTIVATION_INFO.
- 1> in case of a cell update procedure:
 - 2> clear the entry for the CELL UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS.
- 1> in case of a URA update procedure:
 - 2> clear the entry for the URA UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS.
- 1> set the variable CELL_UPDATE_STARTED to FALSE;
- 1> if the variable AM RLC ERROR PENDING RB234 is set to TRUE:
 - 2> set the variable AM_RLC_ERROR_PENDING_RB234 to FALSE;
- 1> if the variable AM_RLC_ERROR_PENDING_RB5_AND_UP is set to TRUE:
 - 2> set the variable AM RLC ERROR PENDING RB5 AND UP to FALSE;
- 1> clear the variable SECURITY_MODIFICATION.

The procedure ends.

13.4 UE variables

[...]

13.4.new1 AM_RLC_ERROR_PENDING_RB234

This variable indicates whether an AM RLC unrecoverable error has been detected during the current cell update procedure on RB 2, 3, or 4.

Information Element/Group name	<u>Need</u>	<u>Multi</u>	Type and reference	Semantics description
AM RLC error pending	MP		<u>Boolean</u>	TRUE means an unrecoverable error was detected on AM RLC during the current cell update procedure Set to FALSE when the cell update procedure is completed.

13.4.new2 AM_RLC_ERROR_PENDING_RB5_AND_UP

This variable indicates whether an AM RLC unrecoverable error has been detected during the current cell update procedure on RB 5 or above.

Information Element/Group name	<u>Need</u>	<u>Multi</u>	Type and reference	Semantics description
AM RLC error pending	MP		<u>Boolean</u>	TRUE means an unrecoverable error was detected on AM RLC during the current cell update procedure Set to FALSE when the cell update procedure is completed.

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CELL UPDATE message contents updated to include error indication if these variables are set to TRUE.

Added requirement for UE to set the new variables to FALSE on reception of a CELL UPDATE CONFIRM message.

Consequences if % Connection could be lost due to UE failing to notify UTRAN of the RLC error. *not approved:*

Clauses affected:	# 8.3.1.3, 8.3.1.6, 13.4.new1, 13.4.new2					
Other specs affected:	Y N % X Other core specifications % X Test specifications X O&M Specifications					
Other comments:	器 Revision of (part of) R2-042037.					

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at <u>http://www.3gpp.org/specs/CR.htm</u>. Below is a brief summary:

- 1) Fill out the above form. The symbols above marked # contain pop-up help information about the field that they are closest to.
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under <u>ftp://ftp.3gpp.org/specs/</u> For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.
- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

8.3.1.3 CELL UPDATE / URA UPDATE message contents to set

In case of cell update procedure the UE shall transmit a CELL UPDATE message.

In case of URA update procedure the UE shall transmit a URA UPDATE message.

The UE shall set the IEs in the CELL UPDATE message as follows:

- 1> set the IE "Cell update cause" corresponding to the cause specified in subclause 8.3.1.2 that is valid when the CELL UPDATE message is submitted to lower layers for transmission;
- NOTE: During the time period starting from when a cell update procedure is initiated by the UE until when the procedure ends, additional CELL UPDATE messages may be transmitted by the UE with different causes.
- 1> set the IE "U-RNTI" to the value of the variable U_RNTI;
- 1> if the value of the variable PROTOCOL_ERROR_INDICATOR is TRUE:
 - 2> include the IE "RRC transaction identifier"; and
 - 3> set it to the value of "RRC transaction identifier" in the entry for the CELL UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS.
 - 2> include and set the IE "failure cause" to the cause value "protocol error";

2> set the IE "Protocol error information" set to the value of the variable PROTOCOL_ERROR_INFORMATION.

1> if the value of the variable FAILURE_INDICATOR is TRUE:

2> include the IE "RRC transaction identifier"; and

3> set it to the value of "RRC transaction identifier" in the entry for the CELL UPDATE CONFIRM message in the table "Accepted transactions" in the variable TRANSACTIONS.

2> include and set the IE "failure cause" to the value of the variable FAILURE_CAUSE.

- 1> include the START values for each CN domain, calculated according to subclause 8.5.9;
- 1> if an unrecoverable error [16] in any of the AM RLC entities for the signalling radio bearers RB2, RB3 or RB4 is detected:

2> set the IE "AM_RLC error indication (RB2, RB3 or RB4)" to TRUE:

2> set the variable AM_RLC_ERROR_PENDING_RB234 to TRUE.

1> otherwise:

2> if the value of the variable AM RLC ERROR PENDING RB234 is TRUE:

3> set the IE "AM_RLC error indication (RB2, RB3 or RB4)" to TRUE.

2> otherwise:

23 set the IE "AM_RLC error indication (RB2, RB3 or RB4)" to FALSE.

1> if an unrecoverable error [16] in any of the AM RLC entities for the RB5 or upward is detected:

2> set the IE "AM_RLC error indication (RB>4)" to TRUE:-

2> set the variable AM RLC ERROR PENDING RB5 AND UP to TRUE.

- 1> otherwise:
 - 2> if the value of the variable AM RLC ERROR PENDING RB5 AND UP is TRUE:

3> set the IE "AM RLC error indication (RB>4)" to TRUE.

2> otherwise:

- $\underline{32}$ set the IE "AM_RLC error indication (RB>4)" to FALSE.
- 1> set the IE "RB Timer indicator" to the value of the variable RB_TIMER_INDICATOR;
- 1> if the variable ESTABLISHMENT_CAUSE is initialised:

2> include the IE "Establishment cause" and set it to the value of the variable ESTABLISHMENT_CAUSE.

The UE shall set the IEs in the URA UPDATE message as follows:

- 1> set the IE "U-RNTI" to the value of the variable U_RNTI;
- 1> set the IE "URA update cause" corresponding to which cause as specified in subclause 8.3.1.2 that is valid when the URA UPDATE message is submitted to lower layers for transmission;
- NOTE: During the time period starting from when a URA update procedure is initiated by the UE until when the procedure ends, additional URA UPDATE messages may be transmitted by the UE with different causes, depending on which causes are valid for the respective URA UPDATE message.
 - 2> if the value of the variable PROTOCOL_ERROR_INDICATOR is TRUE:
 - 3> include the IE "RRC transaction identifier"; and
 - 4> set it to the value of "RRC transaction identifier" in the entry for the URA UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS;
 - 3> set the IE "Protocol error indicator" to TRUE;
 - 3> include the IE "Protocol error information" set to the value of the variable PROTOCOL_ERROR_INFORMATION.
 - 2> if the value of the variable PROTOCOL_ERROR_INDICATOR is FALSE:
 - 3> if the value of the variable INVALID_CONFIGURATION is TRUE:
 - 4> include the IE "RRC transaction identifier"; and
 - 4> set it to the value of "RRC transaction identifier" in the entry for the URA UPDATE CONFIRM message in the table "Accepted transactions" in the variable TRANSACTIONS;
 - 4> set the IE "Protocol error indicator" to TRUE;
 - 4> include the IE "Protocol error information" set to "Information element value not comprehended";
 - 3> if the value of the variable INVALID_CONFIGURATION is FALSE:
 - 4> set the IE "Protocol error indicator" to FALSE.

[...]

8.3.1.6 Reception of the CELL UPDATE CONFIRM/URA UPDATE CONFIRM message by the UE

When the UE receives a CELL UPDATE CONFIRM/URA UPDATE CONFIRM message; and

- if the message is received on the CCCH, and IE "U-RNTI" is present and has the same value as the variable U_RNTI; or
- if the message is received on DCCH:
- the UE may:

1> maintain a list of the set of cells to which the UE has Radio Links if the IE "Cell ID" is present.

the UE shall:

- 1> stop timer T302;
- 1> in case of a cell update procedure and the CELL UPDATE CONFIRM message:
 - 2> includes "RB information elements"; and/or
 - 2> includes "Transport channel information elements"; and/or
 - 2> includes "Physical channel information elements"; and
 - 2> if the variable ORDERED_RECONFIGURATION is set to FALSE:

3> set the variable ORDERED_RECONFIGURATION to TRUE.

- 1> act upon all received information elements as specified in subclause 8.6, unless specified otherwise in the following:
 - 2> if the IE "Frequency info" is included in the message:
 - 3> if the IE "RRC State Indicator" is set to the value "CELL_FACH" or "CELL_PCH" or URA_PCH":
 - 4> select a suitable UTRA cell according to [4] on that frequency;
 - 4> act as specified in subclause 8.3.1.12.
 - 3> if the IE "RRC State Indicator" is set to the value "CELL_DCH":

4> act on the IE "Frequency info" as specified in subclause 8.6.6.1.

- 2> use the transport channel(s) applicable for the physical channel types that is used; and
- 2> if the IE "TFS" is neither included nor previously stored in the UE for that transport channel(s):

3> use the TFS given in system information.

- 2> if none of the TFS stored is compatible with the physical channel:
 - 3> delete the stored TFS;
 - 3> use the TFS given in system information.
- 2> if the IE "RLC re-establish indicator (RB2, RB3 and RB4)" in the CELL UPDATE CONFIRM message is set to TRUE:
 - 3> re-establish the RLC entities for signalling radio bearer RB2, signalling radio bearer RB3 and signalling radio bearer RB4 (if established);
 - 3> if the value of the IE "Status" in the variable CIPHERING_STATUS of the CN domain stored in the variable LATEST_CONFIGURED_CN_DOMAIN is set to "Started":
 - 4> set the HFN component of the respective COUNT-C values for AM RLC entities with RB identity 2,RB identity 3 and RB identity 4 (if established) equal to the START value included in the latest transmitted CELL UPDATE message for the CN domain stored in the variable LATEST_CONFIGURED_CN_DOMAIN.
- 2> if the IE "RLC re-establish indicator (RB5 and upwards)" in the CELL UPDATE CONFIRM message is set to TRUE:
 - 3> for radio bearers with RB identity 5 and upwards:
 - 4> re-establish the AM RLC entities;
 - 4> if the value of the IE "Status" in the variable CIPHERING_STATUS of the CN domain as indicated in the IE "CN domain identity" in the IE "RAB info" in the variable ESTABLISHED_RABS is set to "Started":
 - 5> set the HFN component of the respective COUNT-C values for AM RLC entities equal to the START value included in this CELL UPDATE message for the CN domain as indicated in the IE "CN domain identity" in the IE "RAB info" in the variable ESTABLISHED_RABS.
- NOTE: UE actions, in case IE "Downlink counter synchronisation info" is included and either IE "RLC re-establish indicator (RB2, RB3 and RB4)" or IE "RLC re-establish indicator (RB5 and upwards)" are set to TRUE, are not defined.
- 1> if the CELL UPDATE CONFIRM / URA UPDATE CONFIRM message contained the IE "Ciphering mode info" or contained the IE "Integrity protection mode info":
 - 2> set the IE "Status" in the variable SECURITY_MODIFICATION for all the CN domains in the variable SECURITY_MODIFICATION to "Affected".
- 1> if the variable ESTABLISHMENT_CAUSE is set:

2> clear the variable ESTABLISHMENT_CAUSE.

1> enter a state according to subclause 8.6.3.3 applied on the CELL UPDATE CONFIRM / URA UPDATE CONFIRM message. If the UE after state transition enters CELL_DCH state, it shall:

- 1> perform the physical layer synchronisation procedure A as specified in [29] (FDD only);
- 1> not prohibit periodical status transmission in RLC.

If the UE after state transition remains in CELL_FACH state, it shall

- 1> start the timer T305 using its initial value if timer T305 is not running and periodical cell update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity";
- 1> select PRACH according to subclause 8.5.17;
- 1> select Secondary CCPCH according to subclause 8.5.19;
- 1> not prohibit periodical status transmission in RLC;
- 1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:
 - 2> ignore that IE and stop using DRX.

If the UE after state transition enters URA_PCH or CELL_PCH state, it shall:

- 1> prohibit periodical status transmission in RLC;
- 1> clear the variable C_RNTI;
- 1> stop using that C_RNTI just cleared from the variable C_RNTI in MAC;
- 1> start the timer T305 using its initial value if timer T305 is not running and periodical update has been configured by T305 in the IE "UE Timers and constants in connected mode" set to any other value than "infinity";
- 1> select Secondary CCPCH according to subclause 8.5.19;
- 1> if the IE "UTRAN DRX cycle length coefficient" is included in the same message:
 - 2> use the value in the IE "UTRAN DRX Cycle length coefficient" for calculating Paging Occasion and PICH Monitoring Occasion as specified in subclause 8.6.3.2.
- 1> if the IE "UTRAN DRX cycle length coefficient" is not included in the same message:

2> set the variable INVALID_CONFIGURATION to TRUE.

If the UE after the state transition remains in CELL_FACH state; and

1> the contents of the variable C_RNTI are empty:

it shall check the value of V302; and:

- 1> if V302 is equal to or smaller than N302:
 - 2> if, caused by the received CELL UPDATE CONFIRM or URA UPDATE CONFIRM message:
 - 3> the IE "Reconfiguration" in the variable CIPHERING_STATUS is set to TRUE; and/or
 - 3> the IE "Reconfiguration" in the variable INTEGRITY_PROTECTION_INFO is set to TRUE:
 - 4> abort the ongoing integrity and/or ciphering reconfiguration;
 - 4> if the received CELL UPDATE CONFIRM or URA UPDATE CONFIRM message contained the IE "Ciphering mode info":
 - 5> set the IE "Reconfiguration" in the variable CIPHERING_STATUS to FALSE; and
 - 5> clear the variable RB_UPLINK_CIPHERING_ACTIVATION_TIME_INFO.
 - 4> if the received CELL UPDATE CONFIRM or URA UPDATE CONFIRM message contained the IE "Integrity protection mode info":
 - 5> set the IE "Reconfiguration" in the variable INTEGRITY_PROTECTION_INFO to FALSE; and
 - 5> clear the variable INTEGRITY_PROTECTION_ACTIVATION_INFO.
 - 2> in case of a URA update procedure:

3> stop the URA update procedure;

- 3> clear any entry for the URA UPDATE CONFIRM message in the table "Accepted transactions" in the variable TRANSACTIONS; and
- 3> continue with a cell update procedure.
- 2> set the contents of the CELL UPDATE message according to subclause 8.3.1.3, except for the IE "Cell update cause" which shall be set to "cell reselection";
- 2> submit the CELL UPDATE message for transmission on the uplink CCCH;
- 2> increment counter V302;
- 2> restart timer T302 when the MAC layer indicates success or failure to transmit the message.
- 1> if V302 is greater than N302:
 - 2> clear the variable RB_UPLINK_CIPHERING_ACTIVATION_TIME_INFO;
 - 2> clear the variable INTEGRITY_PROTECTION_ACTIVATION_INFO;
 - 2> in case of a cell update procedure:
 - 3> clear the entry for the CELL UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS.
 - 2> in case of a URA update procedure:
 - 3> clear the entry for the URA UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS.
 - 2> release all its radio resources;
 - 2> indicate release (abort) of the established signalling connections (as stored in the variable ESTABLISHED_SIGNALLING_CONNECTIONS) and established radio access bearers (as stored in the variable ESTABLISHED_RABS) to upper layers;
 - 2> clear the variable ESTABLISHED_SIGNALLING_CONNECTIONS;
 - 2> clear the variable ESTABLISHED_RABS;
 - 2> enter idle mode;
 - 2> other actions the UE shall perform when entering idle mode from connected mode are specified in subclause 8.5.2;
 - 2> and the procedure ends.

If the UE after the state transition remains in CELL_FACH state; and

- a C-RNTI is stored in the variable C_RNTI;

or

the UE after the state transition moves to another state than the CELL_FACH state:

the UE shall:

- 1> if the CELL UPDATE CONFIRM / URA UPDATE CONFIRM message contained the IE "Ciphering mode info":
 - 2> include and set the IE "Radio bearer uplink ciphering activation time info" in any response message transmitted below to the value of the variable RB_UPLINK_CIPHERING_ACTIVATION_TIME_INFO.
- 1> in case cell reselection interrupted an ongoing cell update procedure and a CELL UPDATE CONFIRM/URA UPDATE CONFIRM was received with the IE "Downlink counter synchronisation info" present and the response to which was not submitted to the lower layers due to the cell re-selection:
 - 2> include the IE "START list" in the response message transmitted according to subclause 8.3.1.7;
 - 2> if the CELL UPDATE CONFIRM/URA UPDATE CONFIRM, the response to which was not delivered to the lower layers, due to the cell re-selection, included the IE "RB with PDCP information list":
 - 3> include the IE "RB with PDCP information list" in the response message transmitted according to subclause 8.3.1.7.

- 1> in case of a cell update procedure:
 - 2> set the IE "RRC transaction identifier" in any response message transmitted below to the value of "RRC transaction identifier" in the entry for the CELL UPDATE CONFIRM message in the table "Accepted transactions" in the variable TRANSACTIONS; and

2> clear that entry.

- 1> in case of a URA update procedure:
 - 2> set the IE "RRC transaction identifier" in any response message transmitted below to the value of "RRC transaction identifier" in the entry for the URA UPDATE CONFIRM message in the table "Accepted transactions" in the variable TRANSACTIONS; and

2> clear that entry;

- 1> if the variable PDCP_SN_INFO is non-empty:
 - 2> include the IE "RB with PDCP information list" in any response message transmitted below and set it to the value of the variable PDCP_SN_INFO.
- 1> if the received CELL UPDATE CONFIRM or URA UPDATE CONFIRM message included the IE "Downlink counter synchronisation info":
 - 2> if the variable PDCP_SN_INFO is empty:
 - 3> configure the corresponding RLC entity for all AM and UM radio bearers and AM and UM signalling radio bearers except RB2 to "stop".

2> else:

- 3> configure the RLC entity for signalling radio bearers RB1, RB3 and RB4 to "stop";
- 3> configure the RLC entity for UM and AM radio bearers for which the IE "PDCP SN Info" is not included to "stop".
- 2> re-establish the RLC entity for RB2;
- 2> for the downlink and the uplink, apply the ciphering configuration as follows:
 - 3> if the received re-configuation message included the IE "Ciphering Mode Info":
 - 4> use the ciphering configuration in the received message when transmitting the response message.
 - 3> if the ciphering configuration for RB2 from a previously received SECURITY MODE COMMAND has not yet been applied because the activation times not having been reached:
 - 4> if the previous SECURITY MODE COMMAND was received due to new keys being received:
 - 5> consider the new ciphering configuration to include the received new keys;
 - 5> initialise the HFN component of the uplink COUNT-C and downlink COUNT-C of SRB2 as indicated in subclause 8.1.12.3.1.
 - 4> if the ciphering configuration for RB2 from a previously received SECURITY MODE COMMAND has not yet been applied because of the corresponding activation times not having been reached and the previous SECURITY MODE COMMAND caused a change in LATEST_CONFIGURED_CN_DOMAIN:
 - 5> consider the new ciphering configuration to include the keys associated with the LATEST_CONFIGURED_CN_DOMAIN;
 - 5> initialise the HFN component of the uplink COUNT-C and downlink COUNT-C of SRB2 to the most recently transmitted IE "START list" or IE "START" for the LATEST_CONFIGURED_CN_DOMAIN at the reception of the previous SECURITY MODE COMMAND.
 - 4> apply the new ciphering configuration immediately following RLC re-establishment.

3> else:

4> continue using the current ciphering configuration.

2> set the new uplink and downlink HFN component of the COUNT-C of RB2 to MAX(uplink HFN component of the COUNT-C of RB2, downlink HFN component of the COUNT-C of RB2);

- 2> increment by one the downlink and uplink values of the HFN component of the COUNT-C for RB2;
- 2> calculate the START value according to subclause 8.5.9;
- 2> include the calculated START values for each CN domain in the IE "START list" in the IE "Uplink counter synchronisation info" in any response message transmitted below.
- 1> transmit a response message as specified in subclause 8.3.1.7;
- 1> if the IE "Integrity protection mode info" was present in the CELL UPDATE CONFIRM or URA UPDATE CONFIRM message:
 - 2> start applying the new integrity protection configuration in the uplink for signalling radio bearer RB2 from and including the transmitted response message.
- 1> if the variable ORDERED_RECONFIGURATION is set to TRUE caused by the received CELL UPDATE CONFIRM message in case of a cell update procedure:
 - 2> set the variable ORDERED_RECONFIGURATION to FALSE.
- 1> clear the variable PDCP_SN_INFO;
- 1> when the response message transmitted per subclause 8.3.1.7 to the UTRAN has been confirmed by RLC:
 - 2> if the CELL UPDATE CONFIRM / URA UPDATE CONFIRM message contained the IE "Ciphering mode info":
 - 3> resume data transmission on any suspended radio bearer and signalling radio bearer mapped on RLC-AM or RLC-UM;
 - 3> set the IE "Reconfiguration" in the variable CIPHERING_STATUS to FALSE; and
 - 3> clear the variable RB_UPLINK_CIPHERING_ACTIVATION_TIME_INFO.
 - 2> if the CELL UPDATE CONFIRM / URA UPDATE CONFIRM message contained the IE "Integrity protection mode info":
 - 3> set "Uplink RRC Message sequence number" for signalling radio bearer RB0 in the variable INTEGRITY_PROTECTION_INFO to a value such that next RRC message to be sent on uplink RB0 will use the new integrity protection configuration;
 - 3> allow the transmission of RRC messages on all signalling radio bearers with any RRC SN;
 - 3> set the IE "Reconfiguration" in the variable INTEGRITY_PROTECTION_INFO to FALSE.
 - 2> clear the variable INTEGRITY_PROTECTION_ACTIVATION_INFO.
- 1> in case of a cell update procedure:
 - 2> clear the entry for the CELL UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS.
- 1> in case of a URA update procedure:
 - 2> clear the entry for the URA UPDATE CONFIRM message in the table "Rejected transactions" in the variable TRANSACTIONS.
- 1> set the variable CELL_UPDATE_STARTED to FALSE;
- 1> if the variable AM RLC ERROR PENDING RB234 is set to TRUE:
 - 2> set the variable AM_RLC_ERROR_PENDING_RB234 to FALSE;
- 1> if the variable AM_RLC_ERROR_PENDING_RB5_AND_UP is set to TRUE:
 - 2> set the variable AM RLC ERROR PENDING RB5 AND UP to FALSE;
- 1> clear the variable SECURITY_MODIFICATION.

The procedure ends.

13.4 UE variables

[...]

13.4.new1 AM_RLC_ERROR_PENDING_RB234

This variable indicates whether an AM RLC unrecoverable error has been detected during the current cell update procedure on RB 2, 3, or 4.

Information Element/Group name	<u>Need</u>	<u>Multi</u>	Type and reference	Semantics description
AM RLC error pending	MP		<u>Boolean</u>	TRUE means an unrecoverable error was detected on AM RLC during the current cell update procedure Set to FALSE when the cell update procedure is completed.

13.4.new2 AM_RLC_ERROR_PENDING_RB5_AND_UP

This variable indicates whether an AM RLC unrecoverable error has been detected during the current cell update procedure on RB 5 or above.

Information Element/Group name	<u>Need</u>	<u>Multi</u>	Type and reference	Semantics description
AM RLC error pending	MP		<u>Boolean</u>	TRUE means an unrecoverable error was detected on AM RLC during the current cell update procedure Set to FALSE when the cell update procedure is completed.

3GPP TSG-RAN-WG2 Meeting #45 Shin-Yokohama, Japan, 15-19 October 2004

Tdoc **#***R2-042585*

CHANGE REQUEST								
ж	25.331	CR <mark>2443</mark>	ж геv	- [#]	Current vers	^{ion:} 5.a.0	ж	
For <u>HELP</u> on us	For <u>HELP</u> on using this form, see bottom of this page or look at the pop-up text over the \Re symbols.							
Proposed change affects: UICC apps MEX Radio Access Network Core Network								
Title: ೫	TPC step	size in default conf	igurations					
Source: ೫	RAN WG	2						
Work item code: ℜ	TEI5				<i>Date:</i> ೫	Nov 2004		
Category: ₩	F Use <u>one</u> of F (con A (cor B (add C (fun D (edi Detailed exp be found in	the following categorie rection) responds to a correcti lition of feature), ctional modification of torial modification) blanations of the abov 3GPP <u>TR 21.900</u> .	es: ion in an ear feature) e categories	rlier release s can	Release: # Use <u>one</u> of Ph2 Ph2 R96 R97 R98 R99 Rel-4 Rel-5 Rel-6 Rel-7	Rel-5 the following rele (GSM Phase 2) (Release 1996) (Release 1997) (Release 1998) (Release 1999) (Release 4) (Release 5) (Release 6) (Release 7)	eases:	

Reason for change: #	In section 13.7 of 25.331, the TPC Step size (in the power control algorithm) is shown as 1. It is unclear if this value reflect 1dB (asn1 value of '0') or asn1 value of '1' (which means 2dB). From TS 25.331 Section 13.7 >ul-DPCH-PowerControlInfo >>powerControlAlgorithm Algorithm 1 Algorithm 1 Algorithm 1 Algorithm 1 >>>tpcStepSize 1 1 1 1 From TS 25.331 Section 11.3 Actual value TPC-StepSizeFDD = IE value + 1 TPC-StepSizeFDD ::= INTEGER (01)
Summary of change: ℜ	Clarified that all IEs in the default configurations tables are shown in terms of their real value and not ASN.1. Also added "dB" to the TPC step size.
	Impact Analysis: A UE/UTRAN that has not implemented according to this CR, will require a modification.

Consequences if not approved: Due to a larger TPC step size there would typically be larger UL power spiking and could worsen the UL synchronization times of the UTRAN. This could cause additional UL interference problems to other users.

Clauses affected:	¥ 13.7
Other specs affected:	Y N % Other core specifications % Test specifications ORM Specifications
Other comments:	*

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at <u>http://www.3gpp.org/specs/CR.htm</u>. Below is a brief summary:

- 1) Fill out the above form. The symbols above marked **#** contain pop-up help information about the field that they are closest to.
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under <u>ftp://ftp.3gpp.org/specs/</u> For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.
- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

13.7 Parameter values for default radio configurations

The UE shall support the use of the default radio configurations that are specified in the following.

NOTE 1: These configurations are based on [41] and cover a number of RAB and signalling connection configurations.

In the table that is used to specify the parameter values for these default configurations, the following principles are used:

- Optional IEs that are not used are omitted;
- In case no parameter value is specified in a column, this means the value given the previous (left side) column applies.
- NOTE 2: If needed, signalling radio bearer RB4 is established after the completion of handover.
- NOTE 3: For each default configuration, the value of FDD, 3.84 Mcps TDD and 1.28 Mcps TDD parameters are specified. All parameters apply to FDD, 3.84 Mcps TDD and 1.28 Mcps TDD modes, unless explicitly stated otherwise. It should be noted that in this respect default configurations differ from pre-defined configurations, which only include parameter values for one mode.
- NOTE 4: The transport format sizes, indicated in the following table, concern the RLC PDU size, since all configurations concern dedicated channels. The transport block sizes indicated in TS 34.108 are different since these include the size of the MAC header.

NOTE 5: The tabular values included in this section, represent the actual IE values as in section 10, and not the ASN.1 representation of these values.

Configuration	3.4 kbps signalling	13.6 kbps signalling	7.95 kbps speech + 3.4 kbps signalling	12.2 kbps speech + 3.4 kbps signalling
Ref 34.108	2	3	6	4
Default configuration identity	0	1	2	3
RB INFORMATION				
rb-Identity	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6, RB7: 7
rlc-InfoChoice	Rlc-info	RIc-info	RIc-info	RIc-info
>ul-RLC-Mode	RB1: UM RB2- RB3: AM	RB1: UM RB2- RB3: AM	RB1: UM RB2- RB3: AM RB5-RB6: TM	RB1: UM RB2- RB3: AM RB5-RB7: TM
>>transmissionRLC- DiscardMode	RB1: N/A RB2- RB3: NoDiscard	RB1: N/A RB2- RB3: NoDiscard	RB1: N/A RB2- RB3: NoDiscard RB5- RB6: N/A	RB1: N/A RB2- RB3: NoDiscard RB5- RB7: N/A
>>>maxDat	RB1: N/A RB2- RB3: 15	RB1: N/A RB2- RB3: 15	RB1: N/A RB2- RB3: 15 RB5- RB6: N/A	RB1: N/A RB2- RB3: 15 RB5- RB7: N/A
>>transmissionWindowSiz e	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB6: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB7: N/A
>>timerRST	RB1: N/A RB2- RB3: 300	RB1: N/A RB2- RB3: 300	RB1: N/A RB2- RB3: 300 RB5- RB6: N/A	RB1: N/A RB2- RB3: 300 RB5- RB7: N/A
>>max-RST	RB1: N/A RB2- RB3: 1	RB1: N/A RB2- RB3: 1	RB1: N/A RB2- RB3: 1 RB5- RB6: N/A	RB1: N/A RB2- RB3: 1 RB5- RB7: N/A

Configuration	3.4 kbps signalling	13.6 kbps	7.95 kbps speech	12.2 kbps speech
		signaling	+ 3.4 kbps signalling	+ 3.4 kbps signalling
>>pollingInfo	RB1: N/A RB2- RB3: as below	RB1: N/A RB2- RB3: as below	RB1: N/A RB2- RB3: as below RB5- RB6: N/A	RB1: N/A RB2- RB3: as below RB5- RB7: N/A
>>>lastTransmissionPDU- Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>lastRetransmissionPD U-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300	RB2- RB3: 100	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A	RB1- RB3: N/A	RB1- RB3: N/A RB5- RB6: FALSE	RB1- RB3: N/A RB5- RB7: FALSE
>dl-RLC-Mode	RB1: UM RB2- RB3: AM	RB1: UM RB2- RB3: AM	RB1: UM RB2- RB3: AM RB5- RB6: TM	RB1: UM RB2- RB3: AM RB5- RB7: TM
>>inSequenceDelivery	RB1: N/A RB2- RB3: TRUE	RB1: N/A RB2- RB3: TRUE	RB1: N/A RB2- RB3: TRUE RB5- RB6: N/A	RB1: N/A RB2- RB3: TRUE RB5- RB7: N/A
>>receivingWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB6: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB7: N/A
>>dl-RLC-StatusInfo	RB1: N/A RB2- RB3: as below	RB1: N/A RB2- RB3: as below	RB1: N/A RB2- RB3: as below RB5- RB6: N/A	RB1: N/A RB2- RB3: as below RB5- RB7: N/A
>>>timerStatusProhibit	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100
>>>missingPDU-Indicator	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerStatusPeriodic	RB2- RB3: 300	RB2- RB3: 100	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A	RB1- RB3: N/A	RB1- RB3: N/A RB5- RB6: FALSE	RB1- RB3: N/A RB5- RB7: FALSE
rb-MappingInfo				
>UL- LogicalChannelMappings	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel
>>ul- TransportChannelType	Dch	Dch	Dch	Dch
>>>transportChannelldentit y	RB1- RB3: 1	RB1- RB3: 1	RB1- RB3: 3 RB5: 1, RB6: 2	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3
>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
>>rlc-SizeList	RB1- RB3: configured	RB1- RB3: configured	RB1- RB3: configured RB5- RB6: N/A	RB1- RB3: configured RB5- RB7: N/A
>>mac- LogicalChannelPriority	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: 5	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: 5
>DL- logicalChannelMappingList				
>>Mapping option 1	One mapping option	One mapping option	One mapping option	One mapping option
>>>dl- TransportChannelType	Dch	Dch	Dch	Dch
>>>>transportChannellden tity	RB1- RB3: 1	RB1- RB3: 1	RB1- RB3: 3 RB5: 1, RB6: 2	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3
>>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
TrCH INFORMATION PER TrCH				

Configuration	3.4 kbps signalling	13.6 kbps	7.95 kbps speech	12.2 kbps speech
		signalling	+ 3 4 kbns signalling	+ 3 4 kbps signalling
			5.4 Kops signaling	5.4 Kops signaling
UL- AddReconfTransChInfoList				
>Uplink transport channel type	dch	dch	dch	dch
>transportChannelIdentity	TrCH1: 1	TrCH1: 1	TrCH1: 1, TrCH2: 2, TrCH3: 3	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>transportFormatSet	DedicatedTransChT FS	DedicatedTransChT FS	DedicatedTransChT FS	DedicatedTransChT FS
>>dynamicTF-information				
>>>tf0/ tf0,1	TrCH1: (0x144, 1x144)	TrCH1: (0x144, 1x144)	TrCH1: (0x75) TrCH2: (0x 84 1x84) TrCH3: (0x144, 1x144)	TrCH1: (0x81) TrCH2: (0x 103, 1x103) TrCH3: (0x 60, 1x60) TrCH4: (0x144, 1x144)
>>>>rlcSize	BitMode	BitMode	BitMode	BitMode
>>>>sizeType	TrCH1: type 2, part1= 2, part2= 0 (144)	TrCH1: type 2, part1= 2, part2= 0 (144)	TrCH1: type 1: 75 TrCH2: type 1: 84 TrCH3: 2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 1: 81 TrCH2: type 1: 103 TrCH3: type 1: 60 TrCH4: 2: type 2, part1= 2, part2= 0 (144)
>>>>numberOfTbSizeList	TrCH1: Zero, one	TrCH1: Zero, one	TrCH1: Zero TrCH2-3: Zero, one	TrCH1: Zero TrCH2-4: Zero, one
>>>logicalChannelList	All	All	All	All
>>>tf 1	N/A	N/A	TrCH1: (1x39) TrCH2- TrCH4: N/A	TrCH1: (1x39) TrCH2- TrCH4: N/A
>>>>numberOfTransportBl ocks			TrCH1: One	TrCH1: One
>>>rlc-Size			TrCH1: BitMode	TrCH1: BitMode
>>>>sizeType			TrCH1: 1: 39	TrCH1: 1: 39
>>>numberOfTbSizeList			TrCH1: One	TrCH1: One
>>>logicalChannelList	N1/A	N1/A	TrCH1: all	TrCH1: all
>>>ti 2	N/A	N/A	TrCH1: (1x75) TrCH2- TrCH3: N/A	TrCH1: (1x81) TrCH2- TrCH4: N/A
>>>numberOfTransportBl			IrCH1: One	IrCH1: One
>>>>rlc-Size			TrCH1: BitMode	TrCH1: BitMode
>>>>sizeType			TrCH1: type 1: 75	TrCH1: type 1: 81
>>>numberOfTbSizeList			TrCH1: One	TrCH1: One
>>>logicalChannelList			TrCH1: all	TrCH1: all
>>semistaticTF-Information				
>>>tti	TrCH1: 40	TrCH1: 10	TrCH1- TrCH2: 20 TrCH3: 40	TrCH1- TrCH3: 20 TrCH4: 40
>>>channelCodingType	Convolutional	Convolutional	Convolutional	Convolutional
>>>>codingRate	TrCH1: Third	TrCH1: Third	TrCH1- TrCH2: Third TrCH3: Third	TrCH1- TrCH2: Third TrCH3: Half TrCH4: Third
>>>rateMatchingAttribute	TrCH1: 160	TrCH1: 160	TrCH1: 200 TrCH2: 190 TrCH3: 160	TrCH1: 200 TrCH2: 190 TrCH3: 235 TrCH4: 160
>>>crc-Size	TrCH1: 16	TrCH1: 16	TrCH1: 12 TrCH2: 0 TrCH3: 16	TrCH1: 12 TrCH2- TrCH3: 0 TrCH4: 16
DL-				
>Downlink transport channel type	dch	dch	dch	dch

Configuration	3.4 kbps signalling	13.6 kbps	7.95 kbps speech	12.2 kbps speech
		signalling	+ 3.4 kbps signalling	+ 3.4 kbps signalling
>dl-	TrCH1: 1	TrCH1: 1	TrCH1: 1, TrCH2: 2,	TrCH1: 1, TrCH2: 2,
I ransportChannelIdentity			TrCH3: 3	IrCH3: 3, IrCH4: 4
>tfs-SignallingMode	SameAsUL	SameAsUL	Explicit	Explicit
			<only on="" td="" tf0="" trch1<=""><td><only on="" td="" tf0="" trch1<=""></only></td></only>	<only on="" td="" tf0="" trch1<=""></only>
			is different and	is different and
>>transportFormatSet			DedicatedTransChT	DedicatedTransChT
			FS	FS
>>>dynamicTF-information				
>>>>tf0/ tf0,1			TrCH1: (1x0)	TrCH1: (1x0)
>>>>ricoize			TrCH1: type 1: 0	TrCH1: type 1: 0
>>>numberOfTbSizeList			TrCH1: One	TrCH1: One
>>>logicalChannelList			All	All
>>ULTrCH-Id	TrCH1: 1	TrCH1: 1	TrCH1: 1, TrCH2: 2,	TrCH1: 1, TrCH2: 2,
> deb QualityTarget			TrCH3: 3	TrCH3: 3, TrCH4: 4
>>bler-QualityValue	TrCH1: 5x10 ⁻²	TrCH1: 5x10 ⁻²	TrCH1: 7x10 ⁻³	TrCH1: 7x10 ⁻³
			TrCH2- TrCH3:	TrCH2- TrCH4:
			Absent	Absent
TrCH INFORMATION, COMMON				
ul-CommonTransChInfo				
>tfcs-ID (TDD only)	1	1	1	1
>sharedChannelIndicator	FALSE	FALSE	FALSE	FALSE
>tfc-Subset	Absent, not required	Absent. not required	Absent. not required	Absent, not required
>ul-TFCS	Normal TFCI	Normal TFCI	Normal TFCI	Normal TFCI
	signalling	signalling	signalling	signalling
>>explicitTFCS- ConfigurationMode	Complete	Complete	Complete	Complete
>>>ctfcSize	Ctfc2Bit	Ctfc2Bit	Ctfc4Bit	Ctfc6Bit
>>>>TFCS representation	Addition	Addition	Addition	Addition
>>>>TFCS list				
>>>>>1FCS1	(1F0)	(1F0)	(1F0, 1F0, 1F0)	(TF0, TF0, TF0, TF0)
>>>>>ctfc	0	0	0	0
>>>>>>sgainFactorInform	Computed	Computed	Computed	Computed
>>>>>>>referenceTFCId	0	0	0	0
>>>>>TFCS 2	(TF1)	(TF1)	(TF1, TF0, TF0)	(TF1, TF0, TF0, TF0)
>>>>>ctfc	1	1	1	1
>>>>>gainFactorInform	Signalled	Signalled	Computed	Computed
ation		44	N1/A	N1/A
>>>>>>>βc (FDD only)	11	11	N/A	N/A
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	15	15	N/A	N/A
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	0	0	0 (TF2_TF1_TF0)	0 (TF2_TF1_TF1
			(112, 111, 110)	TF0)
>>>>>ctfc			5	11
>>>>>>sgainFactorInform			Computed	Computed
>>>>>>referenceTFCId			0	0
>>>>>TFCS 4			(TF0, TF0, TF1)	(TF0, TF0, TF0,
			6	1F1) 12
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>			Computed	Computed
ation			comparod	Comparod
>>>>>βc (FDD only)			N/A	N/A

Configuration	3.4 kbps signalling	13.6 kbps	7.95 kbps speech	12.2 kbps speech
		signalling	+ 2.4 kbpc signalling	+ 2.4 kbps signalling
			3.4 Kbps signalling	3.4 Kops signalling
>>>>>Bd			N/A	N/A
>>>>>>referenceTFCId			0	0
>>>>>TFCS 5			(TF1, TF0, TF1)	(TF1, TF0, TF0, TF1)
>>>>>ctfc			7	13
>>>>>>sgainFactorInform ation			Computed	Computed
>>>>>>referenceTFCId			0	0
>>>>>TFCS 6			(TF2, TF1, TF1)	(TF2, TF1, TF1, TF1)
>>>>>ctfc			11	23
>>>>>>gainFactorInform ation			Signalled	Signalled
>>>>>βc (FDD only)			11	11
>>>>>βd			15	15
>>>>>>referenceTFCId			0	0
dl-CommonTransChInfo				
>tfcs-SignallingMode	Same as UL	Same as UL	Same as UL	Same as UL
PhyCH INFORMATION FDD				
UL-DPCH-InfoPredef				
>ul-DPCH-				
PowerControlInfo	Algorithm 1	Algorithm 1	Algorithm 1	Algorithm 1
>>powerControlAigontinin				
>tfci-Existence				
>puncturingLimit	1	1	1	0.88
DL-				
CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>spreadingFactor	256	128	128	128
>>tfci-Existence	FALSE	FALSE	FALSE	FALSE
>>pilotBits	4	4	4	4
>>positionFixed	N/A	N/A	Fixed	Fixed
PhyCH INFORMATION				
3.84 Mcps TDD				
UL-DPCH-InfoPredet				
>UI-DPCH- RowarControllato				
>doch-Constant//alue	0	0	0	0
>commonTimeslotInfo	0	0		0
>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>tfci-Coding	4	4	16	16
>>puncturingLimit	1	0.92	0.52	0.88
>>repetitionPeriodAndLeng th	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
DL-				
CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>commonTimeslotInfo				
>>secondInterleavingMod e	frameRelated	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	4	4	16	16
>>>puncturingLimit	1	0.92	0.52	0.92
>>>repetitionPeriodAndLe ngth	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
PhyCH INFORMATION				
UL-DPCH-InfoPredef				

Configuration	3.4 kbps signalling	13.6 kbps signalling	7.95 kbps speech +	12.2 kbps speech +
			3.4 kbps signalling	3.4 kbps signalling
>commonTimeslotInfo				
>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>tfci-Coding	4	4	16	16
>>puncturingLimit	1	0.64	0.80	0.60
>>repetitionPeriodAndLeng	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
th				
DL-				
CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>commonTimeslotInfo				
>>>secondInterleavingMod	frameRelated	frameRelated	frameRelated	frameRelated
е				
>>>tfci-Coding	4	4	16	16
>>>puncturingLimit	1	0.64	0.80	0.60
>>>repetitionPeriodAndLe ngth	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1

Configuration	28.8 kbps conv.	32 kbps conv. CS-	64kbps conv. CS-	14.4 kbps
	CS- data +	data +	data +	streaming CS-
	3.4 kbps signalling	3.4 kbps signalling	3.4 kbps signalling	data +
				3.4 kbps signalling
Ref 34.108	12	14	13	15
Default configuration	4	5	6	7
identity				
RB INFORMATION				
rb-Identity	RB1: 1, RB2: 2,			
	RB3: 3, RB5: 5			
rlc-InfoChoice	RIc-info	RIc-info	RIc-info	RIc-info
>ul-RLC-Mode	RB1: UM	RB1: UM	RB1: UM	RB1: UM
	RB2- RB3: AM	RB2- RB3: AM	RB2- RB3: AM	RB2- RB3: AM
	RB5: TM	RB5: TM	RB5: TM	RB5: TM
>>transmissionRLC-	RB1: N/A	RB1: N/A	RB1: N/A	RB1: N/A
DiscardMode	RB2- RB3:	RB2- RB3:	RB2- RB3:	RB2- RB3:
	NoDiscard	NoDiscard	NoDiscard	NoDiscard
	RB5: N/A	RB5: N/A	RB5: N/A	RB5: N/A
>>>maxDat	RB1: N/A	RB1: N/A	RB1: N/A	RB1: N/A
	RB2- RB3: 15	RB2- RB3: 15	RB2- RB3: 15	RB2- RB3: 15
	RB5: N/A	RB5: N/A	RB5: N/A	RB5: N/A
>>transmissionWindowSiz	RB1: N/A	RB1: N/A	RB1: N/A	RB1: N/A
е	RB2- RB3: 128 for			
	UEs with more than			
	10 kbyte "total RLC			
	AM buffer size" and			
	32 otherwise	32 otherwise	32 otherwise	32 otherwise
	RB5: N/A	RB5: N/A	RB5: N/A	RB5: N/A
>>timerRST	RB1: N/A	RB1: N/A	RB1: N/A	RB1: N/A
	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
	RB5: N/A	RB5: N/A	RB5: N/A	RB5: N/A
>>max-RST	RB1: N/A	RB1: N/A	RB1: N/A	RB1: N/A
	RB2- RB3: 1	RB2- RB3: 1	RB2- RB3: 1	RB2- RB3: 1
	RB5: N/A	RB5: N/A	RB5: N/A	RB5: N/A
>>pollingInfo	RB1: N/A	RB1: N/A	RB1: N/A	RB1: N/A
-	RB2- RB3: as below			
	RB5: N/A	RB5: N/A	RB5: N/A	RB5: N/A
>>>lastTransmissionPDU- Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>lastRetransmissionPD U-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A	RB1- RB3: N/A	RB1- RB3: N/A	RB1- RB3: N/A
, , , , , , , , , , , , , , , , , , ,	RB5: FALSE	RB5: FALSE	RB5: FALSE	RB5: FALSE

Configuration	28.8 kbps conv.	32 kbps conv. CS-	64kbps conv. CS-	14.4 kbps
	CS- data +	data +	data +	streaming CS-
	3.4 kbps signalling	3.4 kbps signalling	3.4 kbps signalling	data +
				3.4 kbps signalling
>dl-RLC-Mode	RB1: UM	RB1: UM	RB1: UM	RB1: UM
	RB2- RB3: AM	RB2- RB3: AM	RB2- RB3: AM	RB2- RB3: AM
in One manage Delitere ma	RB5: TM	RB5: TM	RB5: TM	RB5: TM
>>InSequenceDelivery	RB1: N/A	RB1: N/A	RB1: N/A	RB1: N/A
	RDZ- RDS. IRUE	RDZ- RDS. IRUE	RDZ- RDS. IRUE	RDZ- RDJ. IRUE RB5· N/A
>>receivingWindowSize	RB1: N/A	RB1: N/A	RB1: N/A	RB1: N/A
>>receivingvindowSize	RB2- RB3 128 for	RB2- RB3 128 for	RB2- RB3 128 for	RB2- RB3 128 for
	UEs with more than	UEs with more than	UEs with more than	UEs with more than
	10 kbyte "total RLC	10 kbyte "total RLC	10 kbyte "total RLC	10 kbyte "total RLC
	AM buffer size" and	AM buffer size" and	AM buffer size" and	AM buffer size" and
	32 otherwise	32 otherwise	32 otherwise	32 otherwise
	RB5: N/A	RB5: N/A	RB5: N/A	RB5: N/A
>>dl-RLC-StatusInfo	RB1: N/A	RB1: N/A	RB1: N/A	RB1: N/A
	RB2- RB3: as below	RB2- RB3: as below	RB2- RB3: as below	RB2- RB3: as below
	RB5: N/A	RB5: N/A	RB5: N/A	RB5: N/A
>>>timerStatusProhibit	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100
>>>missingPDU-Indicator	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerStatusPeriodic	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A	RB1- RB3: N/A	RB1- RB3: N/A	RB1- RB3: N/A
uh Manania altafa	RB5: FALSE	RB5: FALSE	RB5: FALSE	RB5: FALSE
	Onal agicalChangel	Onal agicalChangel	Onal agicalChangel	Onel ericelChannel
>UL-	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel
LogicalChannelMappings	Deb	Deb	Deb	Deb
>>ui- TransportChannelType	DCH	DCII	DCII	DCH
>>>transportChannelldenti	RB1- RB3· 2	RB1- RB3· 2	RB1- RB3· 2	RB1- RB3· 2
ty	RB5: 1	RB5: 1	RB5: 1	RB5: 1
>>logicalChannelIdentity	RB1: 1 RB2: 2	RB1: 1 RB2: 2	RB1: 1 RB2: 2	RB1: 1 RB2: 2
2 / logical chain london adv	RB3: 3	RB3: 3	RB3: 3	RB3: 3
	RB5: N/A	RB5: N/A	RB5: N/A	RB5: N/A
>>rlc-SizeList	RB1- RB3:	RB1- RB3:	RB1- RB3:	RB1- RB3:
	configured	configured	configured	configured
	RB5: N/A	RB5: N/A	RB5: N/A	RB5: N/A
>>mac-	RB1: 1, RB2: 2,	RB1: 1, RB2: 2,	RB1: 1, RB2: 2,	RB1: 1, RB2: 2,
LogicalChannelPriority	RB3: 3	RB3: 3	RB3: 3	RB3: 3
	RB5: 5	RB5: 5	RB5: 5	RB5: 5
>DL-				
	On a manufacture station	On a manufacture station	On a manufacture station	On a manufacture station
>>Mapping option 1	One mapping option	One mapping option	One mapping option	One mapping option
>>>0I-	DCN	DCN	DCN	DCN
tity	RB5- 1	RB5- 1	RB5- 1	RB5- 1
	RB1: 1 RB2: 2	RB1: 1 RB2: 2	RB1: 1 RB2: 2	RB1: 1 RB2: 2
	RB3: 3	RB3: 3	RB3: 3	RB3: 3
	RB5: N/A	RB5: N/A	RB5: N/A	RB5: N/A
TrCH INFORMATION PER				
TrCH				
UL-				
AddReconfTransChInfoLis				
t				
>Uplink transport channel	dch	dch	dch	dch
type				
>transportChannelldentity	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2
>transportFormatSet	DedicatedTransChT	DedicatedTransChT	DedicatedTransChT	DedicatedTransChT
	15	15	15	FS
>>aynamic I F-information				T-014. (0.570
>>>tfU/ tfU,1	1/CH1: (UX5/6,	1rCH1: (0x640,	TrCH1: (0x640,	17CH1: (UX576,
	1X370, 2X576) TrCH2: (0 $\times 1.44$	1X04U) TrCH2: (0v144	∠X04U) TrCH2: (0v144	1X370) TrCH2: (0v144
	1v144, 1v144)	1v144, 1v144)	1v144, 1v144)	1x144, 1x144)
	דדואו)	יאין	דדואי)	יאין

Configuration	28.8 kbps conv.	32 kbps conv. CS-	64kbps conv. CS-	14.4 kbps
_	CS- data +	data +	data +	streaming CS-
	3.4 kbps signalling	3.4 kbps signalling	3.4 kbps signalling	data +
				3.4 kbps signalling
>>>rlcSize	TrCH1: OctetMode	TrCH1: OctetMode	TrCH1: OctetMode	TrCH1: OctetMode
>>>>sizeType	TrCH1: type 2	TrCH1: type 2	TrCH1: type 2	TrCH1: type 2
2222312CT ypc	part1 = 9 part2 = 2	part1 = 11 part2 = 2	part1 = 11 part2 = 2	part1=9
	(576)	(640)	(640)	part2= 2 (576)
	TrCH2: type 2,	TrCH2: type 2,	TrCH2: type 2,	TrCH2: type 2,
	part1= 2, part2= 0	part1= 2, part2= 0	part1= 2, part2= 0	part1= 2,
	(144)	(144)	(144)	part2= 0 (144)
>>>>numberOfTbSizeList	TrCH1: Zero,1, 2	TrCH1: Zero, one	TrCH1: Zero, 2	TrCH1: Zero, one,
	TrCH2: Zero, one	TrCH2: Zero, one	TrCH2: Zero, one	TrCH2: Zero, one
>>>>logicalChannelList	All	All	All	All
>>semiStaticTF-				
Information	T OLIA 40	FOLK 00	FOLK 00	T OLIVA 40
>>>tti	TrCH1: 40 TrCH2: 40	TrCH1: 20 TrCH2: 40	TrCH1: 20 TrCH2: 40	TrCH1: 40 TrCH2: 40
>>>channelCodingType	TrCH1: Turbo	TrCH1: Turbo	TrCH1: Turbo	TrCH1: Turbo
3 /1	TrCH2:	TrCH2:	TrCH2:	TrCH2:
	Convolutional	Convolutional	Convolutional	Convolutional
>>>>codingRate	TrCH1: N/A	TrCH1: N/A	TrCH1: N/A	TrCH1: N/A
-	TrCH2: Third	TrCH2: Third	TrCH2: Third	TrCH2: Third
>>>rateMatchingAttribute	TrCH1: 180	TrCH1: 185	TrCH1: 170	TrCH1: 165
	TrCH2: 160	TrCH2: 160	TrCH2: 160	TrCH2: 160
>>>crc-Size	TrCH1: 16	TrCH1: 16	TrCH1: 16	TrCH1: 16
	TrCH2: 16	TrCH2: 16	TrCH2: 16	TrCH2: 16
DL-				
AddRecont I ransChintoLis				
I Downlink transport	dob	dab	dab	dob
>Downlink transport	den	uch	uch	ach
TransportChannelIdentity	110111. 1, 110112. 2	110111. 1, 110112. 2	110111. 1, 110112. 2	110111. 1, 110112. 2
(should be as for UL)				
>tfs-SignallingMode	SameAsUL	SameAsUL	SameAsUL	SameAsUL
>>transportFormatSet				
>>>dvnamicTF-information				
>>>>tf0/ tf0,1				
>>>rlcSize				
>>>>sizeType				
>>>>numberOfTbSizeList				
>>>>logicalChannelList				
>>ULTrCH-Id	TrCH1: 1, TrCH2: 2			
>dch-QualityTarget	2	2	0	2
>>bler-QualityValue	TrCH1: 2x10 ⁻³	TrCH1: 2x10 ⁻³	TrCH1: 2x10 ⁻³	TrCH1: 1x10 ⁻²
	TrCH2: Absent	TrCH2: Absent	TrCH2: Absent	TrCH2: Absent
TrCH INFORMATION,				
ul-Common I ransChinto	4	4	4	4
>tics-ID (IDD only)				
<pre>>snaredChannelindicator (TDD colv)</pre>	FALSE	FALSE	FALSE	FALSE
(TDD OIlly)	Abcont not required	Abcont not required	Abcont not required	Abcont not required
	Normal TECI	Normal TECI	Normal TECI	Normal TECI
	signalling	signalling	signalling	signalling
>>explicitTFCS-	Complete	Complete	Complete	Complete
ConfigurationMode			Complete	Complete
>>>ctfcSize	Ctfc4Bit	Ctfc2Bit	Ctfc2Bit	Ctfc4Bit
>>>>TFCS representation	Addition	Addition	Addition	Addition
>>>>TFCS list				
>>>>>TFCS 1	(TF0, TF0)	(TF0, TF0)	(TF0, TF0)	(TF0, TF0)
>>>>>>ctfc	0	0	0	0
>>>>>gainFactorInform	Computed	Computed	Computed	Computed
ation	•	•	•	

Configuration	28.8 kbps conv.	32 kbps conv. CS-	64kbps conv. CS-	14.4 kbps
	3.4 kbps signalling	data + 3.4 kbps signalling	data + 3.4 kbps signalling	streaming CS- data +
	· · · · · · · · · · · · · · · · · · ·	5		3.4 kbps signalling
>>>>>>referenceTFCId	0	0	0	0
>>>>>TFCS 2	(TF1, TF0)	(TF1, TF0)	(TF1, TF0)	(TF1, TF0)
>>>>>>>ctfc	1 Operational	1	1 Oceanousta d	1
>>>>>>>sgainFactorinform	Computed	Computed	Computed	Computed
>>>>>>Bc (FDD only)	N/A	N/A	N/A	N/A
>>>>>>>Bd	N/A	N/A	N/A	N/A
>>>>>>referenceTFCId	0	0	0	0
>>>>>TFCS 3	(TF2, TF0)	(TF0, TF1)	(TF0, TF1)	(TF0, TF1)
>>>>>ctfc	2	2	2	2
>>>>>>gainFactorInform ation	Computed	Computed	Computed	Computed
>>>>>>referenceTFCId	0	0	0	0
>>>>>TFCS 4	(TF0, TF1)	(TF1, TF1)	(TF1, TF1)	(TF1, TF1)
>>>>>>ctfc	3	3	3	3
>>>>>>sgainFactorInform ation	Computed	Signalled	Signalled	Signalled
>>>>>>βc (FDD only)	N/A	8	8	11
>>>>>βd	N/A	15	15	15
>>>>>referenceTFCId	0	0	0	0
>>>>>TFCS 5	(TF1, TF1)	N/A	N/A	
>>>>>>ctfc	4			
>>>>>>gainFactorInform ation	Computed			
>>>>>>referenceTFCId	0			
>>>>>TFCS 6	(TF2, TF1)	N/A	N/A	
>>>>>>ctfc	5			
>>>>>>sgainFactorInform ation	Signalled			
>>>>>βc (FDD only)	8			
>>>>>βd	15			
>>>>>>referenceTFCId	0			
>>>>>TFCS 7				
>>>>>>ctfc				
>>>>>>gainFactorInform ation				
>>>>>>referenceTFCId				
>>>>>TFCS 8				
>>>>>>ctfc				
>>>>>>sgainFactorInform ation				
>>>>>>referenceTFCId				
>>>>>TFCS 9				
>>>>>>ctfc				
>>>>>>sgainFactorInform ation				
>>>>>>referenceTFCId				
>>>>>TFCS 10				
>>>>>>ctfc				
>>>>>>sgainFactorInform ation				
>>>>>βc (FDD only)				
>>>>>βd				
>>>>>>referenceTFCId				
dl-CommonTransChInfo				
>tfcs-SignallingMode	Same as UL	Same as UL	Same as UL	Same as UL
PhyCH INFORMATION FDD				

Configuratio	on 2 3.4	8.8 kbps conv. CS- data + kbps signalling	32 kbps conv. CS- data + 3.4 kbps signalling	64kbps conv. CS- data + 3.4 kbps signalling	14.4 kbps streaming CS- data + 3.4 kbps signalling
UI -DPCH-InfoPred	lef				J.4 Kbp3 Signaling
>ul-DPCH- PowerControlInfo					
>>powerControlAld	orithm Alo	porithm 1	Algorithm 1	Algorithm 1	Algorithm 1
>>tpcStepSize	1 0	B	1 dB	1 dB	1 dB
>tfci-Existence	TR	UE	TRUE	TRUE	TRUE
	0.0	12	0.8	0.92	1
DL- CommonInformatic	nPrede				
>dl-DPCH-InfoCon	nmon				
>>spreadingFactor	64		64	32	128
>>tfci-Existence	TR	UE	TRUE	TRUE	TRUE
>>pilotBits	8		8	8	8
>>positionFixed	Fle	exible	Flexible	Flexible	Flexible
PhyCH INFORMA 3.84 Mcps TDD UL-DPCH-InfoPred	FION lef				
>ul-DPCH- PowerControlInfo					
>>dpch-ConstantV	alue 0		0	0	0
>commonTimeslot	nfo				
>>secondInterleav e	ingMod fra	meRelated	frameRelated	frameRelated	frameRelated
>>tfci-Coding	16		8	8	8
>>puncturingLimit	0.4	14	0.8	0.56	0.8
>>repetitionPeriod	AndLen rep	petitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
DL- CommonInformatic f	nPrede				
>dl-DPCH-InfoCon	nmon				
>>commonTimeslo	otInfo				
>>>secondInterlea	vingMo fra	meRelated	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	16		8	8	8
>>>puncturingLimi	t 0.4	14	0.64	0.56	0.8
>>>repetitionPerio	dAndLe rep	petitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
PhyCH INFORMA 1.28 Mcps TDD	TION				
UL-DPCH-InfoPred	lef				
>commonTimeslot	nfo				
>>secondInterleav e	ingMod fra	meRelated	frameRelated	frameRelated	frameRelated
>>tfci-Coding	16		8	8	8
>>puncturinaLimit	0.6	64	0.60	0.64	1
>>repetitionPeriod	AndLen rer	petitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
gth					
DL- CommonInformatic f	nPrede				
>dl-DPCH-InfoCon	nmon				
>>commonTimeslo	otInfo				
>>>secondInterlea	vingMo fra	meRelated	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	16		8	8	8
>>>puncturingLimi	t 0.6	64	0.60	0.64	0.88
>>>repetitionPerio	dAndLe rep	petitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1

Configuration	28.8 kbps streaming CS-	57.6 kbps streaming CS-	12.2 kbps
	data +	data +) +
	3.4 kbps signalling	3.4 kbps signalling	3.4 kbps signalling
Ref 34.108	16	17	1a
Default configuration identity	8	9	10
RB INFORMATION			
rb-Identity	RB1: 1, RB2: 2, RB3: 3, RB5: 5	RB1: 1, RB2: 2, RB3: 3, RB5: 5	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6, RB7: 7
rlc-InfoChoice	Rlc-info	Rlc-info	RIc-info
>ul-RLC-Mode	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5-RB7: TM
>>transmissionRLC-	RB1: N/A	RB1: N/A	RB1: N/A
DiscardMode	RB2- RB3:	RB2- RB3:	RB2- RB3:
	NoDiscard	NoDiscard	NoDiscard
>>>maxDat	RB1: N/A	RB1: N/A	RB1: N/A
	RB2- RB3: 15	RB2- RB3: 15	RB2- RB3: 15
	RB5: N/A	RB5: N/A	RB5- RB7: N/A
>>transmissionWindowSiz e	RB1: N/A RB2- RB3: 128 for	RB1: N/A RB2- RB3: 128 for	RB1: N/A RB2- RB3: 128 for
	UEs with more than 10 kbyte "total RLC	UEs with more than 10 kbyte "total RLC	UEs with more than 10 kbyte "total RLC
	AM buffer size" and	AM buffer size" and	AM buffer size" and
	RB5: N/A	RB5: N/A	RB5- RB7: N/A
>>timerRST	RB1: N/A	RB1: N/A	RB1: N/A
	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
A MOY DOT	RB5: N/A	RB5: N/A	RB5- RB7: N/A
>>IIIax-R31	RB2- RB3: 1	RB2- RB3: 1	RB2- RB3: 1
	RB5: N/A	RB5: N/A	RB5- RB7: N/A
>>pollingInfo	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5- RB7: N/A
>>>lastTransmissionPDU- Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>lastRetransmissionPD U-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A	RB1- RB3: N/A	RB1- RB3: N/A
>dl-BLC-Mode	RDD. FALSE	RDD. FALSE	RDD- RD7. FALSE
	RB2- RB3: AM	RB2- RB3: AM	RB2- RB3: AM
	RB5: TM	RB5: TM	RB5- RB7: TM
>>inSequenceDelivery	RB1: N/A RB2- RB3: TRUE	RB1: N/A RB2- RB3: TRUE	RB1: N/A RB2- RB3: TRUE
	RB5: N/A	RB5: N/A	RB5- RB7: N/A
>>receivingvvindowSize	RB1: N/A RB2- RB3: 128 for	RB1: N/A RB2- RB3: 128 for	RB1: N/A RB2- RB3: 128 for
	UEs with more than	UEs with more than	UEs with more than
	10 kbyte "total RLC	10 kbyte "total RLC	10 kbyte "total RLC
	AM buffer size" and	AM buffer size" and	AM buffer size" and
	RB5: N/A	RB5: N/A	S∠ otherwise RB5- RB7: N/A
>>dl-RLC-StatusInfo	RB1: N/A	RB1: N/A	RB1: N/A
	RB2- RB3: as below RB5: N/A	RB2- RB3: as below RB5: N/A	RB2- RB3: as below RB5- RB7: N/A
>>>timerStatusProhibit	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100
>>>missingPDU-Indicator	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerStatusPeriodic	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
>>segmentationingloation	RB5: FALSE	RB5: FALSE	RB5- RB7: FALSE

Configuration	28.8 kbps streaming CS-	57.6 kbps streaming CS-	12.2 kbps speech(multimode
	3.4 kbps signalling	3.4 kbps signalling	3.4 kbps signalling
rb-MappingInfo			
>UL- LogicalChannelMappings	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel
>>ul- TransportChannelType	Dch	Dch	Dch
>>>transportChannelldenti ty	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3
>>logicalChannelldentity	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
>>rlc-SizeList	RB1- RB3: configured RB5: N/A	RB1- RB3: configured RB5: N/A	RB1- RB3: configured RB5- RB7: N/A
>>mac- LogicalChannelPriority	RB1: 1, RB2: 2, RB3: 3 RB5: 5	RB1: 1, RB2: 2, RB3: 3 RB5: 5	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: 5
>DL- logicalChannelManningList			
>>Mapping option 1	One mapping option	One mapping option	One mapping option
>>>dl- TransportChannelType	Dch	Dch	Dch
>>>>transportChannellden tity	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3
>>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
TrCH INFORMATION PER TrCH			
UL- AddReconfTransChInfoLis t			
>Uplink transport channel type	dch	dch	dch
>transportChannelIdentity	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>transportFormatSet	DedicatedTransChT FS	DedicatedTransChT FS	DedicatedTransChT FS
>>dynamicTF-information			
>>>ttu/ ttu,1	1x576, 2x576) 1x576, 2x576) TrCH2: (0x144, 1x144)	1x576, 2x576, 3x576, 4x576) TrCH2: (0x144, 1x144)	TrCH1: (0x81) TrCH2: (0x 103 TrCH3: (0x 60) TrCH4: (0x144)
>>>rlcSize	TrCH1: OctetMode TrCH2:BitMode	TrCH1: OctetMode TrCH2:BitMode	BitMode
>>>>sizeType >>>>numberOfTbSizeList	TrCH1: type 2, part1= 9, part2= 2 (576) TrCH2: type 2, part1= 2, part2= 0 (144) TrCH1: Zero, one, 2	TrCH1: type 2, part1= 9, part2= 2 (576) TrCH2: type 2, part1= 2, part2= 0 (144) TrCH1: Zero, one,	TrCH1: type 1: 81 TrCH2: type 1: 103 TrCH3: type 1: 60 TrCH4: 2: type 2, part1= 2, part2= 0 (144) TrCH1-4: Zero
	TrCH2: Zero, one	2, 3, 4 TrCH2: Zero, one	
>>>logicalChannelList	All	All	All TrCH1: (1v30)
			TrCH2: (1x53) TrCH3: (1x60) TrCH4: (1x144)

Configuration	28.8 kbps streaming CS-	57.6 kbps streaming CS-	12.2 kbps speech(multimode
	data + 3.4 kbps signalling	data + 3.4 kbps signalling) + 3.4 kbps signalling
>>>>numberOfTransportBl ocks			TrCH1-3: One
>>>rlc-Size			TrCH1-3: BitMode
>>>>sizeType			TrCH1: 1: 39
			TrCH2: 1: 53 TrCH3: 1: 60
>>>>numberOfTbSizeList			TrCH1-3: One
>>>>logicalChannelList			TrCH1-3: all
>>>tf 2			TrCH1: (1x42)
			TrCH2: (1x63)
>>>>numberOfTransportBl			TrCH1-2: One
ocks			
>>>>rlc-Size			TrCH1: BitMode
>>>>sıze l ype			TrCH1: type 1: 42 TrCH2: type 1: 63
>>>>numberOfTbSizeList			TrCH1-2: One
>>>logicalChannelList			TrCH1: all
>>>tf 3			TrCH1: (1x55)
			TrCH2: (1x84)
			TrCH3- TrCH4: N/A
>>>>numberOfTransportBl			IrCH1-2: One
>>>rlc-Size			TrCH1: BitMode
>>>>sizeType			TrCH1: type 1: 55
have a number Of The Sizel int			TrCH2: type 1: 84
>>>>IUIIIDEIOIIDSIZELISI			
			TrCH1: (1x75)
>>>ti 4			TrCH2: (1x103)
			TrCH3- TrCH4: N/A
ocks			TrCH1-2: One
>>>>rlc-Size			TrCH1: BitMode
>>>>sizeType			TrCH1: type 1: 75 TrCH2: type 1: 103
>>>>numberOfTbSizeList			TrCH1-2: One
>>>>logicalChannelList			TrCH1: all
>>>tf 5			TrCH1: (1x81)
			TrCH2- TrCH4: N/A
>>>numberOfTransportBl			IrCH1: One
>>>>rlc-Size			TrCH1: BitMode
>>>>sizeType			TrCH1: type 1: 81
>>>numberOfTbSizeList			IrCH1: One
>>>logicalChannelList			IrCH1: all
>>semiStatic I F- Information			
>>>tti	TrCH1: 40 TrCH2: 40	TrCH1: 40 TrCH2: 40	TrCH1- TrCH3: 20 TrCH4: 40
>>>channelCodingType	TrCH1: Turbo	TrCH1: Turbo	Convolutional
s s s onarnio o o ding i ype	TrCH2:	TrCH2:	Convolutional
>>>> ooding Poto			
>>>coungrate	TrCH2: Third	TrCH2: Third	Third
			TrCH3: Half
			TrCH4: Third
>>>rateMatchingAttribute	TrCH1: 155	TrCH1: 145	TrCH1: 200
	TrCH2: 160	TrCH2: 160	TrCH2: 190
			IrCH3: 235
	<u> </u>	<u> </u>	TrCH4: 160

Configuration	28.8 kbps streaming CS-	57.6 kbps streaming CS-	12.2 kbps speech(multimode
	data + 3.4 kbps signalling	data + 3.4 kbps signalling) + 3.4 kbps signalling
>>>crc-Size	TrCH1: 16 TrCH2: 16	TrCH1: 16 TrCH2: 16	TrCH1: 12 TrCH2- TrCH3: 0 TrCH4: 16
DL- AddReconfTransChInfoLis t			
>Downlink transport channel type	dch	dch	dch
>dl- TransportChannelIdentity (should be as for UL)	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>tfs-SignallingMode	SameAsUL	SameAsUL	Independent <only on="" tf0="" trch1<br="">is different and shown below></only>
>>transportFormatSet			DedicatedTransChT FS
>>>dynamicTF-information			
>>>>tf0/ tf0,1			TrCH1: (1x0)
>>>>rlcSize			bitMode
>>>>sizeType			TrCH1: type 1: 0
>>>>numberOfTbSizeList			TrCH1: One
>>>logicalChannelList			All
>>ULTrCH-Id	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>dch-QualityTarget	-?	-2	-3
>>bler-QualityValue	TrCH1: 1x10 ⁻² TrCH2: Absent	TrCH1: 1x10 ⁻² TrCH2: Absent	TrCH1: 7x10 ° TrCH2- TrCH4: Absent
TrCH INFORMATION, COMMON			
ul-CommonTransChInfo			
>tfcs-ID (TDD only)	1	1	1
>sharedChannelIndicator (TDD only)	FALSE	FALSE	FALSE
>tfc-Subset	Absent, not required	Absent, not required	Absent, not required
>ul-TFCS	Normal TFCI	Normal TFCI	Normal TFCI
	signalling	signalling	signalling
>>explicitTFCS- ConfigurationMode	Complete	Complete	Complete
>>>ctfcSize	Ctfc4Bit	Ctfc4Bit	Ctfc8Bit
>>>>TFCS representation	Addition	Addition	Addition
>>>>TFCS list			
>>>>>TFCS 1	(TF0, TF0)	(TF0, TF0)	(TF0, TF0, TF0, TF0)
>>>>>>ctfc	0	0	0
>>>>>>sgainFactorInform ation	Computed	Computed	Computed
>>>>>>referenceTFCId	0	0	0
>>>>>TFCS 2	(TF1, TF0)	(TF1, TF0)	(TF1, TF0, TF0, TF0)
>>>>>ctfc	1	1	1
>>>>>sgainFactorInform ation	Computed	Computed	Computed
>>>>>Bc (FDD only)	N/A	N/A	N/A
	N/A	N/A	N/A
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	0	0	0
>>>>>TFCS 3	(TF2, TF0)	(TF2, TF0)	(TF2, TF1, TF0, TF0)
>>>>>ctfc	2	2	8

Configuration	28.8 kbps	57.6 kbps	12.2 kbps
	data +	data +) +
	3.4 kbps signalling	3.4 kbps signalling	3.4 kbps signalling
>>>>>>gainFactorInform ation	Computed	Computed	Computed
>>>>>>>referenceTFCId	0	0	0
>>>>>TFCS 4	(TF0, TF1)	(TF3, TF0)	(TF3, TF2, TF0, TF0)
>>>>>ctfc	3	3	15
>>>>>sgainFactorInform ation	Computed	Computed	Computed
>>>>>>Bc (FDD only)	N/A	N/A	N/A
22222228d	N/A	N/A	N/A
>>>>>>>>>>referenceTFCId	0	0	0
>>>>>TFCS 5	(TF1, TF1)	(TF4, TF0)	(TF4, TF3, TF0, TE0)
>>>>>>ctfc	4	4	22
>>>>>sgainFactorInform	Computed	Computed	Computed
>>>>>>>>>referenceTECId	0	0	0
>>>>>TFCS 6	(TF2, TF1)	(TF0, TF1)	(TF5, TF4, TF1,
>>>>>>tfc	5	5	59
>>>>>sgainFactorInform	Signalled	Computed	Computed
ation	8	Ν/Δ	N/Δ
>>>>>>>pc (FDD only)	15	N/A	N/A
>>>>>>pa	15	N/A	N/A
>>>>>>>>reference FCId	0		
>>>>>IFCS /			(1F0,1F0,1F0,1F1)
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>		0 Computed	00 Computed
ation		Computed	Computed
>>>>>>>>reference FCld			
>>>>>IFCS 8		(1F2, 1F1)	(IF1,IF0,IF0,IF1)
>>>>>>CtfC		/ Computed	61 Computed
ation		Computed	Computed
>>>>>>referenceTFCId		0	0
>>>>>TFCS 9		(TF3, TF1)	(TF2,TF1,TF0,TF1)
>>>>>ctfc		8	68
>>>>>>sgainFactorInform ation		Computed	Computed
>>>>>>referenceTFCId		0	0
>>>>>TFCS 10		(TF4, TF1)	(TF3,TF2,TF0,TF1)
>>>>>ctfc		9	75
>>>>>>sgainFactorInform ation		Signalled	Computed
>>>>>Bc (FDD only)		8	N/A
>>>>>>Bd		15	N/A
>>>>>>>>referenceTFCId		0	0
>>>>>TFCS 11			(TF4 TE3 TE0 TE1)
>>>>>>ctfc			82
>>>>>gainFactorInform			Computed
>>>>>>>>referenceTFCId			0
>>>>>TFCS 12			- (TF5.TF4.TF1 TF1)
>>>>>>ctfc			119
>>>>>gainFactorInform			Signalled
~ 0.01			11
ssssssRd			15
>>>>>>>>>>			0

Configuration	28.8 kbps streaming CS- data +	57.6 kbps streaming CS- data +	12.2 kbps speech(multimode
	3.4 kbps signalling	3.4 kbps signalling	3.4 kbps signalling
dl-CommonTransChInfo			
>tfcs-SignallingMode	Same as UL	Same as UL	Same as UL
PhyCH INFORMATION FDD			
UL-DPCH-InfoPredef			
>ul-DPCH-			
PowerControlInfo			
>>powerControlAlgorithm	Algorithm 1	Algorithm 1	Algorithm 1
>>>tpcStepSize	1 <u>dB</u>	1 <u>dB</u>	1 <u>dB</u>
>ttcl-Existence			
DL-		1	0.88
f			
>dl-DPCH-InfoCommon			
>>spreadingFactor	64	32	128
>>tfci-Existence	TRUE	TRUE	FALSE
>>pilotBits	8	8	4
>>positionFixed	Flexible	Flexible	Fixed
PhyCH INFORMATION			
PowerControlInfo			
>>dpch-ConstantValue	0	0	0
>commonTimeslotInfo			
>>secondInterleavingMod	frameRelated	frameRelated	frameRelated
>>tfci-Coding	16	16	16
>>puncturingLimit	0.44	0.48	0.88
>>repetitionPeriodAndLen	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
CommonInformationPrede			
>dl-DPCH-InfoCommon			
>>commonTimeslotInfo			
>>>secondInterleavingMo de	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	16	16	16
>>>puncturingLimit	0.44	0.48	0.92
>>>repetitionPeriodAndLe	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
ngth PhyCH INFORMATION			
1.28 Mcps TDD			
UL-DPCH-InfoPredef			
>common l imesiotinfo	from Deleter	fuere a Distant	
>>secondInterleavingMod e	frameRelated	frameRelated	
>>tfci-Coding	16	16	
>>puncturingLimit	0.64	0.72	
>>repetitionPeriodAndLen gth	repetitionPeriod1	repetitionPeriod1	
DL- CommonInformationPrede			
- >dl-DPCH-InfoCommon			
>>commonTimeslotInfo			
>>>secondInterleavingMo	frameRelated	frameRelated	frameRelated

Configuration	28.8 kbps streaming CS- data + 3.4 kbps signalling	57.6 kbps streaming CS- data + 3.4 kbps signalling	12.2 kbps speech(multimode) + 3.4 kbps signalling
>>>tfci-Coding	16	16	16
>>>puncturingLimit	0.64	0.72	0.92
>>>repetitionPeriodAndLe ngth	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1

Configuration	10.2/6.7/5.9/4.75 kbps speech + 3.4 kbps signalling	7.4/6.7/5.9/4.75 kbps speech + 3.4 kbps signalling
Ref 34 108	N/A	N/A
Default configuration	11	12
identity		
RB INFORMATION		
rb-Identity	RB1: 1, RB2: 2,	RB1: 1, RB2: 2,
2	RB3: 3, RB5: 5,	RB3: 3, RB5: 5,
	RB6: 6, RB7: 7,	RB6: 6, RB7: 7
	RB8: 8	
rlc-InfoChoice	Rlc-info	Rlc-info
>ul-RLC-Mode	RB1: UM	RB1: UM
	RB2- RB3: AM	RB2- RB3: AM
	RB5-RB7: IM	RB5-RB6: TM
>>transmissionRLC-	RB1: N/A	RB1: N/A
Discardividue	NoDiscard	KDZ- KDJ. NoDiscard
	RB5- RB7· N/A	RB5- RB6· N/A
>>>maxDat	RB1· N/A	RB1·N/A
	RB2- RB3: 15	RB2- RB3: 15
	RB5- RB7: N/A	RB5- RB6: N/A
>>transmissionWindowSiz	RB1: N/A	RB1: N/A
е	RB2- RB3: 128 for	RB2- RB3: 128 for
	UEs with more than	UEs with more than
	10 kbyte "total RLC	10 kbyte "total RLC
	AM buffer size" and	AM buffer size" and
	32 otherwise	32 otherwise
time a sport	RB5- RB7: N/A	RB5- RB6: N/A
>>timerRS1	RB1: N/A	RB1: N/A
	RD2- RD3. 300 RB5- RB7· N/A	RD2- RD3. 300 RB5- RB6: N/A
>>max-RST	RB1· N/A	RB1· N/A
>>max=1.01	RB2- RB3: 1	RB2- RB3: 1
	RB5- RB7: N/A	RB5- RB6: N/A
>>pollingInfo	RB1: N/A	RB1: N/A
	RB2- RB3: as below	RB2- RB3: as below
	RB5- RB7: N/A	RB5- RB6: N/A
>>>lastTransmissionPDU- Poll	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>lastRetransmissionPD U-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A	RB1- RB3: N/A
	RB5- RB7: FALSE	RB5- RB6: FALSE
>dI-RLC-Mode	KB1: UM	KB1: UM
	KB2- KB3: AM	KBZ- KB3: AM
	RBD-RB/: IM	КВЭ- КВ0: 111 РВ7: ТМ
>>inSequenceDelivory		
		RB2- RB3. TRUE
	RB5- RB8: N/A	RB5- RB7: N/A
>>receivingWindowSize	RB1: N/A	RB1: N/A
	RB2- RB3: 128 for	RB2- RB3: 128 for
	UEs with more than	UEs with more than
	10 kbyte "total RLC	10 kbyte "total RLC
	AM buffer size" and	AM buffer size" and
	32 otherwise	32 otherwise
	RB5- RB8: N/A	RB5- RB7: N/A
>>dl-RLC-StatusInfo	KB1: N/A	RB1: N/A
	RB2- RB3: as below	RB2- RB3: as below
· · · · timer Ctotus Drokikit	KB2-KB8: N/A	RB2-RB/:N/A
	RDZ- RDJ. 100	RD2- RD3: 100
	RB2- RB2- 200	RB2- RB2- 200

>>segmentationIndication

LogicalChannelMappings

TransportChannelType >>>transportChannelIdentit

>>logicalChannelIdentity

LogicalChannelPriority

>>Mapping option 1

TransportChannelType >>>>transportChannelIden

>>>logicalChannelIdentity

TrCH INFORMATION PER

AddReconfTransChInfoList >Uplink transport channel

>transportChannelIdentity

>>dynamicTF-information

>>>numberOfTbSizeList

>>>>numberOfTransportBl

>>>numberOfTbSizeList

>>>>logicalChannelList

>transportFormatSet

>>>tf0/ tf0,1

>>>>rlcSize

>>>tf 1

ocks

>>>rlc-Size

>>>>sizeType

>>>>sizeType

TrCH3: 3, TrCH4: 4

DedicatedTransChT

TrCH1: (0x65)

TrCH2: (0x 99)

TrCH3: (0x 40,

TrCH4: (0x144, 1x144)

TrCH1: type 1: 65

TrCH2: type 1: 99

TrCH3: type 1: 40 TrCH4: 2: type 2,

part1= 2, part2= 0

TrCH3-4: Zero, one

TrCH3- TrCH4: N/A

TrCH1-2: BitMode

TrCH1-2: Zero

TrCH1: (1x39)

TrCH2: (1x 53)

TrCH1: One

TrCH2: One

TrCH1: 1: 39

TrCH2: 1: 53

TrCH1-2: One

FS

1x40)

BitMode

(144)

All

logicalChannelMappingList

rb-MappingInfo

>>rlc-SizeList

>>mac-

>DL-

>>>dl-

tity

TrCH UL-

type

>UL-

>>ul-

у

YY-MM)		
RB1- RB3: N/A	RB1- RB3: N/A	
RB5- RB8: FALSE	RB5- RB7: FALSE	
OneLogicalChannel	OneLogicalChannel	
Dch	Dch	
RB1- RB3: 4	RB1- RB3: 3	
RB5: 1, RB6: 2, RB7: 3,	RB5: 1, RB6: 2	
RB1: 1, RB2: 2,	RB1: 1, RB2: 2,	
RB3: 3 RB5- RB7: N/A	RB3: 3 RB5- RB6: N/A	
RB1- RB3	RB1- RB3	
configured	configured	
RB5- RB7: N/A	RB5- RB6: N/A	
RB1: 1, RB2: 2,	RB1: 1, RB2: 2,	
RB3: 3	RB3: 3	
RB5- RB7: 5	RB5- RB6: 5	
One mapping option	One mapping option	
Dch	Dch	
RB1- RB3: 4	RB1- RB3: 3	
RB5: 1, RB6: 2,	RB5: 1, RB6: 2,	
RB7: 3, RB8: 5	RB7:4	
RB1: 1, RB2: 2,	RB1: 1, RB2: 2,	
RB3: 3	RB3: 3	
RB5- RB8: N/A	RB5- RB7: N/A	
dch	dch	
TrCH1: 1, TrCH2: 2,	TrCH1: 1, TrCH2: 2,	

TrCH3: 3

FS

1x144)

BitMode

(144)

All

DedicatedTransChT

TrCH1: (0x61)

TrCH2: (0x 87)

TrCH3: (0x 144,

TrCH1: type 1: 61

TrCH2: type 1: 87 TrCH3: 2: type 2,

part1= 2, part2= 0

TrCH3: Zero, one

TrCH1-2: Zero

TrCH1: (1x39)

TrCH2: (1x53) TrCH3: N/A

TrCH1: One

TrCH2: One

TrCH1: 1: 39

TrCH1: 1: 53

TrCH1-2: One

TrCH1-2: BitMode
>>>logicalChannelList	TrCH1: all	TrCH1: all
>>>tf 2	TrCH1: (1x42)	TrCH1: (1x42)
	TrCH2: (1x63)	TrCH2: (1x63)
	TrCH3- TrCH4: N/A	TrCH3: N/A
>>>>numberOfTransportBl	TrCH1: One	TrCH1: One
ocks	TrCh2: One	TrCh2: One
>>>>rlc-Size	TrCH1: BitMode	TrCH1: BitMode
>>>>sizeType	TrCH1: type 1: 42	TrCH1: type 1: 42
51	TrCH2: type 1: 63	TrCH2: type 1: 63
>>>>numberOfTbSizeList	TrCH1: One	TrCH1: One
	TrCH2: One	TrCH2: One
>>>>logicalChannelList	TrCH1: all	TrCH1: all
	TrCH2: all	TrCH2: all
>>>tf 3	TrCH1: (1x55)	TrCH1: (1x55)
	TrCH2: (1x76)	TrCH2: (1x76)
	TrCH3- TrCH4· N/A	TrCH3: N/A
>>>>numberOfTransportBl	TrCH1: One	TrCH1: One
ocks	TrCh2: One	TrCh2: One
>>>rlc-Size	TrCH1: BitMode	TrCH1: BitMode
>>>>Size i ype	TrCH2: type 1: 55	TrCH2: type 1: 55
a a a number Of The Sizel ist		
>>>>numberOrrbSizeList	TrCH1: One	TrCH1: One
>>>>iogicalCnannelList		
>>>tf 4	TrCH1: (1x58)	TrCH1: (1x58)
	TrCH2: (1x99)	TrCH2: (1x87)
	TrCH3- TrCH4: N/A	TrCH3: N/A
>>>>numberOfTransportBl	TrCH1: One	TrCH1: One
OCKS	TrCh2: One	TrCh2: One
>>>>rlc-Size	TrCH1: BitMode	TrCH1: BitMode
>>>>sizeType	TrCH1: type 1: 58	TrCH1: type 1: 58
	TrCH2: type 1: 99	TrCH2: type 1: 87
>>>>numberOfTbSizeList	TrCH1: One	TrCH1: One
	TrCH2: One	TrCH2: One
>>>>logicalChannelList	TrCH1: all	TrCH1: all
	TrCH2: all	TrCH2: all
>>>tf 5	TrCH1: (1x65)	TrCH1: (1x61)
	TrCH2- TrCH4: N/A	TrCH2- TrCH4: N/A
>>>>numberOfTransportBl	TrCH1: One	TrCH1: One
ocks		
>>>rlc-Size	TrCH1: BitMode	TrCH1: BitMode
>>>>sizeType	TrCH1: type 1: 65	TrCH1: type 1: 61
>>>>numberOfTbSizeList	TrCH1: One	TrCH1: One
>>>logicalChannelList	TrCH1: all	TrCH1: all
>>semistaticTF-Information		
>>>tti	TrCH1- TrCH3: 20	TrCH1- TrCH2: 20
	TrCH4: 40	TrCH3: 40
>>>channelCodingType	Convolutional	Convolutional
>>>>codingRate	TrCH1- TrCH2	TrCH1- TrCH2
	Third	Third
	TrCH3 Half	TrCH3. Third
	TrCH4: Third	
>>>rateMatchingAttribute		
	TrCH2: 100	TrCH2: 100
	TrCH3: 225	TrCH3: 160
	TrCH4: 160	
>>>crc-Size		
~~~UU-UI2E		
DI		IIUTIJ. 10
UL- AddDaaanfTrana Oblafal ist		
>Downlink transport	acn	acn
channel type		
>dl-		1
· · · · · · · · · · · · · · · · · · ·		

>tfs-SignallingMode	Independent	Independent
	<only on="" td="" tf0="" trch1<=""><td><only on="" td="" tf0="" trch1<=""></only></td></only>	<only on="" td="" tf0="" trch1<=""></only>
	and tf0/tf1 on	and tf0/tf1 on
	TrCH5 are different	TrCH4 are different
	and shown below>	and shown below>
>>transportFormatSet		
>>>dynamic I F-information		
>>>>tf0/ tf0,1	IrCH1: (1x0)	IrCH1: (1x0)
	TrCH5: (0x3, 1x3)	IrCH4: (0x3, 1x3)
>>>>rlcSize	BitMode	bitMode
>>>>size l ype	TrCH1: type 1: 0	TrCH1: type 1: 0
>>>>numberOf1bSizeList		TrCH1: One
>>> logicalChannell ist		
	All	All
>>>Semistanci F-	for TrCH5	for TrOUA
>>>>lll	Convolutional	Convolutional
>>>>channelCodingType		
>>>>couingKate		
>>>rateiviatchingAttribute	T-CU5: 200	TrCH4: 200
>>>>CIC-SIZe		Troll4: 12
>>ULIICH-Id	ITCH1: 1, ITCH2: 2,	TrCH1: 1, TrCH2: 2,
, dah Qualit Taur (	TICH3: 3, TICH4: 4,	TICH3: 3
>ucn-Quality Larget	T-014, 7 40 ⁻³	T-014.7.10 ⁻³
>>bler-QualityValue		TrCH1: 7x10
	TrCH2- TrCH5:	IrCH2- IrCH4:
	Absent	Absent
COMMON		
ul-Common FransChinio	1	1
>lics-iD (IDD only)		
>snaredChannelindicator	FALSE	FALSE
(TDD only)		
> tic-Subset	Absent, not required	Absent, not required
>ul-TFC5	Normal TFCI	Normal IFCI
	Signalling	Signalling
>>explicit FCS-	Complete	Compiete
	Ctfo0Dit	CitacDit
>>>clicSize		
>>>>TFCS representation	Addition	
		Addition
>>>>TFC list		
>>>>TFC list >>>>>TFC 1	(TF0, TF0, TF0,	(TF0, TF0, TF0)
>>>>TFC list >>>>>TFC 1	(TF0, TF0, TF0, TF0)	(TF0, TF0, TF0)
>>>>TFC list >>>>>TFC 1 >>>>>ctfc	(TF0, TF0, TF0, TF0) 0	(TF0, TF0, TF0)
>>>>TFC list >>>>>TFC 1 >>>>>>ctfc >>>>>>gainFactorInform	(TF0, TF0, TF0, TF0) 0 Computed	(TF0, TF0, TF0) 0 Computed
>>>>TFC list >>>>>TFC 1 >>>>>ctfc >>>>>gainFactorInform ation	(TF0, TF0, TF0, TF0) 0 Computed	(TF0, TF0, TF0) 0 Computed
>>>>TFC list >>>>>TFC 1 >>>>>ctfc >>>>>>gainFactorInform ation >>>>>referenceTFCId	(TF0, TF0, TF0, TF0) 0 Computed 0	(TF0, TF0, TF0) 0 Computed 0
>>>>TFC list >>>>>TFC 1 >>>>>ctfc >>>>>>gainFactorInform ation >>>>>referenceTFCId >>>>>TFC 2	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0)	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0)
>>>>TFC list >>>>>TFC 1 >>>>>ctfc >>>>>>gainFactorInform ation >>>>>referenceTFCId >>>>>TFC 2	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0)	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0)
>>>>TFC list >>>>>TFC 1 >>>>>ctfc >>>>>gainFactorInform ation >>>>>referenceTFCId >>>>>TFC 2 >>>>>ctfc	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0) 1 Computed
>>>>TFC list >>>>>TFC 1 >>>>>gainFactorInform ation >>>>>referenceTFCId >>>>>TFC 2 >>>>>ctfc >>>>>ctfc	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0) 1 Computed
>>>>TFC list >>>>>TFC 1 >>>>>sctfc >>>>>sgainFactorInform ation >>>>>referenceTFCId >>>>>TFC 2 >>>>>ctfc >>>>>ctfc	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0) 1 Computed
>>>>>TFC list         >>>>>TFC 1         >>>>>>gainFactorInform         ation         >>>>>>referenceTFCId         >>>>>TFC 2         >>>>>>tfc         >>>>>>ctfc         >>>>>>TFC 2         >>>>>>>>>>>sgainFactorInform         ation         >>>>>>TFC 2	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0) 1 Computed N/A
>>>>TFC list >>>>>TFC 1 >>>>>ctfc >>>>>gainFactorInform ation >>>>>referenceTFCld >>>>>TFC 2 >>>>>ctfc >>>>>>ctfc >>>>>>gainFactorInform ation >>>>>βc (FDD only) >>>>>βd	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed N/A N/A	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0) 1 Computed N/A N/A
>>>>>TFC list         >>>>>TFC 1         >>>>>sqainFactorInform ation         >>>>>referenceTFCId         >>>>>TFC 2         >>>>>>tfc         >>>>>sqainFactorInform ation         >>>>>stfc         >>>>>bfc (FDD only)         >>>>>>bfd         >>>>>>referenceTFCId	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed N/A N/A 0	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0) 1 Computed N/A N/A 0
>>>>>TFC list         >>>>>TFC 1         >>>>>sqainFactorInform ation         >>>>>referenceTFCId         >>>>>TFC 2         >>>>>>tfc         >>>>>sqainFactorInform ation         >>>>>spainFactorInform ation         >>>>>>bfc (FDD only)         >>>>>>bfd         >>>>>TFC 3	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0,	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0)
>>>>TFC list           >>>>>TFC 1           >>>>>sqainFactorInform ation           >>>>>referenceTFCId           >>>>>TFC 2           >>>>>ctfc           >>>>>>tfc           >>>>>>tfc           >>>>>>tfc           >>>>>>tfc           >>>>>>tfc           >>>>>>tfc           >>>>>>bfc (FDD only)           >>>>>>>>>>>>bfd           >>>>>>TFC 3	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0, TF0, TF0)	Addition(TF0, TF0, TF0)0Computed0(TF1, TF0, TF0)1ComputedN/AN/A0(TF2, TF1, TF0)
>>>>TFC list           >>>>>TFC 1           >>>>>sgainFactorInform ation           >>>>>TFC 2           >>>>>TFC 2           >>>>>>tfc           >>>>>sgainFactorInform ation           >>>>>>tfc           >>>>>stfc           >>>>>bfc (FDD only)           >>>>>>bfd           >>>>>>TFC 3           >>>>>>ctfc	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0, TF0) 8	(TF0, TF0, TF0)         0         Computed         0         (TF1, TF0, TF0)         1         Computed         N/A         N/A         0         (TF2, TF1, TF0)         8
>>>>TFC list           >>>>>TFC 1           >>>>>sqainFactorInform ation           >>>>>referenceTFCId           >>>>>TFC 2           >>>>>sqainFactorInform ation           >>>>>sqainFactorInform ation           >>>>>spainFactorInform ation           >>>>>spainFactorInform ation           >>>>>spactfc           >>>>>spainFactorInform ation           >>>>>>spc (FDD only)           >>>>>>spd           >>>>>>steferenceTFCId           >>>>>>steferenceTFCId           >>>>>>>>>>spd           >>>>>>teferenceTFCId           >>>>>>steferenceTFCId           >>>>>>steferenceTFCId           >>>>>>>>>>>>>>>>>>>>>>>>>>>>	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0, TF0) 8 Computed	Addition(TF0, TF0, TF0)0Computed0(TF1, TF0, TF0)1ComputedN/AN/A0(TF2, TF1, TF0)8Computed
$>>>>TFC list \\ >>>>>TFC 1 \\ \Rightarrow>>>>>sctfc \\ >>>>>gainFactorInform \\ ation \\ >>>>>TFC 2 \\ \Rightarrow>>>>>tFC 2 \\ \Rightarrow>>>>ctfc \\ \Rightarrow>>>>sgainFactorInform \\ ation \\ \Rightarrow>>>>>bfc (FDD only) \\ \Rightarrow>>>>>bfd \\ \Rightarrow>>>>sfeferenceTFCId \\ \Rightarrow>>>>>bfd \\ \Rightarrow>>>>>tFC 3 \\ \Rightarrow>>>>>ctfc \\ \Rightarrow>>>>>ctfc \\ \Rightarrow>>>>>ctfc \\ \Rightarrow>>>>>sgainFactorInform \\ ation \\ \Rightarrow>>>>>bfc (FDD only) \\ \Rightarrow>>>>>bfd \\ \Rightarrow>>>>>bfc (FDD only) \\ \Rightarrow>>>>>bfd \\ \Rightarrow>>>>>bfc (FDD only) \\ \Rightarrow>>>>bfc (FDD only) \\ \Rightarrow>>>>bfc (FDD only) \\ \Rightarrow>>>>bfc (FDD only) \\ \Rightarrow>>>>>bfc (FDD only) \\ \Rightarrow>>>>bfc (FDD only) \\ \Rightarrow ff (FD only) \\ \Rightarrow ff (FD$	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0, TF0) 8 Computed	Addition(TF0, TF0, TF0)0Computed0(TF1, TF0, TF0)1ComputedN/AN/A0(TF2, TF1, TF0)8Computed
$>>>>TFC list \\ >>>>>TFC 1 \\ \Rightarrow>>>>>sctfc \\ >>>>>gainFactorInform \\ ation \\ >>>>>TFC 2 \\ \Rightarrow>>>>>tFC 2 \\ \Rightarrow>>>>ctfc \\ \Rightarrow>>>>ctfc \\ \Rightarrow>>>>sgainFactorInform \\ ation \\ \Rightarrow>>>>>bfc (FDD only) \\ \Rightarrow>>>>>bfd \\ \Rightarrow>>>>sctfc \\ \Rightarrow>>>>sctfc \\ \Rightarrow>>>>sctfc \\ \Rightarrow>>>>sctfc \\ \Rightarrow>>>>sctfc \\ \Rightarrow>>>>sctfc \\ \Rightarrow>>>>referenceTFCId \\ \Rightarrow>>>>>sctfc \\ \Rightarrow>>>>>referenceTFCId \\ \Rightarrow>>>>>sctfc \\ \Rightarrow>>>>>referenceTFCId \\ \Rightarrow>>>>>sctfc \\ \Rightarrow>>>>>sctfc \\ \Rightarrow>>>>>sctfc \\ \Rightarrow>>>>>>>>>>>>>>>sctfc \\ \Rightarrow>>>>>sctfc \\ \Rightarrow>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>$	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0, TF0) 8 Computed 0	Addition(TF0, TF0, TF0)0Computed0(TF1, TF0, TF0)1ComputedN/AN/A0(TF2, TF1, TF0)8Computed00
$>>>>TFC list \\ >>>>>TFC 1 \\ \Rightarrow>>>>>sctfc \\ >>>>>gainFactorInform \\ ation \\ >>>>>TFC 2 \\ \Rightarrow>>>>>tFC 2 \\ \Rightarrow>>>>ctfc \\ \Rightarrow>>>>sctfc \\ \Rightarrow>>>>spainFactorInform \\ ation \\ \Rightarrow>>>>>bfc (FDD only) \\ \Rightarrow>>>>bfd \\ \Rightarrow>>>>>feferenceTFCId \\ \Rightarrow>>>>>bfd \\ \Rightarrow>>>>>tfC 3 \\ \Rightarrow>>>>>tfc 3 \\ \Rightarrow>>>>>tfc 4 \\ $	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0, TF0) 8 Computed 0 (TF3, TF2, TF0,	(TF0, TF0, TF0)         0         Computed         0         (TF1, TF0, TF0)         1         Computed         N/A         N/A         0         (TF2, TF1, TF0)         8         Computed         0         (TF3, TF2, TF0)
$>>>>TFC list \\ >>>>>TFC 1 \\ >>>>>sqainFactorInform \\ ation \\ >>>>>TFC 2 \\ >>>>tfC 2 \\ >>>>>tfC 2 \\ >>>>>>>>>tfC 2 \\ >>>>>tfC 2 \\ >>>>>>>>>>tfC 2 \\ >>>>>>>>>>tfC 2 \\ >>>>>tfC 2 \\ >>>>>tfC 2 \\ >>>>>>>>>>tfC 2 \\ >>>>>>>>>>>>>>>>>>>>>>>tfC 2 \\ >>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>$	(TF0, TF0, TF0, TF0)         0         Computed         0         (TF1, TF0, TF0, TF0)         1         Computed         N/A         0         (TF2, TF1, TF0, TF0, TF0)         8         Computed         0         (TF3, TF2, TF0, TF0, TF0, TF0)	(TF0, TF0, TF0)         0         Computed         0         (TF1, TF0, TF0)         1         Computed         N/A         N/A         0         (TF2, TF1, TF0)         8         Computed         0         (TF3, TF2, TF0)

>>>>>>gainFactorInform	Computed	Computed
ssssss Bd		
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	0	0
>>>>>TFC 5	(TE4 TE3 TE0	(TF4_TE3_TE0)
	TF0)	(11 1, 11 0, 11 0)
>>>>>ctfc	22	22
>>>>>>gainFactorInform	Computed	Computed
ation		
>>>>>>>>reference I FCId		
>>>>>	(1F5, 1F4, 1F1, TF0)	(15, 154, 150)
>>>>>ctfc	59	29
>>>>>>>sgainFactorInform	Computed	Computed
ation		
>>>>>βc (FDD only)		
_>>>>>>βd		
>>>>>>>referenceTFCId		
>>>>>IFC /	(TF0, TF0, TF0, TF1)	(1F0, 1F0, 1F1)
>>>>>ctfc	60	30
>>>>>>sgainFactorInform	Computed	Computed
ation		
>>>>>>referenceTFCId		
>>>>>IFC 8	(1F1, 1F0, 1F0, 1F0, TE1)	(1F1, 1F0, 1F1)
>>>>>>ctfc	61	31
>>>>>>>>>>	computed	computed
ation		
>>>>>>βc (FDD only)		
>>>>>βd		
>>>>>>referenceTFCId	0	0
>>>>>TFC 9	(TF2, TF1, TF0, TF1)	(TF2, TF1, TF1)
>>>>>>ctfc	68	38
>>>>>>gainFactorInform	computed	computed
ation		
>>>>>>referenceTFCId	0	0
>>>>>TFC 10	(TF3, TF2, TF0, TF1)	(TF3, TF2, TF1)
>>>>>>ctfc	75	45
>>>>>>gainFactorInform	computed	computed
ation		
>>>>>>βc (FDD only)		
>>>>>βd		
>>>>>>>referenceTFCId	0	0
>>>>>TFC 11	(TF4, TF3, TF0, TF1)	(TF4, TF3, TF1)
>>>>>ctfc	82	52
>>>>>>gainFactorInform	computed	computed
ation		
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>		
>>>>>>1FU 12	TF1)	(153, 154, 151)
>>>>>ctfc	97	59
>>>>>>gainFactorInform ation	signalled	signalled
>>>>>>Bc (FDD only)	11	11
>>>>>Bd	15	15
>>>>>>referenceTFCId	0	0
> TFC subset list		
>>TFC subset 1	(speech rate 10.2)	(speech rate 7.4)

>>> Allowed transport	(TFC1, TFC2,	(TFC1, TFC2,
format combination list	TFC7, TFC8, TFC6,	TFC7, TFC8, TFC6,
	TFC12)	TFC12)
>>TFC subset 2	(speech rate 6.7)	(speech rate 6.7)
>>> Allowed transport	(TEC1_TEC2	(TEC1_TEC2
format combination list	TEC7 TEC8 TEC5	TEC7 TEC8 TEC5
Torriat combination list	TEC(11)	TEC11)
TEC subset 2	(apacch rate 5.0)	(apacch rate 5.0)
>>TFC Subset 3	(speech late 5.9)	(speech late 5.9)
>>> Allowed transport		
format combination list	TFC7, TFC8, TFC4,	IFC7, IFC8, IFC4,
	IFC10)	TFC10)
>>TFC subset 4	(speech rate 4.75)	(speech rate 4.75)
>>> Allowed transport	(TFC1, TFC2,	(TFC1, TFC2,
format combination list	TFC7, TFC8, TFC3,	TFC7, TFC8, TFC3,
	TFC9)	TFC9)
dl-CommonTransChInfo		
>tfcs-SignallingMode	Independent	Independent
>tfcs-ID (TDD only)	1	1
	FALSE	FALSE
> ttc-Subset	Absent, not required	Absent, not required
>dl-TFCS	Normal TFCI	Normal TFCI
	signalling	signalling
>>explicitTFCS-	Complete	Complete
ConfigurationMode		
>>>ctfcSize	Ctfc8Bit	Ctfc8Bit
>>>>TECS representation	Addition	Addition
	Addition	Addition
	(TE0 TE0 TE0	(TE0 TE0 TE0
>>>>>IFC 1	(1F0, 1F0, 1F0,	(1F0, 1F0, 1F0,
	1F0, 1F0)	1F0)
>>>>>>ctfc	0	0
>>>>>TFC 2	(TF1, TF0, TF0,	(TF1, TF0, TF0,
	TF0, TF0)	TF0)
>>>>>ctfc	1	1
>>>>>TFC 3	(TF2, TF1, TF0,	(TF2, TF1, TF0,
	TEO TEO	(11 <u>1</u> , 111, 110, TEO)
>>>>>>	8	8
<i>&gt;&gt;&gt;&gt;&gt;</i> 1FC 4	(1F3, 1F2, 1F0, TE0, TE0)	(1F3, 1F2, 1F0, TF0)
	1F0, 1F0)	1F0)
>>>>>>ctfc	15	15
>>>>>TFC 5	(TF4, TF3, TF0,	(TF4, TF3, TF0,
	TF0, TF0)	TF0)
>>>>>>ctfc	22	22
>>>>>TFC 6	(TF5, TF4, TF1,	(TF5, TF4, TF0,
	TFO. TFO)	TF0)
>>>>>ctfc	59	29
		(TE0_TE0_TE1
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	(1F0, 1F0, 1F0, 1F0, 1F0, 1F0, 1F0, 1F0,	(1FO, 1FO, 1FT, TEO)
		1F0)
>>>>>Ctic		30
>>>>>IFC 8	(IF1, IF0, IF0,	(IF1, IF0, IF1,
	IF1, IF0)	IF0)
>>>>>ctfc	61	31
>>>>>TFC 9	(TF2, TF1, TF0,	(TF2, TF1, TF1,
	TF1, TF0)	TF0)
>>>>>ctfc	68	37
>>>>>TFC 10	(TE3 TE2 TE0	(TE3 TE2 TE1
	TF1 TF0	(110, 112, 111, TF0)
>>>>>>	75	55
>>>>>IFC 11	(1+4, 1+3, 1+0, 1+0, 1+0)	(1F4, 1F3, 1F1, TE0)
	IF1, IF0)	IF0)
>>>>>ctfc	82	52
>>>>>TFC 12	(TF5, TF4, TF1,	(TF5, TF4, TF1,
	TF1, TF0)	TF0)
>>>>>ctfc	119	59

		1
>>>>>TFC 13	(TF0, TF0, TF0, TE0, TE1)	(TF0, TF0, TF0,
· · · · · · · offo	120	60
>>>>>IFC 14	(1F1, 1F0, 1F0, 1F0, TF0, TF0, TF0, TF0, TF0, TF0, TF0, T	(1F1, 1F0, 1F0, 1F0, TF4)
	1F0, 1F1)	1F1)
>>>>>>CtfC	121	
>>>>>IFC 15	(IF2, IF1, IF0,	(IF2, IF1, IF0,
-	IF0, IF1)	IF1)
>>>>>>ctfc	128	68
>>>>>TFC 16	(TF3, TF2, TF0,	(TF3, TF2, TF0,
	TF0, TF1)	TF1)
>>>>>>ctfc	135	75
>>>>>TFC 17	(TF4, TF3, TF0,	(TF4, TF3, TF0,
	TF0, TF1)	TF1)
>>>>>ctfc	152	82
>>>>>TFC 18	(TF5, TF4, TF1,	(TF5, TF4, TF0,
	TF0, TF1)	TF1)
>>>>>>ctfc	189	89
>>>>>TFC 19	(TF0, TF0, TF0,	(TF0, TF0, TF1,
	TF1, TF1)	TF1)
>>>>>ctfc	180	90
>>>>>TFC 20	(TE1, TE0, TE0	(TE1, TE0, TE1
	TF1 TF1)	TF1)
>>>>>>tfc	181	91
>>>>>TEC 21	(TE2 TE1 TE0	
>>>>>TFG 21	(IFZ, IFI, IFU, TE1 TE1)	(1F2, 1F1, 1F1, 1F1, TE1)
· · · · · · · · otto	100	
	100	
>>>>>IFC 22	(1F3, 1F2, 1F0, TF4, TF4)	(1F3, 1F2, 1F1, TF4)
	1F1, 1F1)	1F1)
>>>>>>Ctfc	195	105
>>>>>TFC 23	(TF4, TF3, TF0,	(TF4, TF3, TF1,
-	IF1, IF1)	IF1)
>>>>>>ctfc	239	112
>>>>>TFC 24	(TF5, TF4, TF1,	(TF5, TF4, TF1,
	TF1, TF1)	TF1)
>>>>>>ctfc	218	119
PhyCH INFORMATION		
FDD		
UL-DPCH-InfoPredef		
>ul-DPCH-		
PowerControlInfo		
>>powerControlAlgorithm	Algorithm 1	Algorithm 1
>>>tpcStepSize	1 dB	1 dB
>tfci-Existence	TRUE	TRUE
>puncturingl imit	0.88	0.88
CommonInformationPredef		
		<u> </u>
	129	128
	TALOE	TALOE
>>pilotBits	4	4
>>positionFixed	Fixed	Fixed
PhyCH INFORMATION		
3.84 Mcps TDD		
UL-DPCH-InfoPredef		
>ul-DPCH-		
PowerControlInfo		
>>dpch-ConstantValue	0	0
>commonTimeslotInfo		
	1	

frameRelated

repetitionPeriod1

16

0.60

>>secondInterleavingMode

>>repetitionPeriodAndLeng

>>tfci-Coding

th

>>puncturingLimit

repetitionPeriod1

frameRelated

16

0.60

DL-		
CommonInformationPredef		
>dI-DPCH-InfoCommon		
>>commonTimeslotInfo		
>>>secondInterleavingMod	frameRelated	frameRelated
е		
>>>tfci-Coding	16	16
>>>puncturingLimit	0.60	0.60
>>>repetitionPeriodAndLe	repetitionPeriod1	repetitionPeriod1
ngth		
PhyCH INFORMATION		
1.28 Mcps TDD		
UL-DPCH-InfoPredef		
>commonTimeslotInfo		
>>secondInterleavingMode	frame Related	frame Related
>>tfci-Coding	16	16
>>puncturingLimit	0.64	0.64
>>repetitionPeriodAndLeng	repetitionPeriod1	repetitionPeriod1
th		
DL-		
CommonInformationPredef		
>dl-DPCH-InfoCommon		
>>commonTimeslotInfo		
>>>secondInterleavingMod	frame Related	frame Related
е		
>>>tfci-Coding	16	16
>>>puncturingLimit	0.64	0.64
>>>repetitionPeriodAndLe	repetitionPeriod1	repetitionPeriod1
ngth		

Configuration	12.65/8.85/6.6 kbps
	3.4 kbps signalling
	••••••••••••••••••••••••••••••••••••••
Ref 34.108	62
Default configuration	13
identity	
rb-identity	RB1: 1, RB2: 2, DB2: 2, DB5: 5
	RB6: 6 RB8: 8
rlc-InfoChoice	RIC-info
>ul-RLC-Mode	RB1: UM
	RB2- RB3: AM
	RB5-RB6: TM
>>transmissionRLC-	RB1: N/A
DiscardMode	RB2- RB3:
	RB5- RB6: N/A
>>>maxDat	RB1· N/A
	RB2- RB3: 15
	RB5- RB6: N/A
>>transmissionWindowSiz	RB1: N/A
е	RB2- RB3: 128 for
	UEs with more than
	10 kbyte "total RLC
	32 otherwise
	RB5- RB6: N/A
>>timerRST	RB1: N/A
	RB2- RB3: 300
	RB5- RB6: N/A
>>max-RST	RB1: N/A
	RB2- RB3: 1
a a llia a la fa	RB5- RB6: N/A
>>pollinginto	RB1: N/A RB2_RB2: as bolow
	RB5- RB6: N/A
>>>lastTransmissionPDU-	RB2- RB3: FALSE
Poll	
>>>lastRetransmissionPD U-Poll	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A
d PLC Mada	RB5- RB6: FALSE
>di-RLC-iviode	RDT. UNI RR2- RR3' AM
	RB5- RB6: TM
	RB8: TM
>>inSequenceDelivery	RB1: N/A
	RB2- RB3: TRUE
	RB5- RB6: N/A
	RB8: N/A
>>receivingWindowSize	RB1: N/A PB2_PB2: 129 for
	LIFs with more than
	10 kbvte "total RLC
	AM buffer size" and
	32 otherwise
	RB5- RB6: N/A
	RB8: N/A
>>dl-RLC-StatusInfo	RB1: N/A
	RB2- RB3: as below
	κβς- κβς: Ν/Α RB8· Ν/Δ
>>>timerStatusProhibit	RB2- RB3- 100
>>>missingPDU-Indicator	RB2- RB3: FALSE

>>>timerStatusPeriodic	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A
	RB5- RB6: FALSE
	RB8: FALSE
rb-MappingInfo	
>UL-	OneLogicalChannel
LogicalChannelMappings	
>>ul-	Dch
I ransportChannel I ype	
>>>transportChannelIdentit	RB1- RB3: 4
y	RB5: 1, RB6: 2.
>>logicalChannelidentity	RB1: 1, RB2: 2,
	RDJ. J RB5- RB6· N/A
>>rlc_SizeList	RB1- RB3
	configured
	RB5- RB6: N/A
>>mac-	RB1: 1, RB2: 2.
LogicalChannelPriority	RB3: 3
5	RB5- RB6: 5
>DL-	
logicalChannelMappingList	
>>Mapping option 1	One mapping option
>>>dl-	Dch
TransportChannelType	
>>>>transportChannellden	RB1- RB3: 4
tity	RB5: 1, RB6: 2,
	RB8: 5
>>>logicalChannelIdentity	RB1: 1, RB2: 2,
	RB3: 3
	RB5- RB6: N/A
	RB8: N/A
OL- AddReconfTransChinfol ist	
>I Inlink transport channel	dch
	uch
>transportChannelldentity	TrCH1: 1. TrCH2: 2.
	TrCH4: 4
>transportFormatSet	DedicatedTransChT
	FS
>>dynamicTF-information	
>>>tf0/ tf0,1	TrCH1: (0x72)
	TrCH2: (0x 181)
	TrCH4: (0x144,
	1x144)
>>>rlcSize	BitMode
>>>>sizeType	TrCH1: type 1: 72
	TrCH2: type 1: 181
	TrCH4: 2: type 2,
	part1 = 2, part2 = 0
	(144)
>>>>numberOfIbSizeList	TrCH1-2: Zero
	TrCH4: Zero, one
>>>iogicalChannelList	
>>>tf 1	TrCH1: (1x40)
	$\Pi \cup \Pi \angle .$ (1X / $\delta$ ) Tr $\cap \square A : N/A$
>>>pumbarOfTransartD	ПСП4. IN/А
	TrCH1: One
0010	TrCH1: One
>>>rlc-Size	TrCH1: One TrCH2: One TrCH1-2: BitModo
>>>>rlc-Size	TrCH1: One TrCH2: One TrCH1-2: BitMode
>>>>rlc-Size >>>>sizeType	TrCH1: One TrCH2: One TrCH1-2: BitMode TrCH1: 1: 40 TrCH2: 1: 78
>>>>rlc-Size >>>>sizeType	TrCH1: One TrCH2: One TrCH1-2: BitMode TrCH1: 1: 40 TrCH2: 1: 78 TrCH1-2: One
>>>>rlc-Size >>>>sizeType >>>>numberOfTbSizeList	TrCH1: One TrCH2: One TrCH1-2: BitMode TrCH1: 1: 40 TrCH2: 1: 78 TrCH1-2: One TrCH1: all

>>>tf 2	TrCH1: (1x54)
	TrCH2: (1x113)
	TrCH4: N/A
>>>>numberOfTransportBl	TrCH1: One
ocks	TrCh2: One
>>>rlc-Size	TrCH1: BitMode
>>>>sizeType	TrCH1: type 1: 54
	TrCH2: type 1: 113
>>>>numberOfThSizeList	TrCH1: One
	TrCH2: One
· · · · logicalChannell ist	
>>>>iogicalChanneiList	
>>>tt 3	TrCH1: (1x64)
	TrCH2: (1x181)
	TrCH4: N/A
>>>>numberOfTransportBl	TrCH1: One
ocks	TrCh2: One
>>>>rlc-Size	TrCH1: BitMode
>>>>sizeType	TrCH1: type 1: 64
21	TrCH2: type 1: 181
>>>>numberOfTbSizeList	TrCH1: One
	TrCH2: One
	The first second secon
>>>ti 4	TICHI: $(1X/2)$
	TrCH2: N/A
	TrCH4: N/A
>>>>numberOfTransportBl	TrCH1: One
ocks	
>>>>rlc-Size	TrCH1: BitMode
>>>>sizeType	TrCH1: type 1: 72
>>>>numberOfTbSizeList	TrCH1: One
	TOUL
>>>>logical(Channell ist	IrCH1 all
>>>logicalChannelList	TrCH1: all
>>>logicalChannelList >>semistaticTF-Information	
>>>logicalChannelList >>semistaticTF-Information >>>tti	TrCH1: all TrCH1- TrCH2: 20
>>>logicalChannelList >>semistaticTF-Information >>>tti	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional
<pre>&gt;&gt;&gt;logicalChannelList &gt;&gt;semistaticTF-Information &gt;&gt;&gt;tti &gt;&gt;&gt;channelCodingType &gt;&gt;&gt;codingRate</pre>	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2:
<pre>&gt;&gt;&gt;logicalChannelList &gt;&gt;semistaticTF-Information &gt;&gt;&gt;tti &gt;&gt;&gt;channelCodingType &gt;&gt;&gt;codingRate</pre>	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third
<pre>&gt;&gt;&gt;logicalChannelList &gt;&gt;semistaticTF-Information &gt;&gt;&gt;tti &gt;&gt;&gt;channelCodingType &gt;&gt;&gt;codingRate &gt;&gt;&gt;rateMatchingAttribute</pre>	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH1: 200
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH1: 200 TrCH2: 190
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH1: 200 TrCH2: 190 TrCH4: 170
<pre>&gt;&gt;&gt;logicalChannelList &gt;&gt;semistaticTF-Information &gt;&gt;&gt;tti &gt;&gt;&gt;channelCodingType &gt;&gt;&gt;codingRate &gt;&gt;&gt;rateMatchingAttribute &gt;&gt;&gt;crc-Size</pre>	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH1: 200 TrCH2: 190 TrCH4: 170 TrCH1: 12
<pre>&gt;&gt;&gt;logicalChannelList &gt;&gt;semistaticTF-Information &gt;&gt;&gt;tti &gt;&gt;&gt;channelCodingType &gt;&gt;&gt;codingRate &gt;&gt;&gt;rateMatchingAttribute &gt;&gt;&gt;crc-Size</pre>	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH1: 200 TrCH2: 190 TrCH4: 170 TrCH1: 12 TrCH1: 12 TrCH2: 0
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>crc-Size	TrCH1: all           TrCH1- TrCH2: 20           TrCH4: 40           Convolutional           TrCH1- TrCH2:           Third           TrCH4: Third           TrCH1: 200           TrCH2: 190           TrCH4: 170           TrCH1: 12           TrCH2: 0           TrCH4: 16
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>crc-Size DL-	TrCH1: all         TrCH1- TrCH2: 20         TrCH4: 40         Convolutional         TrCH1- TrCH2:         Third         TrCH4: Third         TrCH1: 200         TrCH2: 190         TrCH4: 170         TrCH1: 12         TrCH2: 0         TrCH4: 16
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>crc-Size DL- AddReconfTransChInfoList	TrCH1: all         TrCH1- TrCH2: 20         TrCH4: 40         Convolutional         TrCH1- TrCH2:         Third         TrCH4: Third         TrCH1: 200         TrCH2: 190         TrCH4: 170         TrCH1: 12         TrCH2: 0         TrCH4: 16
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>crc-Size DL- AddReconfTransChInfoList >Downlink transport	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH1: 200 TrCH2: 190 TrCH4: 170 TrCH1: 12 TrCH2: 0 TrCH4: 16 dch
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>rateMatchingAttribute DL- AddReconfTransChInfoList >Downlink transport channel type	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH1: 200 TrCH2: 190 TrCH4: 170 TrCH1: 12 TrCH2: 0 TrCH4: 16 dch
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>rateMatchingAttribute >>>crc-Size DL- AddReconfTransChInfoList >Downlink transport channel type >dl-	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH1: 200 TrCH2: 190 TrCH4: 170 TrCH1: 12 TrCH2: 0 TrCH4: 16 dch
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>rateMatchingAttribute >>>crc-Size DL- AddReconfTransChInfoList >Downlink transport channel type >dl- TransportChannelIdentity	TrCH1: all         TrCH1- TrCH2: 20         TrCH4: 40         Convolutional         TrCH1- TrCH2:         Third         TrCH4: Third         TrCH2: 190         TrCH4: 170         TrCH4: 12         TrCH2: 0         TrCH4: 16         dch
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>rateMatchingAttribute >>>crc-Size DL- AddReconfTransChInfoList >Downlink transport channel type >dl- TransportChannelIdentity >tfs_SignallingMode	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH1: 200 TrCH2: 190 TrCH4: 170 TrCH1: 12 TrCH2: 0 TrCH4: 16 dch
<pre>&gt;&gt;&gt;logicalChannelList &gt;&gt;semistaticTF-Information &gt;&gt;&gt;tti &gt;&gt;&gt;channelCodingType &gt;&gt;&gt;codingRate &gt;&gt;&gt;rateMatchingAttribute &gt;&gt;&gt;rateMatchingAttribute DL- AddReconfTransChInfoList &gt;Downlink transport channel type &gt;dl- TransportChannelIdentity &gt;tfs-SignallingMode</pre>	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH1: 200 TrCH2: 190 TrCH4: 170 TrCH4: 170 TrCH4: 16 dch
<pre>&gt;&gt;&gt;logicalChannelList &gt;&gt;semistaticTF-Information &gt;&gt;&gt;tti &gt;&gt;&gt;channelCodingType &gt;&gt;&gt;codingRate &gt;&gt;&gt;rateMatchingAttribute &gt;&gt;&gt;rateMatchingAttribute DL- AddReconfTransChInfoList &gt;Downlink transport channel type &gt;dl- TransportChannelIdentity &gt;tfs-SignallingMode</pre>	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH2: 190 TrCH2: 190 TrCH4: 170 TrCH4: 170 TrCH4: 16 dch Independent <only on="" tf0="" trch1<br="">and tf0/tf1 on</only>
<pre>&gt;&gt;&gt;logicalChannelList &gt;&gt;semistaticTF-Information &gt;&gt;&gt;tti &gt;&gt;&gt;channelCodingType &gt;&gt;&gt;codingRate &gt;&gt;&gt;rateMatchingAttribute &gt;&gt;&gt;crc-Size DL- AddReconfTransChInfoList &gt;Downlink transport channel type &gt;dl- TransportChannelIdentity &gt;tfs-SignallingMode</pre>	IrcH1: all         TrCH1- TrCH2: 20         TrCH4: 40         Convolutional         TrCH1- TrCH2:         Third         TrCH4: Third         TrCH4: Third         TrCH2: 190         TrCH4: 170         TrCH4: 170         TrCH4: 16         dch         Independent <only on="" td="" tf0="" trch1<="">         and tf0/tf1 on         TrCH5 care difference</only>
<pre>&gt;&gt;&gt;logicalChannelList &gt;&gt;semistaticTF-Information &gt;&gt;&gt;tti &gt;&gt;&gt;channelCodingType &gt;&gt;&gt;codingRate &gt;&gt;&gt;rateMatchingAttribute &gt;&gt;&gt;crc-Size DL- AddReconfTransChInfoList &gt;Downlink transport channel type &gt;dl- TransportChannelIdentity &gt;tfs-SignallingMode</pre>	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH2: 190 TrCH2: 190 TrCH4: 170 TrCH4: 170 TrCH4: 16 dch Independent <only on="" tf0="" trch1<br="">and tf0/tf1 on TrCH5 are different and of our below</only>
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>rateMatchingAttribute >>>crc-Size DL- AddReconfTransChInfoList >Downlink transport channel type >dl- TransportChannelIdentity >tfs-SignallingMode	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH2: 190 TrCH2: 190 TrCH4: 170 TrCH1: 12 TrCH2: 0 TrCH4: 16 dch Independent <only on="" tf0="" trch1<br="">and tf0/tf1 on TrCH5 are different and shown below&gt;</only>
<pre>&gt;&gt;&gt;logicalChannelList &gt;&gt;semistaticTF-Information &gt;&gt;&gt;tti &gt;&gt;&gt;channelCodingType &gt;&gt;&gt;codingRate &gt;&gt;&gt;rateMatchingAttribute &gt;&gt;&gt;rateMatchingAttribute DL- AddReconfTransChInfoList &gt;Downlink transport channel type &gt;dl- TransportChannelIdentity &gt;tfs-SignallingMode &gt;&gt;transportFormatSet</pre>	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH2: 190 TrCH2: 190 TrCH4: 170 TrCH1: 12 TrCH2: 0 TrCH4: 16 dch Independent <only on="" tf0="" trch1<br="">and tf0/tf1 on TrCH5 are different and shown below&gt;</only>
<pre>&gt;&gt;&gt;logicalChannelList &gt;&gt;semistaticTF-Information &gt;&gt;&gt;tti &gt;&gt;&gt;channelCodingType &gt;&gt;&gt;codingRate &gt;&gt;&gt;rateMatchingAttribute &gt;&gt;&gt;rateMatchingAttribute DL- AddReconfTransChInfoList &gt;Downlink transport channel type &gt;dl- TransportChannelIdentity &gt;tfs-SignallingMode &gt;&gt;transportFormatSet &gt;&gt;&gt;dynamicTF-information</pre>	IrcH1: all         TrCH1- TrCH2: 20         TrCH4: 40         Convolutional         TrCH1- TrCH2:         Third         TrCH4: Third         TrCH2: 190         TrCH4: 170         TrCH2: 0         TrCH4: 16         dch         Independent <only on="" td="" tf0="" trch1<="">         and tf0/tf1 on         TrCH5 are different         and shown below&gt;</only>
<pre>&gt;&gt;&gt;logicalChannelList &gt;&gt;semistaticTF-Information &gt;&gt;&gt;tti &gt;&gt;&gt;channelCodingType &gt;&gt;&gt;codingRate &gt;&gt;&gt;rateMatchingAttribute &gt;&gt;&gt;rateMatchingAttribute DL- AddReconfTransChInfoList &gt;Downlink transport channel type &gt;dl- TransportChannelIdentity &gt;tfs-SignallingMode &gt;&gt;&gt;tfs-SignallingMode &gt;&gt;&gt;tfo/ tf0,1</pre>	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH2: 190 TrCH2: 190 TrCH4: 170 TrCH2: 0 TrCH2: 0 TrCH4: 16 dch Independent <only on="" tf0="" trch1<br="">and tf0/tf1 on TrCH5 are different and shown below&gt; TrCH1: (1x0)</only>
<pre>&gt;&gt;&gt;logicalChannelList &gt;&gt;semistaticTF-Information &gt;&gt;&gt;tti &gt;&gt;&gt;channelCodingType &gt;&gt;&gt;codingRate &gt;&gt;&gt;rateMatchingAttribute &gt;&gt;&gt;rateMatchingAttribute  DL- AddReconfTransChInfoList &gt;Downlink transport channel type &gt;dl- TransportChannelIdentity &gt;tfs-SignallingMode  &gt;&gt;&gt;tfs-SignallingMode </pre>	IrcH1: all         TrCH1- TrCH2: 20         TrCH4: 40         Convolutional         TrCH1- TrCH2:         Third         TrCH4: Third         TrCH4: 170         TrCH2: 190         TrCH2: 0         TrCH4: 16         dch         Independent <only on="" td="" tf0="" trch1<="">         and tf0/tf1 on         TrCH5 are different         and shown below&gt;         TrCH1: (1x0)         TrCH5: (0x3, 1x3)</only>
<pre>&gt;&gt;&gt;logicalChannelList &gt;&gt;semistaticTF-Information &gt;&gt;&gt;tti &gt;&gt;&gt;channelCodingType &gt;&gt;&gt;codingRate &gt;&gt;&gt;rateMatchingAttribute &gt;&gt;&gt;crc-Size DL- AddReconfTransChInfoList &gt;Downlink transport channel type &gt;dl- TransportChannelIdentity &gt;tfs-SignallingMode &gt;&gt;&gt;tfs-SignallingMode &gt;&gt;&gt;tfo/ tf0,1 &gt;&gt;&gt;&gt;tf0/ tf0,1 &gt;&gt;&gt;&gt;tfo/ tf0,1</pre>	IrCH1: all         TrCH1- TrCH2: 20         TrCH4: 40         Convolutional         TrCH1- TrCH2:         Third         TrCH4: Third         TrCH2: 190         TrCH2: 190         TrCH4: 170         TrCH4: 16         dch         Independent <only on="" td="" tf0="" trch1<="">         and tf0/tf1 on         TrCH5 are different         and shown below&gt;         TrCH1: (1x0)         TrCH5: (0x3, 1x3)         BitMode</only>
<pre>&gt;&gt;&gt;logicalChannelList &gt;&gt;semistaticTF-Information &gt;&gt;&gt;tti &gt;&gt;&gt;channelCodingType &gt;&gt;&gt;codingRate &gt;&gt;&gt;rateMatchingAttribute &gt;&gt;&gt;crc-Size DL- AddReconfTransChInfoList &gt;Downlink transport channel type &gt;dl- TransportChannelIdentity &gt;tfs-SignallingMode &gt;&gt;&gt;tfs-SignallingMode &gt;&gt;&gt;tfo/ tf0,1 &gt;&gt;&gt;&gt;tf0/ tf0,1 &gt;&gt;&gt;&gt;tfo/ tf0,1</pre>	IrcH1: all         TrCH1- TrCH2: 20         TrCH4: 40         Convolutional         TrCH1- TrCH2:         Third         TrCH4: Third         TrCH2: 190         TrCH2: 190         TrCH4: 170         TrCH4: 16         dch         Independent <only on="" td="" tf0="" trch1<="">         and tf0/tf1 on         TrCH5 are different         and shown below&gt;        </only>
<pre>&gt;&gt;&gt;logicalChannelList &gt;&gt;semistaticTF-Information &gt;&gt;&gt;tti &gt;&gt;&gt;channelCodingType &gt;&gt;&gt;codingRate &gt;&gt;&gt;rateMatchingAttribute &gt;&gt;&gt;crc-Size DL- AddReconfTransChInfoList &gt;Downlink transport channel type &gt;dl- TransportChannelIdentity &gt;tfs-SignallingMode &gt;&gt;&gt;tfs-SignallingMode &gt;&gt;&gt;tfo/ tf0,1 &gt;&gt;&gt;&gt;tf0/ tf0,1 &gt;&gt;&gt;&gt;tfo/ tf0,1</pre>	IrCH1: all         TrCH1- TrCH2: 20         TrCH4: 40         Convolutional         TrCH1- TrCH2:         Third         TrCH4: Third         TrCH4: 170         TrCH2: 190         TrCH4: 170         TrCH4: 16         dch         Independent <only on="" td="" tf0="" trch1<="">         and tf0/tf1 on         TrCH5 are different         and shown below&gt;         TrCH1: (1x0)         TrCH5: (0x3, 1x3)         BitMode         TrCH1: type 1: 0         TrCH5: type 1: 3</only>
<pre>&gt;&gt;&gt;logicalChannelList &gt;&gt;semistaticTF-Information &gt;&gt;&gt;tti &gt;&gt;&gt;channelCodingType &gt;&gt;&gt;codingRate &gt;&gt;&gt;rateMatchingAttribute &gt;&gt;&gt;crc-Size DL- AddReconfTransChInfoList &gt;Downlink transport channel type &gt;dl- TransportChannelIdentity &gt;tfs-SignallingMode &gt;&gt;tfs-SignallingMode &gt;&gt;&gt;tf0/ tf0,1 &gt;&gt;&gt;&gt;tf0/ tf0,1 &gt;&gt;&gt;&gt;tf0/ tf0,1</pre>	IrCH1: all         TrCH1- TrCH2: 20         TrCH4: 40         Convolutional         TrCH1- TrCH2:         Third         TrCH4: Third         TrCH4: 170         TrCH2: 190         TrCH4: 170         TrCH4: 16         dch         Independent <only on="" td="" tf0="" trch1<="">         and tf0/tf1 on         TrCH5 are different         and shown below&gt;         TrCH1: (1x0)         TrCH5: (0x3, 1x3)         BitMode         TrCH1: type 1: 0         TrCH5: type 1: 3         TrCH1: One</only>
<pre>&gt;&gt;&gt;logicalChannelList &gt;&gt;semistaticTF-Information &gt;&gt;&gt;tti &gt;&gt;&gt;channelCodingType &gt;&gt;&gt;codingRate &gt;&gt;&gt;rateMatchingAttribute &gt;&gt;&gt;crc-Size DL- AddReconfTransChInfoList &gt;Downlink transport channel type &gt;dl- TransportChannelIdentity &gt;tfs-SignallingMode &gt;&gt;&gt;tfs-SignallingMode &gt;&gt;&gt;tfo/ tf0,1 &gt;&gt;&gt;&gt;tf0/ tf0,1 &gt;&gt;&gt;&gt;tf0/ tf0,1 &gt;&gt;&gt;&gt;tf0/ tf0,1</pre>	IrcH1: all         TrCH1- TrCH2: 20         TrCH4: 40         Convolutional         TrCH1- TrCH2:         Third         TrCH4: Third         TrCH4: 170         TrCH2: 190         TrCH4: 170         TrCH4: 16         dch         Independent <only on="" td="" tf0="" trch1<="">         and tf0/tf1 on         TrCH5 are different         and shown below&gt;         TrCH1: (1x0)         TrCH5: (0x3, 1x3)         BitMode         TrCH1: type 1: 0         TrCH5: type 1: 3         TrCH1: One         TrCH5: Zero one</only>

>>>>logicalChannelList	All
>>>semistaticTF-	same as UL except
Information	for TrCH5
>>>>tti	TrCH5: 20
>>>>channelCodingType	Convolutional
>>>>codingRate	TrCH5: Third
>>>>rateMatchingAttribute	TrCH5: 205
>>>>crc-Size	TrCH5: 8
>>ULTrCH-Id	TrCH1: 1, TrCH2: 2,
	TrCH4: 4,
>dch-QualityTarget	
>>bler-QualityValue	TrCH1: 7x10 ⁻³
, , , ,	TrCH2: Absent
	TrCH4- TrCH5:
	Absent
TrCH INFORMATION,	
COMMON	
ul-CommonTransChInfo	
>tfcs-ID (TDD only)	1
>sharedChannelIndicator	FALSE
(TDD only)	-
> tfc-Subset	Absent. not required
>ul-TFCS	Normal TFCI
	signalling
>>explicitTFCS-	Complete
ConfigurationMode	
>>>ctfcSize	Ctfc6Bit
>>>>TECS representation	Addition
>>>>TEC liet	Addition
	0 Computed
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	Computed
111/11	
	0
>>>>>referenceTFCId	0
>>>>>referenceTFCId >>>>>TFC 2	0 (TF1, TF0, TF0)
>>>>>referenceTFCId >>>>>TFC 2 >>>>>ctfc	0 (TF1, TF0, TF0) 1
>>>>>referenceTFCld >>>>>TFC 2 >>>>>ctfc >>>>>>gainFactorInform	0 (TF1, TF0, TF0) 1 Computed
>>>>>referenceTFCId >>>>>TFC 2 >>>>>ctfc >>>>>>gainFactorInform ation	0 (TF1, TF0, TF0) 1 Computed
>>>>>referenceTFCId         >>>>>TFC 2         >>>>>ctfc         >>>>>gainFactorInform         ation         >>>>>βc (FDD only)	0 (TF1, TF0, TF0) 1 Computed N/A
>>>>>referenceTFCId         >>>>>TFC 2         >>>>>ctfc         >>>>>gainFactorInform         ation         >>>>>βc (FDD only)         >>>>>>βd	0 (TF1, TF0, TF0) 1 Computed N/A N/A
>>>>>referenceTFCId           >>>>>TFC 2           >>>>>ctfc           >>>>>gainFactorInform           ation           >>>>>βc (FDD only)           >>>>>βd           >>>>>referenceTFCId	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0
>>>>>referenceTFCId           >>>>>TFC 2           >>>>>ctfc           >>>>>gainFactorInform ation           >>>>>βc (FDD only)           >>>>>βd           >>>>>referenceTFCId           >>>>>FreferenceTFCId           >>>>>>FreferenceTFCId	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0)
$\begin{array}{l} \\ >>>>>referenceTFCId \\ \\ >>>>>tFC 2 \\ \\ >>>>>ctfc \\ \\ >>>>>gainFactorInform \\ ation \\ \\ \\ >>>>>\betac (FDD only) \\ \\ \\ >>>>>\betad \\ \\ \\ \\ >>>>>sreferenceTFCId \\ \\ \\ \\ >>>>>tFC 3 \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ $	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7
$\begin{array}{l} \\ >>>>>referenceTFCId \\ \\ >>>>>tFC 2 \\ \\ >>>>>ctfc \\ \\ >>>>>gainFactorInform \\ ation \\ \\ \\ >>>>>\betac (FDD only) \\ \\ \\ \\ >>>>>\betad \\ \\ \\ \\ >>>>>sreferenceTFCId \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed
$\begin{array}{l} \\ >>>>>referenceTFCld \\ \\ >>>>>tfC 2 \\ \\ >>>>>ctfc \\ \\ \\ >>>>>gainFactorInform \\ ation \\ \\ \\ >>>>>\betac (FDD only) \\ \\ \\ \\ >>>>>\betad \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed
$\begin{array}{l} \\ >>>>>referenceTFCld \\ \\ >>>>>tfC 2 \\ \\ >>>>>ctfc \\ \\ >>>>>gainFactorInform \\ ation \\ \\ \\ >>>>>\betac (FDD only) \\ \\ \\ >>>>>\betad \\ \\ \\ \\ >>>>>sreferenceTFCld \\ \\ \\ \\ >>>>>tfC 3 \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ $	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0)
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed
attor >>>>>referenceTFCld >>>>>tFC 2 >>>>>sctfc >>>>>sgainFactorInform ation >>>>> $\beta c$ (FDD only) >>>>> $\beta d$ >>>>> $\beta d$ >>>>>sreferenceTFCld >>>>>sctfc >>>>>sctfc >>>>>sctfc >>>>>sreferenceTFCld >>>>>sreferenceTFCld >>>>>stfc 4 >>>>>tFC 4	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed
ation >>>>>referenceTFCld >>>>>tFC 2 >>>>>sctfc >>>>>sgainFactorInform ation >>>>> $\beta c$ (FDD only) >>>>> $\beta d$ >>>>> $\beta d$ >>>>>ffC 3 >>>>>tfc >>>>>ctfc >>>>>sctfc >>>>>sctfc >>>>>sctfc >>>>>sctfc >>>>>sctfc >>>>>sctfc >>>>>sctfc >>>>>sctfc >>>>>sctfc >>>>>sctfc >>>>>sctfc >>>>>sctfc >>>>>sctfc >>>>>sctfc >>>>>sctfc >>>>>sctfc >>>>>sctfc >>>>>sctfc >>>>>sctfc >>>>>sctfc >>>>>sctfc >>>>>>sctfc	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed
attor >>>>>referenceTFCld >>>>>ctfc >>>>>>gainFactorInform ation >>>>> $\beta c$ (FDD only) >>>>> $\beta d$ >>>>> $\beta d$ >>>>>ffC 3 >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>>ctfc >>>>>>ctfc >>>>>>ctfc >>>>>>ctfc >>>>>>ctfc	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0 (TF4, TF3, TF0)
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0 (TF4, TF3, TF0) 19
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0 (TF4, TF3, TF0) 19 Computed
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0 (TF4, TF3, TF0) 19 Computed
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0 (TF4, TF3, TF0) 19 Computed 0
$\begin{tabular}{l}{llllllllllllllllllllllllllllllll$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0 (TF4, TF3, TF0) 19 Computed 0 (TF0, TF0, TF1)
ation >>>>>referenceTFCld >>>>>sctfc >>>>>> $\betac (FDD only)$ >>>>> $\betad$ >>>>>ffC 3 >>>>>tfc 3 >>>>>tfc 3 >>>>>tfc 3 >>>>>tfc 3 >>>>>tfc 3 >>>>>tfc 4 >>>>>tfc 4 >>>>>tfc 4 >>>>>tfc 4 >>>>>tfc 4 >>>>>tfc 5 >>>>> $\betad$ >>>>>tfc 5 >>>>> $\betad$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0 (TF4, TF3, TF0) 19 Computed 0 (TF0, TF0, TF1) 20
$\begin{tabular}{l}{llllllllllllllllllllllllllllllll$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0 (TF4, TF3, TF0) 19 Computed 0 (TF0, TF0, TF1) 20 Computed
ation >>>>>TFC 2 >>>>>sctfc >>>>>> $\betac (FDD only)$ >>>>> $\betad$ >>>>> $\betac (FDD only)$ >>>>> $\betad$ >>>>>TFC 3 >>>>>tfc 3 >>>>>tfc >>>>>tfc >>>>>tfc >>>>>gainFactorInform ation >>>>>tfc >>>>>tfc >>>>>gainFactorInform ation >>>>>bfc (FDD only) >>>>>ffc >>>>>bfc (FDD only) >>>>>ffc >>>>>bfd >>>>>ffc >>>>>bfc (FDD only) >>>>>bfd >>>>>ffc >>>>>bfc (FDD only) >>>>>bfd >>>>>bfc (FDD only) >>>>>bfd >>>>>bfc (FDD only) >>>>>bfd >>>>>bfc >>>>>bfc >>>>>bfc >>>>>bfc >>>>>bfc >>>>>bfc >>>>>bfc >>>>>bfc >>>>>bfc >>>>>bfc >>>>>bfc >>>>>bfc >>>>>bfc >>>>>bfc >>>>>bfc >>>>>bfc >>>>>bfc >>>>>>bfc >>>>>bfc >>>>>bfc >>>>>>bfc >>>>>bfc >>>>>>>bfc >>>>>bfc >>>>>>bfc >>>>>>bfc >>>>>bfc >>>>>>bfc >>>>>>bfc >>>>>>bfc >>>>>>>>>>>bfc >>>>>>>>>>bfc >>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0 (TF4, TF3, TF0) 19 Computed 0 (TF0, TF0, TF1) 20 Computed
autori >>>>>TFC 2 >>>>>sqainFactorInform ation >>>>> $\beta c$ (FDD only) >>>>> $\beta d$ >>>>> $\beta d$ >>>>>TFC 3 >>>>>tfc 3 >>>>>tfc >>>>>tfc >>>>>tfc >>>>>gainFactorInform ation >>>>>tfc >>>>>tfc >>>>>gainFactorInform ation >>>>>bf (FDD only) >>>>> $\beta d$ >>>>>tfc >>>>>bf (FDD only) >>>>> $\beta d$ >>>>>tfc >>>>>bf (FDD only) >>>>> $\beta d$ >>>>>bf (FDD only) >>>>> $\beta d$ >>>>>bf (FDD only) >>>>>bf (FDD only) >>>>>>>>bf (FDD only) >>>>>bf (FDD only) >>>>>bf (FDD only) >>>>>bf (FDD only) >>>>>>>>bf (FDD only) >>>>>>>>>>>bf (FDD only) >>>>>tfc 6 >>>>>tfc 6 >>>>>>tfc (FDD only)	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0 (TF4, TF3, TF0) 19 Computed 0 (TF0, TF0, TF1) 20 Computed
$\begin{tabular}{l}{l} \\ \hline \end{tabular} \\ \hline \end{tildersized} \\ $	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0 (TF4, TF3, TF0) 19 Computed 0 (TF0, TF0, TF1) 20 Computed

>>>>>>referenceTFCId	0
>>>>>TFC 7	(TF1, TF0, TF1)
>>>>>>ctfc	21
>>>>>>gainFactorInform	Computed
ation	
>>>>>>referenceTFCId	0
>>>>>TFC 8	(TF2, TF1, TF1)
>>>>>ctfc	27
>>>>>>gainFactorInform	computed
ation	
>>>>>>Bc (FDD only)	
>>>>>>Bd	
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	0
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	(15, 152, 151)
>>>>>>>CIIC	33
>>>>>>gainFactorInform	computed
ation	
>>>>>>>>reterence I FCId	
>>>>> IFC 10	(1F4, 1F3, 1F1)
>>>>>>ctfc	39
>>>>>>>gainFactorInform	signalled
ation	
>>>>>>βc (FDD only)	11
>>>>>>Bd	15
>>>>>>>>>	0
> TFC subset list	
> TEC subset 1	(cpoach rate 6.6)
>>TFC Subset T	
>>> Allowed transport	(1FO1, 1FO2, TECS, TEC
Ionnal combination list	TEC9
A TEC autoat 2	(appageb rate 9.95)
>>TFC Subset 2	
>>> Allowed transport	
format combination list	1FC3, 1FC4, 1FC6,
TEO I IO	1FC7, 1FC8, 1FC9)
>>TFC subset 3	(speech rate 12.65)
	(TEQ4 TEQ2
>>> Allowed transport	(TFC1, TFC2,
>>> Allowed transport format combination list	(TFC1, TFC2, TFC3, TFC4, TFC5,
>>> Allowed transport format combination list	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8,
>>> Allowed transport format combination list	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10)
>>> Allowed transport format combination list dl-CommonTransChInfo	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10)
<ul> <li>&gt;&gt;&gt; Allowed transport format combination list</li> <li>dl-CommonTransChInfo</li> <li>&gt;tfcs-SignallingMode</li> </ul>	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10)
<ul> <li>&gt;&gt;&gt; Allowed transport format combination list</li> <li>dl-CommonTransChInfo</li> <li>&gt;tfcs-SignallingMode</li> <li>ul-CommonTransChInfo</li> </ul>	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only)	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only)	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE
>>> Allowed transport format combination list           dl-CommonTransChInfo           >tfcs-SignallingMode           ul-CommonTransChInfo           >tfcs-ID (TDD only)           >sharedChannelIndicator           (TDD only)           > tfc-Subset	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI
>>> Allowed transport format combination list           dl-CommonTransChInfo           >tfcs-SignallingMode           ul-CommonTransChInfo           >tfcs-ID (TDD only)           >sharedChannelIndicator           (TDD only)           > tfc-Subset           >dl-TFCS	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>explicitTFCS-	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>explicitTFCS- ConfigurationMode	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>explicitTFCS- ConfigurationMode >>>ctfcSize	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>explicitTFCS- ConfigurationMode >>>ctfcSize >>>TFCS representation	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>explicitTFCS- ConfigurationMode >>>ctfcSize >>>TFCS representation >>>>TFCS list	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>explicitTFCS- ConfigurationMode >>>ctfcSize >>>TFCS representation >>>>TFCS list	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>explicitTFCS- ConfigurationMode >>>ctfcSize >>>TFCS representation >>>>TFCS list >>>>TFC 1	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition (TF0, TF0, TF0, TE0)
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>explicitTFCS- ConfigurationMode >>>ctfcSize >>>TFCS representation >>>>TFCS list >>>>TFC 1	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition (TF0, TF0, TF0, TF0) 0
>>> Allowed transport format combination list          dl-CommonTransChInfo         >tfcs-SignallingMode         ul-CommonTransChInfo         >tfcs-ID (TDD only)         >sharedChannelIndicator (TDD only)         > tfc-Subset         >dl-TFCS         >>explicitTFCS- ConfigurationMode         >>>>TFCS representation         >>>>TFCS list         >>>>>TFC 1         >>>>>>tfc 2	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition (TF0, TF0, TF0, TF0) 0
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>explicitTFCS- ConfigurationMode >>>ctfcSize >>>>TFCS representation >>>>TFCS list >>>>>TFC 1 >>>>>TFC 1	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition (TF0, TF0, TF0, TF0) 0 (TF1, TF0, TF0, TF0, TF0, TF0, TF0, TF0,
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>explicitTFCS- ConfigurationMode >>>ctfcSize >>>>TFCS representation >>>>TFCS list >>>>>TFC 1 >>>>>TFC 2	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition (TF0, TF0, TF0, TF0) 0 (TF1, TF0, TF0, TF0, TF0,
>>> Allowed transport format combination list          dl-CommonTransChInfo         >tfcs-SignallingMode         ul-CommonTransChInfo         >tfcs-ID (TDD only)         >sharedChannelIndicator (TDD only)         > tfc-Subset         >dl-TFCS         >>explicitTFCS- ConfigurationMode         >>>>TFCS representation         >>>>TFCS list         >>>>>TFC 1         >>>>>TFC 2         >>>>>ctfc	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition (TF0, TF0, TF0, TF0) 0 (TF1, TF0, TF0, TF0, 1
<pre>&gt;&gt;&gt; Allowed transport format combination list  dl-CommonTransChInfo &gt;tfcs-SignallingMode ul-CommonTransChInfo &gt;tfcs-ID (TDD only) &gt;sharedChannelIndicator (TDD only) &gt; tfc-Subset &gt;dl-TFCS &gt;&gt;explicitTFCS- ConfigurationMode &gt;&gt;&gt;ctfcSize &gt;&gt;&gt;&gt;TFCS representation &gt;&gt;&gt;&gt;TFCS list &gt;&gt;&gt;&gt;&gt;TFC 1  &gt;&gt;&gt;&gt;&gt;TFC 2 &gt;&gt;&gt;&gt;&gt;TFC 2 </pre>	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition (TF0, TF0, TF0, TF0) 0 (TF1, TF0, TF0, TF0, 1 (TF2, TF1, TF0, TF0, TF0, 1
>>> Allowed transport format combination list          dl-CommonTransChInfo         >tfcs-SignallingMode         ul-CommonTransChInfo         >tfcs-ID (TDD only)         >sharedChannelIndicator         (TDD only)         >tfc-Subset         >dl-TFCS         >>explicitTFCS- ConfigurationMode         >>>>TFCS representation         >>>>TFCS list         >>>>>TFC 1         >>>>>>TFC 2         >>>>>>TFC 3	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition (TF0, TF0, TF0, TF0) 0 (TF1, TF0, TF0, TF0, 1 (TF2, TF1, TF0, TF0)
<pre>&gt;&gt;&gt; Allowed transport format combination list  dl-CommonTransChInfo &gt;tfcs-SignallingMode ul-CommonTransChInfo &gt;tfcs-ID (TDD only) &gt;sharedChannelIndicator (TDD only) &gt; tfc-Subset &gt;dl-TFCS &gt;&gt;&gt;&gt;tfc-Subset &gt;dl-TFCS &gt;&gt;&gt;&gt;TFCS representation &gt;&gt;&gt;&gt;TFCS list &gt;&gt;&gt;&gt;&gt;TFC 1 &gt;&gt;&gt;&gt;&gt;TFC 2 &gt;&gt;&gt;&gt;&gt;TFC 3 &gt;&gt;&gt;&gt;&gt;tfc 3 &gt;&gt;&gt;&gt;&gt;tfc 3</pre>	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition (TF0, TF0, TF0, TF0) 0 (TF1, TF0, TF0, TF0, 1 (TF2, TF1, TF0, TF0) 7
<pre>&gt;&gt;&gt; Allowed transport format combination list  dl-CommonTransChInfo &gt;tfcs-SignallingMode ul-CommonTransChInfo &gt;tfcs-ID (TDD only) &gt;sharedChannelIndicator (TDD only) &gt; tfc-Subset &gt;dl-TFCS &gt;&gt;&gt;&gt;tfc-Subset &gt;dl-TFCS &gt;&gt;&gt;&gt;TFCS representation &gt;&gt;&gt;&gt;TFCS list &gt;&gt;&gt;&gt;&gt;TFC 1 &gt;&gt;&gt;&gt;&gt;TFC 1 &gt;&gt;&gt;&gt;&gt;TFC 2 &gt;&gt;&gt;&gt;&gt;TFC 3 &gt;&gt;&gt;&gt;&gt;TFC 3 </pre>	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition (TF0, TF0, TF0, TF0) 0 (TF1, TF0, TF0, TF0, 1 (TF2, TF1, TF0, TF0, 7 (TF3, TF2, TF0,

	13
>>>>>TFC 5	(TF4, TF3, TF0,
	TF0)
>>>>>ctfc	19
>>>>TFC 6	(TF0, TF0, TF1,
	TF0)
>>>>>ctfc	20
>>>>>TFC 7	(TF1, TF0, TF1,
	TF0)
>>>>>ctfc	21
>>>>>TFC 8	(TF2, TF1, TF1,
	TF0)
>>>>>ctfc	27
>>>>>TFC 9	(TF3, TF2, TF1,
	TF0)
>>>>>ctfc	33
>>>>>TFC 10	(TF4, TF3, TF1,
	TF0)
>>>>>ctfc	39
>>>>>TFC 11	(TF0, TF0, TF0,
	(TF1)
>>>>>ctfc	40
>>>>>TFC 12	(TE1, TE0, TE0
	TF1)
>>>>>ctfc	41
>>>>>TFC 13	(TF2 TF1 TF0
	(112, 111, 110, TF1)
	/7
	(TE3 TE2 TE0
	(11 3, 11 2, 11 0, TF1)
	53
	(TE4 TE3 TE0
	(11 4, 11 3, 11 0, TE1)
	59
	(TEO TEO TE1
	(110, 110, 111, TF1)
	60
	(TE1_TE0_TE1
SSSSSTEC 17	(11 1, 11 0, 11 1,
>>>>>TFC 17	TF1)
>>>>>TFC 17	TF1) 61
>>>>>TFC 17 >>>>>ctfc	TF1) 61 (TE2_TE1_TE1
>>>>>TFC 17 >>>>>>tfc >>>>>>tfc 17	TF1) 61 (TF2, TF1, TF1, TF1)
>>>>>TFC 17 >>>>>ctfc >>>>>TFC 18	TF1) 61 (TF2, TF1, TF1, TF1) 67
>>>>>TFC 17 >>>>>>tfc 17 >>>>>>tfc 18 >>>>>tfc 19	TF1) 61 (TF2, TF1, TF1, TF1) 67 (TF3, TF2, TF1
>>>>>TFC 17 >>>>>ctfc >>>>>TFC 18 >>>>>ctfc >>>>>TFC 18	TF1) 61 (TF2, TF1, TF1, TF1) 67 (TF3, TF2, TF1, TF1)
>>>>>TFC 17 >>>>>>tfc 17 >>>>>TFC 18 >>>>>tfc 18 >>>>>tfc 19	TF1) 61 (TF2, TF1, TF1, TF1) 67 (TF3, TF2, TF1, TF1) 73
>>>>>TFC 17 >>>>>>tfc 17 >>>>>TFC 18 >>>>>tfc 18 >>>>>tfc 19 >>>>>tfc 19	TF1) 61 (TF2, TF1, TF1, TF1) 67 (TF3, TF2, TF1, TF1) 73 (TF4, TF3, TF1)
>>>>>TFC 17 >>>>>>TFC 17 >>>>>TFC 18 >>>>>Ctfc >>>>>TFC 19 >>>>>Ctfc >>>>>TFC 20	TF1) 61 (TF2, TF1, TF1, TF1) 67 (TF3, TF2, TF1, TF1) 73 (TF4, TF3, TF1, TF1) 73
>>>>>TFC 17 >>>>>TFC 17 >>>>>TFC 18 >>>>>Ctfc >>>>>TFC 19 >>>>>Ctfc >>>>>TFC 20	TF1) 61 (TF2, TF1, TF1, TF1) 67 (TF3, TF2, TF1, TF1) 73 (TF4, TF3, TF1, TF1) 79
>>>>>TFC 17 >>>>>Ctfc >>>>>Ctfc >>>>>Ctfc >>>>>Ctfc >>>>>TFC 19 >>>>>Ctfc >>>>>TFC 20 PhyCH INEOPMATION	TF1) 61 (TF2, TF1, TF1, TF1) 67 (TF3, TF2, TF1, TF1) 73 (TF4, TF3, TF1, TF1) 79
>>>>>TFC 17           >>>>>Ctfc           >>>>>TFC 18           >>>>>Ctfc           >>>>>Ctfc           >>>>>Ctfc           >>>>>>TFC 19           >>>>>Ctfc           >>>>>>tfc 20           >>>>>>tfc PhyCH INFORMATION           FDD	TF1)         61         (TF2, TF1, TF1, TF1, TF1)         67         (TF3, TF2, TF1, TF1)         73         (TF4, TF3, TF1, TF1)         79
>>>>>TFC 17           >>>>>TFC 18           >>>>>tfc 18           >>>>>tfc 19           >>>>>tfc 20           >>>>>tfc 20           >>>>>tfc 10           >>>>>tfc 20           >>>>>tfc 10           >>>>>tfc 20           >>>>>tffc 10           >>>>>tfc 10	TF1)         61         (TF2, TF1, TF1, TF1, TF1)         67         (TF3, TF2, TF1, TF1)         73         (TF4, TF3, TF1, TF1)         79
>>>>>TFC 17           >>>>>TFC 18           >>>>>tfc 18           >>>>>tfc 19           >>>>>tfc 20           >>>>>tfc PhyCH INFORMATION           FDD           UL-DPCH-InfoPredef	TF1)         61         (TF2, TF1, TF1, TF1, TF1)         67         (TF3, TF2, TF1, TF1)         73         (TF4, TF3, TF1, TF1)         79
>>>>>TFC 17 >>>>>TFC 17 >>>>>TFC 18 >>>>>TFC 18 >>>>>TFC 19 >>>>>TFC 20 >>>>>tfc PhyCH INFORMATION FDD UL-DPCH-InfoPredef >ul-DPCH- BowerControllate	TF1)         61         (TF2, TF1, TF1, TF1, TF1)         67         (TF3, TF2, TF1, TF1)         73         (TF4, TF3, TF1, TF1)         79
>>>>>TFC 17 >>>>>>TFC 17 >>>>>TFC 18 >>>>>TFC 18 >>>>>TFC 19 >>>>>TFC 20 >>>>>tfc PhyCH INFORMATION FDD UL-DPCH-InfoPredef >ul-DPCH- PowerControlInfo	TF1)         61         (TF2, TF1, TF1, TF1, TF1)         67         (TF3, TF2, TF1, TF1)         73         (TF4, TF3, TF1, TF1)         79
>>>>>TFC 17         >>>>>TFC 18         >>>>>TFC 18         >>>>>TFC 19         >>>>>TFC 20         >>>>>TFC 20         >>>>>TFC 19         UL-DPCH-InfoPredef         >ul-DPCH-         PowerControlAlgorithm         >>powerControlAlgorithm	TF1)         61         (TF2, TF1, TF1, TF1)         67         (TF3, TF2, TF1, TF1)         73         (TF4, TF3, TF1, TF1)         79         Algorithm 1         1 <dp< td=""></dp<>
>>>>>TFC 17         >>>>>TFC 18         >>>>>TFC 18         >>>>>TFC 19         >>>>>TFC 20         >>>>>TFC 20         >>>>>TFC 19         UL-DPCH-InfoPredef         >ul-DPCH-         PowerControlAlgorithm         >>>tpcStepSize         >//foi Fuidtages	TF1)         61         (TF2, TF1, TF1, TF1)         67         (TF3, TF2, TF1, TF1)         73         (TF4, TF3, TF1, TF1)         79         Algorithm 1         1_dB         TDUE
>>>>>TFC 17         >>>>>TFC 18         >>>>>TFC 18         >>>>>TFC 19         >>>>>TFC 20         >>>>>TFC 20         >>>>>TFC 19         UL-DPCH-InfoPredef         >ul-DPCH-         PowerControlAlgorithm         >>>tpcStepSize         >tfci-Existence	TF1)         61         (TF2, TF1, TF1, TF1)         67         (TF3, TF2, TF1, TF1)         73         (TF4, TF3, TF1, TF1)         79         Algorithm 1         1_dB         TRUE         0.94
>>>>>TFC 17         >>>>>TFC 18         >>>>>TFC 18         >>>>>TFC 19         >>>>>TFC 20         >>>>>TFC 20         >>>>>TFC 19         UL-DPCH-InfoPredef         >ul-DPCH-         PowerControlInfo         >>powerControlAlgorithm         >>>tpcStepSize         >tfci-Existence         >puncturingLimit	TF1)         61         (TF2, TF1, TF1, TF1)         67         (TF3, TF2, TF1, TF1)         73         (TF4, TF3, TF1, TF1)         79         Algorithm 1         1         1         0.84
>>>>>TFC 17         >>>>>TFC 18         >>>>>TFC 18         >>>>>TFC 19         >>>>>TFC 20         >>>>>TFC 20         >>>>>TFC 20         >>>>>TFC 19         UL-DPCH-InfoPredef         >ul-DPCH-         PowerControlAlgorithm         >>>tpcStepSize         >tfci-Existence         >puncturingLimit         DL-	TF1)         61         (TF2, TF1, TF1, TF1)         67         (TF3, TF2, TF1, TF1)         73         (TF4, TF3, TF1, TF1)         79         Algorithm 1         1_dB         TRUE         0.84
>>>>>TFC 17 >>>>>>TFC 18 >>>>>>tfc 18 >>>>>TFC 18 >>>>>TFC 19 >>>>>tfc 19 >>>>>tfc 20 >>>>>tfc 20 >>>>>tfc PhyCH INFORMATION FDD UL-DPCH-InfoPredef >ul-DPCH- PowerControlInfo >>powerControlAlgorithm >>>tpcStepSize >tfci-Existence >puncturingLimit DL- CommonInformationPredef	TF1)         61         (TF2, TF1, TF1, TF1)         67         (TF3, TF2, TF1, TF1)         73         (TF4, TF3, TF1, TF1)         79         Algorithm 1         1         1         0.84
>>>>>TFC 17 >>>>>>TFC 18 >>>>>TFC 18 >>>>>TFC 19 >>>>>tfc 19 >>>>>tfc 20 >>>>>tfc 20 >>>>>tfc PhyCH INFORMATION FDD UL-DPCH-InfoPredef >ul-DPCH- PowerControlAlgorithm >>powerControlAlgorithm >>tpcStepSize >tfci-Existence >puncturingLimit DL- CommonInformationPredef >dl-DPCH-InfoCommon	TF1)         61         (TF2, TF1, TF1, TF1)         67         (TF3, TF2, TF1, TF1)         73         (TF4, TF3, TF1, TF1)         79         Algorithm 1         1_dB         TRUE         0.84
>>>>>TFC 17 >>>>>>TFC 18 >>>>>TFC 18 >>>>>TFC 19 >>>>>tfc 19 >>>>>tfc 20 >>>>>tfc 20 >>>>>tfc PhyCH INFORMATION FDD UL-DPCH-InfoPredef >ul-DPCH- PowerControlAlgorithm >>powerControlAlgorithm >>>tpcStepSize >tfci-Existence >puncturingLimit DL- CommonInformationPredef >dl-DPCH-InfoCommon >>spreadingFactor	TF1)         61         (TF2, TF1, TF1, TF1)         67         (TF3, TF2, TF1, TF1)         73         (TF4, TF3, TF1, TF1)         79         Algorithm 1         1_dB         TRUE         0.84
>>>>>TFC 17         >>>>>TFC 18         >>>>>TFC 18         >>>>>TFC 19         >>>>>TFC 20         >>>>>TFC 20         >>>>>TFC 20         >>>>>TFC 19         >>>>>tfc         >>>>>tfc         PhyCH INFORMATION FDD         UL-DPCH-InfoPredef         >ul-DPCH-         PowerControlAlgorithm         >>>tpcStepSize         >tfci-Existence         >puncturingLimit         DL-         CommonInformationPredef         >dl-DPCH-InfoCommon         >>spreadingFactor         >>tfci-Existence	TF1)         61         (TF2, TF1, TF1, TF1)         67         (TF3, TF2, TF1, TF1)         73         (TF4, TF3, TF1, TF1)         79         Algorithm 1         1_dB         TRUE         0.84         128         FALSE
>>>>>TFC 17         >>>>>TFC 18         >>>>>TFC 18         >>>>>TFC 19         >>>>>TFC 20         >>>>>TFC 20         >>>>>TFC 20         >>>>>TFC 19         >>>>>tfc         >>>>>tfc         PhyCH INFORMATION FDD         UL-DPCH-InfoPredef         >ul-DPCH-         PowerControlAlgorithm         >>>tpcStepSize         >tfci-Existence         >puncturingLimit         DL-         CommonInformationPredef         >dl-DPCH-InfoCommon         >>spreadingFactor         >>tfci-Existence         >pilotBits	TF1)         61         (TF2, TF1, TF1, TF1)         67         (TF3, TF2, TF1, TF1)         73         (TF4, TF3, TF1, TF1)         79         Algorithm 1         1         0.84         128         FALSE         4

## 3GPP TSG-RAN-WG2 Meeting #45 Shin-Yokohama, Japan, 15-19 October 2004

## *Tdoc* **#***R2-042586*

CHANGE REQUEST									
ж	25.331	CR 2444	жrev	-	ິສ Cເ	urrent versi	on: 6	.3.0	ж
For <mark>HELP</mark> on	For <u><b>HELP</b></u> on using this form, see bottom of this page or look at the pop-up text over the $\Re$ symbols.								
Proposed change affects: UICC apps# ME X Radio Access Network X Core Network									
Title:	TPC ster	o size in default o	configuration	S					
Source:	RAN WO	62							
Work item code: ३	E TEI5					Date: ೫	Nov 2	004	
Category: 3	B A Use <u>one</u> of F (co A (co B (ac C (fui D (co Detailed ex be found in	the following cate rrection) rresponds to a cor dition of feature), nctional modification (torial modification splanations of the a 3GPP <u>TR 21.900</u>	gories: rection in an e on of feature) ) above categori	earlier rele ies can	R ease)	elease: % Use <u>one</u> of t Ph2 R96 R97 R98 R99 Rel-4 Rel-5 Rel-6 Rel-7	Rel-6 the follow (GSM Pl (Release (Release (Release (Release (Release (Release (Release	ving rele hase 2) > 1996) > 1997) > 1998) > 1999) > 1999) > 1999) > 4) > 5) > 5) > 6) > 7)	eases:

Reason for change: अ	In section 13.7 of 25.331, the TPC Step size (in the power control algorithm) is shown as 1. It is unclear if this value reflect 1dB (asn1 value of '0') or asn1 value of '1' (which means 2dB). From TS 25.331 Section 13.7 >ul-DPCH-PowerControlInfo >>powerControlAlgorithm Algorithm 1 Algorithm 1 Algorithm 1 >>>tpcStepSize 1 1 1 1 From TS 25.331 Section 11.3 Actual value TPC-StepSizeFDD = IE value + 1 TPC-StepSizeFDD ::= INTEGER (01)
Summary of change: ℜ	Clarified that all IEs in the default configurations tables are shown in terms of their real value and not ASN.1. Also added "dB" to the TPC step size. Implementation of this CR by R99/Rel-4 UEs will not cause backwards compatibility issues Impact Analysis: A UE/UTRAN that has not implemented according to this CR, will require a modification.

Consequences if not approved: Due to a larger TPC step size there would typically be larger UL power spiking and could worsen the UL synchronization times of the UTRAN. This could cause additional UL interference problems to other users.

Clauses affected:	¥ 13.7
Other specs affected:	Y     N       %     Other core specifications       %     Test specifications       ORM Specifications
Other comments:	*

## How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at <u>http://www.3gpp.org/specs/CR.htm</u>. Below is a brief summary:

- 1) Fill out the above form. The symbols above marked **#** contain pop-up help information about the field that they are closest to.
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under <u>ftp://ftp.3gpp.org/specs/</u> For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.
- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

## 13.7 Parameter values for default radio configurations

The UE shall support the use of the default radio configurations that are specified in the following.

NOTE 1: These configurations are based on [41] and cover a number of RAB and signalling connection configurations.

In the table that is used to specify the parameter values for these default configurations, the following principles are used:

- Optional IEs that are not used are omitted;
- In case no parameter value is specified in a column, this means the value given the previous (left side) column applies.
- NOTE 2: If needed, signalling radio bearer RB4 is established after the completion of handover.
- NOTE 3: For each default configuration, the value of FDD, 3.84 Mcps TDD and 1.28 Mcps TDD parameters are specified. All parameters apply to FDD, 3.84 Mcps TDD and 1.28 Mcps TDD modes, unless explicitly stated otherwise. It should be noted that in this respect default configurations differ from pre-defined configurations, which only include parameter values for one mode.
- NOTE 4: The transport format sizes, indicated in the following table, concern the RLC PDU size, since all configurations concern dedicated channels. The transport block sizes indicated in TS 34.108 are different since these include the size of the MAC header.

NOTE 5: The tabular values included in this section, represent the actual IE values as in section 10, and not the ASN.1 representation of these values.

Configuration	3.4 kbps signalling	13.6 kbps signalling	7.95 kbps speech + 3.4 kbps signalling	12.2 kbps speech + 3.4 kbps signalling
Ref 34.108	2	3	6	4
Default configuration identity	0	1	2	3
RB INFORMATION				
rb-Identity	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6, RB7: 7
rlc-InfoChoice	Rlc-info	RIc-info	RIc-info	RIc-info
>ul-RLC-Mode	RB1: UM RB2- RB3: AM	RB1: UM RB2- RB3: AM	RB1: UM RB2- RB3: AM RB5-RB6: TM	RB1: UM RB2- RB3: AM RB5-RB7: TM
>>transmissionRLC- DiscardMode	RB1: N/A RB2- RB3: NoDiscard	RB1: N/A RB2- RB3: NoDiscard	RB1: N/A RB2- RB3: NoDiscard RB5- RB6: N/A	RB1: N/A RB2- RB3: NoDiscard RB5- RB7: N/A
>>>maxDat	RB1: N/A RB2- RB3: 15	RB1: N/A RB2- RB3: 15	RB1: N/A RB2- RB3: 15 RB5- RB6: N/A	RB1: N/A RB2- RB3: 15 RB5- RB7: N/A
>>transmissionWindowSiz e	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB6: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB7: N/A
>>timerRST	RB1: N/A RB2- RB3: 300	RB1: N/A RB2- RB3: 300	RB1: N/A RB2- RB3: 300 RB5- RB6: N/A	RB1: N/A RB2- RB3: 300 RB5- RB7: N/A
>>max-RST	RB1: N/A RB2- RB3: 1	RB1: N/A RB2- RB3: 1	RB1: N/A RB2- RB3: 1 RB5- RB6: N/A	RB1: N/A RB2- RB3: 1 RB5- RB7: N/A

Configuration	3.4 kbps signalling	13.6 kbps	7.95 kbps speech	12.2 kbps speech
		signaling	+ 3.4 kbps signalling	+ 3.4 kbps signalling
>>pollingInfo	RB1: N/A RB2- RB3: as below	RB1: N/A RB2- RB3: as below	RB1: N/A RB2- RB3: as below RB5- RB6: N/A	RB1: N/A RB2- RB3: as below RB5- RB7: N/A
>>>lastTransmissionPDU- Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>lastRetransmissionPD U-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300	RB2- RB3: 100	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A	RB1- RB3: N/A	RB1- RB3: N/A RB5- RB6: FALSE	RB1- RB3: N/A RB5- RB7: FALSE
>dl-RLC-Mode	RB1: UM RB2- RB3: AM	RB1: UM RB2- RB3: AM	RB1: UM RB2- RB3: AM RB5- RB6: TM	RB1: UM RB2- RB3: AM RB5- RB7: TM
>>inSequenceDelivery	RB1: N/A RB2- RB3: TRUE	RB1: N/A RB2- RB3: TRUE	RB1: N/A RB2- RB3: TRUE RB5- RB6: N/A	RB1: N/A RB2- RB3: TRUE RB5- RB7: N/A
>>receivingWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB6: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB7: N/A
>>dl-RLC-StatusInfo	RB1: N/A RB2- RB3: as below	RB1: N/A RB2- RB3: as below	RB1: N/A RB2- RB3: as below RB5- RB6: N/A	RB1: N/A RB2- RB3: as below RB5- RB7: N/A
>>>timerStatusProhibit	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100
>>>missingPDU-Indicator	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerStatusPeriodic	RB2- RB3: 300	RB2- RB3: 100	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A	RB1- RB3: N/A	RB1- RB3: N/A RB5- RB6: FALSE	RB1- RB3: N/A RB5- RB7: FALSE
rb-MappingInfo				
>UL- LogicalChannelMappings	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel
>>ul- TransportChannelType	Dch	Dch	Dch	Dch
>>>transportChannelldentit y	RB1- RB3: 1	RB1- RB3: 1	RB1- RB3: 3 RB5: 1, RB6: 2	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3
>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
>>rlc-SizeList	RB1- RB3: configured	RB1- RB3: configured	RB1- RB3: configured RB5- RB6: N/A	RB1- RB3: configured RB5- RB7: N/A
>>mac- LogicalChannelPriority	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: 5	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: 5
>DL- logicalChannelMappingList				
>>Mapping option 1	One mapping option	One mapping option	One mapping option	One mapping option
>>>dl- TransportChannelType	Dch	Dch	Dch	Dch
>>>>transportChannellden tity	RB1- RB3: 1	RB1- RB3: 1	RB1- RB3: 3 RB5: 1, RB6: 2	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3
>>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
TrCH INFORMATION PER TrCH				

Configuration	3.4 kbps signalling	13.6 kbps	7.95 kbps speech	12.2 kbps speech
		signalling	+ 3 4 kbns signalling	+ 3 4 kbps signalling
			5.4 Kops signaling	5.4 Kops signaling
UL- AddReconfTransChInfoList				
>Uplink transport channel type	dch	dch	dch	dch
>transportChannelIdentity	TrCH1: 1	TrCH1: 1	TrCH1: 1, TrCH2: 2, TrCH3: 3	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>transportFormatSet	DedicatedTransChT FS	DedicatedTransChT FS	DedicatedTransChT FS	DedicatedTransChT FS
>>dynamicTF-information				
>>>tf0/ tf0,1	TrCH1: (0x144, 1x144)	TrCH1: (0x144, 1x144)	TrCH1: (0x75) TrCH2: (0x 84 1x84) TrCH3: (0x144, 1x144)	TrCH1: (0x81) TrCH2: (0x 103, 1x103) TrCH3: (0x 60, 1x60) TrCH4: (0x144, 1x144)
>>>>rlcSize	BitMode	BitMode	BitMode	BitMode
>>>>sizeType	TrCH1: type 2, part1= 2, part2= 0 (144)	TrCH1: type 2, part1= 2, part2= 0 (144)	TrCH1: type 1: 75 TrCH2: type 1: 84 TrCH3: 2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 1: 81 TrCH2: type 1: 103 TrCH3: type 1: 60 TrCH4: 2: type 2, part1= 2, part2= 0 (144)
>>>>numberOfTbSizeList	TrCH1: Zero, one	TrCH1: Zero, one	TrCH1: Zero TrCH2-3: Zero, one	TrCH1: Zero TrCH2-4: Zero, one
>>>logicalChannelList	All	All	All	All
>>>tf 1	N/A	N/A	TrCH1: (1x39) TrCH2- TrCH4: N/A	TrCH1: (1x39) TrCH2- TrCH4: N/A
>>>>numberOfTransportBl ocks			TrCH1: One	TrCH1: One
>>>rlc-Size			TrCH1: BitMode	TrCH1: BitMode
>>>>sizeType			TrCH1: 1: 39	TrCH1: 1: 39
>>>numberOfTbSizeList			TrCH1: One	TrCH1: One
>>>logicalChannelList	N1/A	N1/A	TrCH1: all	TrCH1: all
>>>ti 2	N/A	N/A	TrCH1: (1x75) TrCH2- TrCH3: N/A	TrCH1: (1x81) TrCH2- TrCH4: N/A
>>>numberOfTransportBl			IrCH1: One	IrCH1: One
>>>>rlc-Size			TrCH1: BitMode	TrCH1: BitMode
>>>>sizeType			TrCH1: type 1: 75	TrCH1: type 1: 81
>>>numberOfTbSizeList			TrCH1: One	TrCH1: One
>>>logicalChannelList			TrCH1: all	TrCH1: all
>>semistaticTF-Information				
>>>tti	TrCH1: 40	TrCH1: 10	TrCH1- TrCH2: 20 TrCH3: 40	TrCH1- TrCH3: 20 TrCH4: 40
>>>channelCodingType	Convolutional	Convolutional	Convolutional	Convolutional
>>>>codingRate	TrCH1: Third	TrCH1: Third	TrCH1- TrCH2: Third TrCH3: Third	TrCH1- TrCH2: Third TrCH3: Half TrCH4: Third
>>>rateMatchingAttribute	TrCH1: 160	TrCH1: 160	TrCH1: 200 TrCH2: 190 TrCH3: 160	TrCH1: 200 TrCH2: 190 TrCH3: 235 TrCH4: 160
>>>crc-Size	TrCH1: 16	TrCH1: 16	TrCH1: 12 TrCH2: 0 TrCH3: 16	TrCH1: 12 TrCH2- TrCH3: 0 TrCH4: 16
DL-				
>Downlink transport channel type	dch	dch	dch	dch

Configuration	3.4 kbps signalling	13.6 kbps	7.95 kbps speech	12.2 kbps speech
		signalling	+ 3.4 kbps signalling	+ 3.4 kbps signalling
>dl-	TrCH1: 1	TrCH1: 1	TrCH1: 1, TrCH2: 2,	TrCH1: 1, TrCH2: 2,
I ransportChannelIdentity			TrCH3: 3	IrCH3: 3, IrCH4: 4
>tfs-SignallingMode	SameAsUL	SameAsUL	Explicit	Explicit
			<only on="" td="" tf0="" trch1<=""><td><only on="" td="" tf0="" trch1<=""></only></td></only>	<only on="" td="" tf0="" trch1<=""></only>
			is different and	is different and
>>transportFormatSet			DedicatedTransChT	DedicatedTransChT
			FS	FS
>>>dynamicTF-information				
>>>>tf0/ tf0,1			TrCH1: (1x0)	TrCH1: (1x0)
>>>>ricoize			TrCH1: type 1: 0	TrCH1: type 1: 0
>>>numberOfTbSizeList			TrCH1: One	TrCH1: One
>>>logicalChannelList			All	All
>>ULTrCH-Id	TrCH1: 1	TrCH1: 1	TrCH1: 1, TrCH2: 2,	TrCH1: 1, TrCH2: 2,
> deb QualityTarget			TrCH3: 3	TrCH3: 3, TrCH4: 4
>>bler-QualityValue	TrCH1: 5x10 ⁻²	TrCH1: 5x10 ⁻²	TrCH1: 7x10 ⁻³	TrCH1: 7x10 ⁻³
			TrCH2- TrCH3:	TrCH2- TrCH4:
			Absent	Absent
TrCH INFORMATION, COMMON				
ul-CommonTransChInfo				
>tfcs-ID (TDD only)	1	1	1	1
>sharedChannelIndicator	FALSE	FALSE	FALSE	FALSE
>tfc-Subset	Absent, not required	Absent. not required	Absent. not required	Absent, not required
>ul-TFCS	Normal TFCI	Normal TFCI	Normal TFCI	Normal TFCI
	signalling	signalling	signalling	signalling
>>explicitTFCS- ConfigurationMode	Complete	Complete	Complete	Complete
>>>ctfcSize	Ctfc2Bit	Ctfc2Bit	Ctfc4Bit	Ctfc6Bit
>>>>TFCS representation	Addition	Addition	Addition	Addition
>>>>TFCS list				
>>>>>1FCS1	(1F0)	(1F0)	(1F0, 1F0, 1F0)	(TF0, TF0, TF0, TF0)
>>>>>ctfc	0	0	0	0
>>>>>>sgainFactorInform	Computed	Computed	Computed	Computed
>>>>>>>referenceTFCId	0	0	0	0
>>>>>TFCS 2	(TF1)	(TF1)	(TF1, TF0, TF0)	(TF1, TF0, TF0, TF0)
>>>>>ctfc	1	1	1	1
>>>>>gainFactorInform	Signalled	Signalled	Computed	Computed
ation		44	N1/A	N1/A
>>>>>>>βc (FDD only)	11	11	N/A	N/A
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	15	15	N/A	N/A
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	0	0	0 (TF2_TF1_TF0)	0 (TF2_TF1_TF1
			(112, 111, 110)	TF0)
>>>>>ctfc			5	11
>>>>>>sgainFactorInform			Computed	Computed
>>>>>>referenceTFCId			0	0
>>>>>TFCS 4			(TF0, TF0, TF1)	(TF0, TF0, TF0,
			6	1F1) 12
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>			Computed	Computed
ation			comparod	Comparod
>>>>>βc (FDD only)			N/A	N/A

Configuration	3.4 kbps signalling	13.6 kbps	7.95 kbps speech	12.2 kbps speech
		signalling	+ 2.4 kbpc signalling	+ 2.4 kbpc cignolling
			3.4 Kbps signalling	3.4 Kops signalling
>>>>>Bd			N/A	N/A
>>>>>>referenceTFCId			0	0
>>>>>TFCS 5			(TF1, TF0, TF1)	(TF1, TF0, TF0, TF1)
>>>>>ctfc			7	13
>>>>>>sgainFactorInform ation			Computed	Computed
>>>>>>referenceTFCId			0	0
>>>>>TFCS 6			(TF2, TF1, TF1)	(TF2, TF1, TF1, TF1)
>>>>>ctfc			11	23
>>>>>>gainFactorInform ation			Signalled	Signalled
>>>>>βc (FDD only)			11	11
>>>>>βd			15	15
>>>>>>referenceTFCId			0	0
dl-CommonTransChInfo				
>tfcs-SignallingMode	Same as UL	Same as UL	Same as UL	Same as UL
PhyCH INFORMATION FDD				
UL-DPCH-InfoPredef				
>ul-DPCH-				
PowerControlInfo	Algorithm 1	Algorithm 1	Algorithm 1	Algorithm 1
>>powerControlAigontinin				
>tfci-Existence				
>puncturingLimit	1	1	1	0.88
DL-				
CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>spreadingFactor	256	128	128	128
>>tfci-Existence	FALSE	FALSE	FALSE	FALSE
>>pilotBits	4	4	4	4
>>positionFixed	N/A	N/A	Fixed	Fixed
PhyCH INFORMATION				
3.84 Mcps TDD				
UL-DPCH-InfoPredet				
>UI-DPCH- RowarControllato				
>doch-Constant//alue	0	0	0	0
>commonTimeslotInfo	0	0		0
>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>tfci-Coding	4	4	16	16
>>puncturingLimit	1	0.92	0.52	0.88
>>repetitionPeriodAndLeng th	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
DL-				
CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>commonTimeslotInfo				
>>secondInterleavingMod e	frameRelated	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	4	4	16	16
>>>puncturingLimit	1	0.92	0.52	0.92
>>>repetitionPeriodAndLe ngth	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
PhyCH INFORMATION				
UL-DPCH-InfoPredef				

Configuration	3.4 kbps signalling	13.6 kbps signalling	7.95 kbps speech +	12.2 kbps speech +
			3.4 kbps signalling	3.4 kbps signalling
>commonTimeslotInfo				
>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>tfci-Coding	4	4	16	16
>>puncturingLimit	1	0.64	0.80	0.60
>>repetitionPeriodAndLeng	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
th				
DL-				
CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>commonTimeslotInfo				
>>>secondInterleavingMod	frameRelated	frameRelated	frameRelated	frameRelated
е				
>>>tfci-Coding	4	4	16	16
>>>puncturingLimit	1	0.64	0.80	0.60
>>>repetitionPeriodAndLe ngth	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1

Configuration	28.8 kbps conv.	32 kbps conv. CS-	64kbps conv. CS-	14.4 kbps
	CS- data +	data +	data +	streaming CS-
	3.4 kbps signalling	3.4 kbps signalling	3.4 kbps signalling	data +
				3.4 kbps signalling
Ref 34.108	12	14	13	15
Default configuration	4	5	6	7
identity				
RB INFORMATION				
rb-Identity	RB1: 1, RB2: 2,			
	RB3: 3, RB5: 5			
rlc-InfoChoice	RIc-info	RIc-info	RIc-info	RIc-info
>ul-RLC-Mode	RB1: UM	RB1: UM	RB1: UM	RB1: UM
	RB2- RB3: AM	RB2- RB3: AM	RB2- RB3: AM	RB2- RB3: AM
	RB5: TM	RB5: TM	RB5: TM	RB5: TM
>>transmissionRLC-	RB1: N/A	RB1: N/A	RB1: N/A	RB1: N/A
DiscardMode	RB2- RB3:	RB2- RB3:	RB2- RB3:	RB2- RB3:
	NoDiscard	NoDiscard	NoDiscard	NoDiscard
	RB5: N/A	RB5: N/A	RB5: N/A	RB5: N/A
>>>maxDat	RB1: N/A	RB1: N/A	RB1: N/A	RB1: N/A
	RB2- RB3: 15	RB2- RB3: 15	RB2- RB3: 15	RB2- RB3: 15
	RB5: N/A	RB5: N/A	RB5: N/A	RB5: N/A
>>transmissionWindowSiz	RB1: N/A	RB1: N/A	RB1: N/A	RB1: N/A
е	RB2- RB3: 128 for			
	UEs with more than			
	10 kbyte "total RLC			
	AM buffer size" and			
	32 otherwise	32 otherwise	32 otherwise	32 otherwise
	RB5: N/A	RB5: N/A	RB5: N/A	RB5: N/A
>>timerRST	RB1: N/A	RB1: N/A	RB1: N/A	RB1: N/A
	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
	RB5: N/A	RB5: N/A	RB5: N/A	RB5: N/A
>>max-RST	RB1: N/A	RB1: N/A	RB1: N/A	RB1: N/A
	RB2- RB3: 1	RB2- RB3: 1	RB2- RB3: 1	RB2- RB3: 1
	RB5: N/A	RB5: N/A	RB5: N/A	RB5: N/A
>>pollingInfo	RB1: N/A	RB1: N/A	RB1: N/A	RB1: N/A
-	RB2- RB3: as below			
	RB5: N/A	RB5: N/A	RB5: N/A	RB5: N/A
>>>lastTransmissionPDU- Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>lastRetransmissionPD U-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A	RB1- RB3: N/A	RB1- RB3: N/A	RB1- RB3: N/A
, , , , , , , , , , , , , , , , , , ,	RB5: FALSE	RB5: FALSE	RB5: FALSE	RB5: FALSE

Configuration	28.8 kbps conv.	32 kbps conv. CS-	64kbps conv. CS-	14.4 kbps
	CS- data +	data +	data +	streaming CS-
	3.4 kbps signalling	3.4 kbps signalling	3.4 kbps signalling	data +
				3.4 kbps signalling
>dl-RLC-Mode	RB1: UM	RB1: UM	RB1: UM	RB1: UM
	RB2- RB3: AM	RB2- RB3: AM	RB2- RB3: AM	RB2- RB3: AM
in One many a Dalta and	RB5: TM	RB5: TM	RB5: TM	RB5: TM
>>InSequenceDelivery	RB1: N/A	RB1: N/A	RB1: N/A	RB1: N/A
	RDZ- RDS. IRUE	RDZ- RDS. IRUE	RDZ- RDS. IRUE	RDZ- RDJ. IRUE RB5· N/A
>>receivingWindowSize	RB1: N/A	RB1: N/A	RB1: N/A	RB1: N/A
>>receivingvindowSize	RB2- RB3 128 for	RB2- RB3 128 for	RB2- RB3 128 for	RB2- RB3 128 for
	UEs with more than	UEs with more than	UEs with more than	UEs with more than
	10 kbyte "total RLC	10 kbyte "total RLC	10 kbyte "total RLC	10 kbyte "total RLC
	AM buffer size" and	AM buffer size" and	AM buffer size" and	AM buffer size" and
	32 otherwise	32 otherwise	32 otherwise	32 otherwise
	RB5: N/A	RB5: N/A	RB5: N/A	RB5: N/A
>>dl-RLC-StatusInfo	RB1: N/A	RB1: N/A	RB1: N/A	RB1: N/A
	RB2- RB3: as below	RB2- RB3: as below	RB2- RB3: as below	RB2- RB3: as below
	RB5: N/A	RB5: N/A	RB5: N/A	RB5: N/A
>>>timerStatusProhibit	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100
>>>missingPDU-Indicator	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerStatusPeriodic	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A	RB1- RB3: N/A	RB1- RB3: N/A	RB1- RB3: N/A
uh Manania altafa	RB5: FALSE	RB5: FALSE	RB5: FALSE	RB5: FALSE
	Onal agicalChangel	Onal agicalChangel	Onal agicalChangel	Onel ericelChernel
>UL-	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel
LogicalChannelMappings	Deb	Deb	Deb	Deb
>>ui- TransportChannelType	DCH	DCII	DCII	DCH
>>>transportChannelldenti	RB1- RB3· 2	RB1- RB3· 2	RB1- RB3· 2	RB1- RB3· 2
ty	RB5: 1	RB5: 1	RB5: 1	RB5: 1
>>logicalChannelIdentity	RB1: 1 RB2: 2	RB1: 1 RB2: 2	RB1: 1 RB2: 2	RB1: 1 RB2: 2
2 / logical chain london adv	RB3: 3	RB3: 3	RB3: 3	RB3: 3
	RB5: N/A	RB5: N/A	RB5: N/A	RB5: N/A
>>rlc-SizeList	RB1- RB3:	RB1- RB3:	RB1- RB3:	RB1- RB3:
	configured	configured	configured	configured
	RB5: N/A	RB5: N/A	RB5: N/A	RB5: N/A
>>mac-	RB1: 1, RB2: 2,	RB1: 1, RB2: 2,	RB1: 1, RB2: 2,	RB1: 1, RB2: 2,
LogicalChannelPriority	RB3: 3	RB3: 3	RB3: 3	RB3: 3
	RB5: 5	RB5: 5	RB5: 5	RB5: 5
>DL-				
	On a manufacture station	On a manufacture station	On a manufacture station	On a manufacture station
>>Mapping option 1	One mapping option	One mapping option	One mapping option	One mapping option
>>>0I-	DCN	DCN	DCN	DCN
tity	RB5- 1	RB5-1	RB5- 1	RB5- 1
	RB1: 1 RB2: 2	RB1: 1 RB2: 2	RB1: 1 RB2: 2	RB1: 1 RB2: 2
	RB3: 3	RB3: 3	RB3: 3	RB3: 3
	RB5: N/A	RB5: N/A	RB5: N/A	RB5: N/A
TrCH INFORMATION PER				
TrCH				
UL-				
AddReconfTransChInfoLis				
t				
>Uplink transport channel	dch	dch	dch	dch
type				
>transportChannelldentity	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2
>transportFormatSet	DedicatedTransChT	DedicatedTransChT	DedicatedTransChT	DedicatedTransChT
	15	15	15	FS
>>aynamic I F-information				T-014. (0.570
>>>tfU/ tfU,1	1/CH1: (UX5/6,	1rCH1: (0x640,	TrCH1: (0x640,	17CH1: (UX576,
	1X370, 2X576) TrCH2: (0 $\times 1.44$	1X04U) TrCH2: (0v144	∠X04U) TrCH2: (0v144	1X370) TrCH2: (0v144
	1v144, 1v144)	1v144, 1v144)	1v144, 1v144)	1x144, 1x144)
	דדואו)	יאין	דדואי)	יאין

Configuration	28.8 kbps conv.	32 kbps conv. CS-	64kbps conv. CS-	14.4 kbps
_	CS- data +	data +	data +	streaming CS-
	3.4 kbps signalling	3.4 kbps signalling	3.4 kbps signalling	data +
				3.4 kbps signalling
>>>rlcSize	TrCH1: OctetMode	TrCH1: OctetMode	TrCH1: OctetMode	TrCH1: OctetMode
>>>>sizeType	TrCH1: type 2	TrCH1: type 2	TrCH1: type 2	TrCH1: type 2
2222312CT ypc	part1 = 9 part2 = 2	part1 = 11 part2 = 2	part1 = 11 part2 = 2	part1=9
	(576)	(640)	(640)	part2= 2 (576)
	TrCH2: type 2,	TrCH2: type 2,	TrCH2: type 2,	TrCH2: type 2,
	part1= 2, part2= 0	part1= 2, part2= 0	part1= 2, part2= 0	part1= 2,
	(144)	(144)	(144)	part2= 0 (144)
>>>>numberOfTbSizeList	TrCH1: Zero,1, 2	TrCH1: Zero, one	TrCH1: Zero, 2	TrCH1: Zero, one,
	TrCH2: Zero, one	TrCH2: Zero, one	TrCH2: Zero, one	TrCH2: Zero, one
>>>>logicalChannelList	All	All	All	All
>>semiStaticTF-				
Information	T OLIA 40	FOLK 00	FOLK 00	T OLIVA 40
>>>tti	TrCH1: 40 TrCH2: 40	TrCH1: 20 TrCH2: 40	TrCH1: 20 TrCH2: 40	TrCH1: 40 TrCH2: 40
>>>channelCodingType	TrCH1: Turbo	TrCH1: Turbo	TrCH1: Turbo	TrCH1: Turbo
3 /1	TrCH2:	TrCH2:	TrCH2:	TrCH2:
	Convolutional	Convolutional	Convolutional	Convolutional
>>>>codingRate	TrCH1: N/A	TrCH1: N/A	TrCH1: N/A	TrCH1: N/A
-	TrCH2: Third	TrCH2: Third	TrCH2: Third	TrCH2: Third
>>>rateMatchingAttribute	TrCH1: 180	TrCH1: 185	TrCH1: 170	TrCH1: 165
	TrCH2: 160	TrCH2: 160	TrCH2: 160	TrCH2: 160
>>>crc-Size	TrCH1: 16	TrCH1: 16	TrCH1: 16	TrCH1: 16
	TrCH2: 16	TrCH2: 16	TrCH2: 16	TrCH2: 16
DL-				
AddRecont I ransChintoLis				
I Downlink transport	dob	dab	dab	dob
>Downlink transport	den	uch	uch	ach
TransportChannelIdentity	110111. 1, 110112. 2	110111. 1, 110112. 2	110111. 1, 110112. 2	110111. 1, 110112. 2
(should be as for UL)				
>tfs-SignallingMode	SameAsUL	SameAsUL	SameAsUL	SameAsUL
>>transportFormatSet				
>>>dvnamicTF-information				
>>>>tf0/ tf0,1				
>>>rlcSize				
>>>>sizeType				
>>>>numberOfTbSizeList				
>>>>logicalChannelList				
>>ULTrCH-Id	TrCH1: 1, TrCH2: 2			
>dch-QualityTarget	2	2	0	2
>>bler-QualityValue	TrCH1: 2x10 ⁻³	TrCH1: 2x10 ⁻³	TrCH1: 2x10 ⁻³	TrCH1: 1x10 ⁻²
	TrCH2: Absent	TrCH2: Absent	TrCH2: Absent	TrCH2: Absent
TrCH INFORMATION,				
ul-Common I ransChinto	4	4	4	4
>tics-ID (IDD only)				
<pre>&gt;snaredChannelindicator (TDD colv)</pre>	FALSE	FALSE	FALSE	FALSE
(TDD OIlly)	Abcont not required	Abcont not required	Abcont not required	Abcont not required
	Normal TECI	Normal TECI	Normal TECI	Normal TECI
	signalling	signalling	signalling	signalling
>>explicitTFCS-	Complete	Complete	Complete	Complete
ConfigurationMode		Complete		Complete
>>>ctfcSize	Ctfc4Bit	Ctfc2Bit	Ctfc2Bit	Ctfc4Bit
>>>>TFCS representation	Addition	Addition	Addition	Addition
>>>>TFCS list				
>>>>>TFCS 1	(TF0, TF0)	(TF0, TF0)	(TF0, TF0)	(TF0, TF0)
>>>>>>ctfc	0	0	0	0
>>>>>gainFactorInform	Computed	Computed	Computed	Computed
ation	•	•	•	

Configuration	28.8 kbps conv.	32 kbps conv. CS-	64kbps conv. CS-	14.4 kbps
	3.4 kbps signalling	data + 3.4 kbps signalling	data + 3.4 kbps signalling	streaming CS- data +
	· · · · · · · · · · · · · · · · · · ·	5		3.4 kbps signalling
>>>>>>referenceTFCId	0	0	0	0
>>>>>TFCS 2	(TF1, TF0)	(TF1, TF0)	(TF1, TF0)	(TF1, TF0)
>>>>>>>ctfc	1 Operational	1	1 Oceanousta d	1
>>>>>>>sgainFactorinform	Computed	Computed	Computed	Computed
>>>>>>Bc (FDD only)	N/A	N/A	N/A	N/A
>>>>>>>Bd	N/A	N/A	N/A	N/A
>>>>>>referenceTFCId	0	0	0	0
>>>>>TFCS 3	(TF2, TF0)	(TF0, TF1)	(TF0, TF1)	(TF0, TF1)
>>>>>ctfc	2	2	2	2
>>>>>>gainFactorInform ation	Computed	Computed	Computed	Computed
>>>>>>referenceTFCId	0	0	0	0
>>>>>TFCS 4	(TF0, TF1)	(TF1, TF1)	(TF1, TF1)	(TF1, TF1)
>>>>>>ctfc	3	3	3	3
>>>>>>sgainFactorInform ation	Computed	Signalled	Signalled	Signalled
>>>>>>βc (FDD only)	N/A	8	8	11
>>>>>βd	N/A	15	15	15
>>>>>referenceTFCId	0	0	0	0
>>>>>TFCS 5	(TF1, TF1)	N/A	N/A	
>>>>>>ctfc	4			
>>>>>>gainFactorInform ation	Computed			
>>>>>>referenceTFCId	0			
>>>>>TFCS 6	(TF2, TF1)	N/A	N/A	
>>>>>>ctfc	5			
>>>>>>sgainFactorInform ation	Signalled			
>>>>>βc (FDD only)	8			
>>>>>βd	15			
>>>>>>referenceTFCId	0			
>>>>>TFCS 7				
>>>>>>ctfc				
>>>>>>sgainFactorInform ation				
>>>>>>referenceTFCId				
>>>>>TFCS 8				
>>>>>>ctfc				
>>>>>>sgainFactorInform ation				
>>>>>>referenceTFCId				
>>>>>TFCS 9				
>>>>>>ctfc				
>>>>>>sgainFactorInform ation				
>>>>>>referenceTFCId				
>>>>>TFCS 10				
>>>>>>ctfc				
>>>>>>sgainFactorInform ation				
>>>>>βc (FDD only)				
>>>>>βd				
>>>>>>referenceTFCId				
dl-CommonTransChInfo				
>tfcs-SignallingMode	Same as UL	Same as UL	Same as UL	Same as UL
PhyCH INFORMATION FDD				

Configuratio	on 2 3.4	8.8 kbps conv. CS- data + kbps signalling	32 kbps conv. CS- data + 3.4 kbps signalling	64kbps conv. CS- data + 3.4 kbps signalling	14.4 kbps streaming CS- data + 3.4 kbps signalling
UI -DPCH-InfoPred	lef				J.4 Kbp3 Signaling
>ul-DPCH- PowerControlInfo					
>>powerControlAld	orithm Alo	porithm 1	Algorithm 1	Algorithm 1	Algorithm 1
>>tpcStepSize	1 0	B	1 dB	1 dB	1 dB
>tfci-Existence	TR	UE	TRUE	TRUE	TRUE
	0.0	12	0.8	0.92	1
DL- CommonInformatic	nPrede				
>dl-DPCH-InfoCon	nmon				
>>spreadingFactor	64		64	32	128
>>tfci-Existence	TR	UE	TRUE	TRUE	TRUE
>>pilotBits	8		8	8	8
>>positionFixed	Fle	exible	Flexible	Flexible	Flexible
PhyCH INFORMA 3.84 Mcps TDD UL-DPCH-InfoPred	FION lef				
>ul-DPCH- PowerControlInfo					
>>dpch-ConstantV	alue 0		0	0	0
>commonTimeslot	nfo				
>>secondInterleav e	ingMod fra	meRelated	frameRelated	frameRelated	frameRelated
>>tfci-Coding	16		8	8	8
>>puncturingLimit	0.4	14	0.8	0.56	0.8
>>repetitionPeriod	AndLen rep	petitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
DL- CommonInformatic f	nPrede				
>dl-DPCH-InfoCon	nmon				
>>commonTimeslo	otInfo				
>>>secondInterlea	vingMo fra	meRelated	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	16		8	8	8
>>>puncturingLimi	t 0.4	14	0.64	0.56	0.8
>>>repetitionPerio	dAndLe rep	petitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
PhyCH INFORMA 1.28 Mcps TDD	TION				
UL-DPCH-InfoPred	lef				
>commonTimeslot	nfo				
>>secondInterleav e	ingMod fra	meRelated	frameRelated	frameRelated	frameRelated
>>tfci-Coding	16		8	8	8
>>puncturinaLimit	0.6	64	0.60	0.64	1
>>repetitionPeriod	AndLen rer	petitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
gth					
DL- CommonInformatic f	nPrede				
>dl-DPCH-InfoCon	nmon				
>>commonTimeslo	otInfo				
>>>secondInterlea	vingMo fra	meRelated	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	16		8	8	8
>>>puncturingLimi	t 0.6	64	0.60	0.64	0.88
>>>repetitionPerio	dAndLe rep	petitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1

Configuration	28.8 kbps streaming CS-	57.6 kbps streaming CS-	12.2 kbps
	data +	data +	) +
	3.4 kbps signalling	3.4 kbps signalling	3.4 kbps signalling
Ref 34.108	16	17	1a
Default configuration identity	8	9	10
RB INFORMATION			
rb-Identity	RB1: 1, RB2: 2, RB3: 3, RB5: 5	RB1: 1, RB2: 2, RB3: 3, RB5: 5	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6, RB7: 7
rlc-InfoChoice	Rlc-info	Rlc-info	RIc-info
>ul-RLC-Mode	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5-RB7: TM
>>transmissionRLC-	RB1: N/A	RB1: N/A	RB1: N/A
DiscardMode	RB2- RB3:	RB2- RB3:	RB2- RB3:
	NoDiscard	NoDiscard	NoDiscard
>>>maxDat	RB1: N/A	RB1: N/A	RB1: N/A
	RB2- RB3: 15	RB2- RB3: 15	RB2- RB3: 15
	RB5: N/A	RB5: N/A	RB5- RB7: N/A
>>transmissionWindowSiz e	RB1: N/A RB2- RB3: 128 for	RB1: N/A RB2- RB3: 128 for	RB1: N/A RB2- RB3: 128 for
	UEs with more than 10 kbyte "total RLC	UEs with more than 10 kbyte "total RLC	UEs with more than 10 kbyte "total RLC
	AM buffer size" and	AM buffer size" and	AM buffer size" and
	RB5: N/A	RB5: N/A	RB5- RB7: N/A
>>timerRST	RB1: N/A	RB1: N/A	RB1: N/A
	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
A MOY DOT	RB5: N/A	RB5: N/A	RB5- RB7: N/A
>>IIIax-R31	RB2- RB3: 1	RB2- RB3: 1	RB2- RB3: 1
	RB5: N/A	RB5: N/A	RB5- RB7: N/A
>>pollingInfo	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5- RB7: N/A
>>>lastTransmissionPDU- Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>lastRetransmissionPD U-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A	RB1- RB3: N/A	RB1- RB3: N/A
>dl-BLC-Mode	RDD. FALSE	RDD. FALSE	RDD- RD7. FALSE
	RB2- RB3: AM	RB2- RB3: AM	RB2- RB3: AM
	RB5: TM	RB5: TM	RB5- RB7: TM
>>inSequenceDelivery	RB1: N/A RB2- RB3: TRUE	RB1: N/A RB2- RB3: TRUE	RB1: N/A RB2- RB3: TRUE
	RB5: N/A	RB5: N/A	RB5- RB7: N/A
>>receivingvvindowSize	RB1: N/A RB2- RB3: 128 for	RB1: N/A RB2- RB3: 128 for	RB1: N/A RB2- RB3: 128 for
	UEs with more than	UEs with more than	UEs with more than
	10 kbyte "total RLC	10 kbyte "total RLC	10 kbyte "total RLC
	AM buffer size" and	AM buffer size" and	AM buffer size" and
	RB5: N/A	RB5: N/A	S∠ otherwise RB5- RB7: N/A
>>dl-RLC-StatusInfo	RB1: N/A	RB1: N/A	RB1: N/A
	RB2- RB3: as below RB5: N/A	RB2- RB3: as below RB5: N/A	RB2- RB3: as below RB5- RB7: N/A
>>>timerStatusProhibit	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100
>>>missingPDU-Indicator	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerStatusPeriodic	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
>>segmentationingloation	RB5: FALSE	RB5: FALSE	RB5- RB7: FALSE

Configuration	28.8 kbps streaming CS-	57.6 kbps streaming CS-	12.2 kbps speech(multimode
	3.4 kbps signalling	3.4 kbps signalling	3.4 kbps signalling
rb-MappingInfo			
>UL- LogicalChannelMappings	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel
>>ul- TransportChannelType	Dch	Dch	Dch
>>>transportChannelldenti ty	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3
>>logicalChannelldentity	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
>>rlc-SizeList	RB1- RB3: configured RB5: N/A	RB1- RB3: configured RB5: N/A	RB1- RB3: configured RB5- RB7: N/A
>>mac- LogicalChannelPriority	RB1: 1, RB2: 2, RB3: 3 RB5: 5	RB1: 1, RB2: 2, RB3: 3 RB5: 5	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: 5
>DL- logicalChannelManningList			
>>Mapping option 1	One mapping option	One mapping option	One mapping option
>>>dl- TransportChannelType	Dch	Dch	Dch
>>>>transportChannellden tity	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3
>>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
TrCH INFORMATION PER TrCH			
UL- AddReconfTransChInfoLis t			
>Uplink transport channel type	dch	dch	dch
>transportChannelIdentity	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>transportFormatSet	DedicatedTransChT FS	DedicatedTransChT FS	DedicatedTransChT FS
>>dynamicTF-information			
>>>ttu/ ttu,1	1x576, 2x576) 1x576, 2x576) TrCH2: (0x144, 1x144)	1x576, 2x576, 3x576, 4x576) TrCH2: (0x144, 1x144)	TrCH1: (0x81) TrCH2: (0x 103 TrCH3: (0x 60) TrCH4: (0x144)
>>>rlcSize	TrCH1: OctetMode TrCH2:BitMode	TrCH1: OctetMode TrCH2:BitMode	BitMode
>>>>sizeType >>>>numberOfTbSizeList	TrCH1: type 2, part1= 9, part2= 2 (576) TrCH2: type 2, part1= 2, part2= 0 (144) TrCH1: Zero, one, 2	TrCH1: type 2, part1= 9, part2= 2 (576) TrCH2: type 2, part1= 2, part2= 0 (144) TrCH1: Zero, one,	TrCH1: type 1: 81 TrCH2: type 1: 103 TrCH3: type 1: 60 TrCH4: 2: type 2, part1= 2, part2= 0 (144) TrCH1-4: Zero
	TrCH2: Zero, one	2, 3, 4 TrCH2: Zero, one	
>>>logicalChannelList	All	All	All TrCH1: (1v30)
			TrCH2: (1x53) TrCH3: (1x60) TrCH4: (1x144)

Configuration	28.8 kbps streaming CS-	57.6 kbps streaming CS-	12.2 kbps speech(multimode
	data + 3.4 kbps signalling	data + 3.4 kbps signalling	) + 3.4 kbps signalling
>>>>numberOfTransportBl ocks			TrCH1-3: One
>>>rlc-Size			TrCH1-3: BitMode
>>>>sizeType			TrCH1: 1: 39
			TrCH2: 1: 53 TrCH3: 1: 60
>>>>numberOfTbSizeList			TrCH1-3: One
>>>>logicalChannelList			TrCH1-3: all
>>>tf 2			TrCH1: (1x42)
			TrCH2: (1x63)
>>>>numberOfTransportBl			TrCH1-2: One
ocks			
>>>>rlc-Size			TrCH1: BitMode
>>>>sıze l ype			TrCH1: type 1: 42 TrCH2: type 1: 63
>>>>numberOfTbSizeList			TrCH1-2: One
>>>logicalChannelList			TrCH1: all
>>>tf 3			TrCH1: (1x55)
			TrCH2: (1x84)
			TrCH3- TrCH4: N/A
>>>numberOfTransportBl			IrCH1-2: One
>>>rlc-Size			TrCH1: BitMode
>>>>sizeType			TrCH1: type 1: 55
have a number Of The Sizel int			TrCH2: type 1: 84
>>>>IUIIIDEIOIIDSIZELISI			
			TrCH1: (1x75)
>>>ti 4			TrCH2: (1x103)
			TrCH3- TrCH4: N/A
ocks			TrCH1-2: One
>>>>rlc-Size			TrCH1: BitMode
>>>>sizeType			TrCH1: type 1: 75 TrCH2: type 1: 103
>>>>numberOfTbSizeList			TrCH1-2: One
>>>>logicalChannelList			TrCH1: all
>>>tf 5			TrCH1: (1x81)
			TrCH2- TrCH4: N/A
>>>>numberOfTransportBl ocks			IrCH1: One
>>>>rlc-Size			TrCH1: BitMode
>>>>sizeType			TrCH1: type 1: 81
>>>numberOfTbSizeList			IrCH1: One
>>>logicalChannelList			IrCH1: all
>>semiStatic I F- Information			
>>>tti	TrCH1: 40 TrCH2: 40	TrCH1: 40 TrCH2: 40	TrCH1- TrCH3: 20 TrCH4: 40
>>>channelCodingType	TrCH1: Turbo	TrCH1: Turbo	Convolutional
s s s onarnio o o ding i ype	TrCH2:	TrCH2:	Convolutional
>>>> ooding Poto			
>>>coungrate	TrCH2: Third	TrCH2: Third	Third
			TrCH3: Half
			TrCH4: Third
>>>rateMatchingAttribute	TrCH1: 155	TrCH1: 145	TrCH1: 200
	TrCH2: 160	TrCH2: 160	TrCH2: 190
			IrCH3: 235
	<u> </u>	<u> </u>	TrCH4: 160

Configuration	28.8 kbps streaming CS-	57.6 kbps streaming CS-	12.2 kbps speech(multimode
	data + 3.4 kbps signalling	data + 3.4 kbps signalling	) + 3.4 kbps signalling
>>>crc-Size	TrCH1: 16 TrCH2: 16	TrCH1: 16 TrCH2: 16	TrCH1: 12 TrCH2- TrCH3: 0 TrCH4: 16
DL- AddReconfTransChInfoLis t			
>Downlink transport channel type	dch	dch	dch
>dl- TransportChannelIdentity (should be as for UL)	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>tfs-SignallingMode	SameAsUL	SameAsUL	Independent <only on="" tf0="" trch1<br="">is different and shown below&gt;</only>
>>transportFormatSet			DedicatedTransChT FS
>>>dynamicTF-information			
>>>>tf0/ tf0,1			TrCH1: (1x0)
>>>>rlcSize			bitMode
>>>>sizeType			TrCH1: type 1: 0
>>>>numberOfTbSizeList			TrCH1: One
>>>logicalChannelList			All
>>ULTrCH-Id	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>dch-QualityTarget	-?	-2	-3
>>bler-QualityValue	TrCH1: 1x10 ⁻² TrCH2: Absent	TrCH1: 1x10 ⁻² TrCH2: Absent	TrCH1: 7x10 ° TrCH2- TrCH4: Absent
TrCH INFORMATION, COMMON			
ul-CommonTransChInfo			
>tfcs-ID (TDD only)	1	1	1
>sharedChannelIndicator (TDD only)	FALSE	FALSE	FALSE
>tfc-Subset	Absent, not required	Absent, not required	Absent, not required
>ul-TFCS	Normal TFCI	Normal TFCI	Normal TFCI
	signalling	signalling	signalling
>>explicitTFCS- ConfigurationMode	Complete	Complete	Complete
>>>ctfcSize	Ctfc4Bit	Ctfc4Bit	Ctfc8Bit
>>>>TFCS representation	Addition	Addition	Addition
>>>>TFCS list			
>>>>>TFCS 1	(TF0, TF0)	(TF0, TF0)	(TF0, TF0, TF0, TF0)
>>>>>>ctfc	0	0	0
>>>>>>sgainFactorInform ation	Computed	Computed	Computed
>>>>>>referenceTFCId	0	0	0
>>>>>TFCS 2	(TF1, TF0)	(TF1, TF0)	(TF1, TF0, TF0, TF0)
>>>>>ctfc	1	1	1
>>>>>sgainFactorInform ation	Computed	Computed	Computed
>>>>>Bc (FDD only)	N/A	N/A	N/A
	N/A	N/A	N/A
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	0	0	0
>>>>>TFCS 3	(TF2, TF0)	(TF2, TF0)	(TF2, TF1, TF0, TF0)
>>>>>ctfc	2	2	8

Configuration	28.8 kbps	57.6 kbps	12.2 kbps
	data +	data +	) +
	3.4 kbps signalling	3.4 kbps signalling	3.4 kbps signalling
>>>>>>gainFactorInform ation	Computed	Computed	Computed
>>>>>>>referenceTFCId	0	0	0
>>>>>TFCS 4	(TF0, TF1)	(TF3, TF0)	(TF3, TF2, TF0, TF0)
>>>>>ctfc	3	3	15
>>>>>sgainFactorInform ation	Computed	Computed	Computed
>>>>>>Bc (FDD only)	N/A	N/A	N/A
22222228d	N/A	N/A	N/A
>>>>>>>>>>referenceTFCId	0	0	0
>>>>>TFCS 5	(TF1, TF1)	(TF4, TF0)	(TF4, TF3, TF0, TE0)
>>>>>ctfc	4	4	22
>>>>>gainFactorInform	Computed	Computed	Computed
>>>>>>>>>referenceTFCId	0	0	0
>>>>>TFCS 6	(TF2, TF1)	(TF0, TF1)	(TF5, TF4, TF1, TF0)
>>>>>>ctfc	5	5	59
>>>>>sgainFactorInform	Signalled	Computed	Computed
ssssss Ba (EDD ank)	8	N/A	N/A
	15	N/A	N/A
>>>>>>pd	15	N/A	N/A
>>>>>>>>reference   FCId	0		
>>>>>>>>>>			(1F0,1F0,1F0,1F1)
>>>>>>CIIC		0 Computed	60 Computed
ation		Computed	Computed
>>>>>>>>reference   FCld			
>>>>>IFCS 8		(1F2, 1F1)	(1F1,1F0,1F0,1F1)
>>>>>>CtfC		/ Computed	61 Computed
ation		Computed	Computed
>>>>>>referenceTFCId		0	0
>>>>>TFCS 9		(TF3, TF1)	(TF2,TF1,TF0,TF1)
>>>>>>ctfc		8	68
>>>>>>sgainFactorInform ation		Computed	Computed
>>>>>>referenceTFCId		0	0
>>>>>TFCS 10		(TF4, TF1)	(TF3,TF2,TF0,TF1)
>>>>>>ctfc		9	75
>>>>>>gainFactorInform ation		Signalled	Computed
>>>>>>βc (FDD only)		8	N/A
>>>>>βd		15	N/A
>>>>>referenceTFCId		0	0
>>>>>TFCS 11			(TF4,TF3,TF0.TF1)
>>>>>>ctfc			82
>>>>>>gainFactorInform ation			Computed
>>>>>>referenceTFCId			0
>>>>>TFCS 12			(TF5,TF4,TF1,TF1)
>>>>>>ctfc			119
>>>>>>gainFactorInform ation			Signalled
>>>>>>>Rc (FDD only)			11
>>>>>>Rd			15
>>>>>>>>>referenceTFCId			0

Configuration	28.8 kbps streaming CS- data +	57.6 kbps streaming CS- data +	12.2 kbps speech(multimode
	3.4 kbps signalling	3.4 kbps signalling	3.4 kbps signalling
dl-CommonTransChInfo			
>tfcs-SignallingMode	Same as UL	Same as UL	Same as UL
PhyCH INFORMATION FDD			
UL-DPCH-InfoPredef			
>ul-DPCH-			
PowerControlInfo			
>>powerControlAlgorithm	Algorithm 1	Algorithm 1	Algorithm 1
>>>tpcStepSize	1 <u>dB</u>	1 <u>dB</u>	1 <u>dB</u>
>ttcl-Existence			
DL-		1	0.88
f			
>dl-DPCH-InfoCommon			
>>spreadingFactor	64	32	128
>>tfci-Existence	TRUE	TRUE	FALSE
>>pilotBits	8	8	4
>>positionFixed	Flexible	Flexible	Fixed
PhyCH INFORMATION			
PowerControlInfo			
>>dpch-ConstantValue	0	0	0
>commonTimeslotInfo			
>>secondInterleavingMod	frameRelated	frameRelated	frameRelated
s>tfci-Coding	16	16	16
>>puncturingLimit	0.44	0.48	0.88
>>repetitionPeriodAndLen	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
CommonInformationPrede			
>dl-DPCH-InfoCommon			
>>commonTimeslotInfo			
>>>secondInterleavingMo de	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	16	16	16
>>>puncturingLimit	0.44	0.48	0.92
>>>repetitionPeriodAndLe	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
ngth PhyCH INFORMATION			
1.28 Mcps TDD			
UL-DPCH-InfoPredef			
>common l imesiotinfo	from Deleter	fuere a Distant	
>>secondInterleavingMod e	frameRelated	frameRelated	
>>tfci-Coding	16	16	
>>puncturingLimit	0.64	0.72	
>>repetitionPeriodAndLen gth	repetitionPeriod1	repetitionPeriod1	
DL- CommonInformationPrede			
- >dl-DPCH-InfoCommon			
>>commonTimeslotInfo			
>>>secondInterleavingMo de	frameRelated	frameRelated	frameRelated

Configuration	28.8 kbps streaming CS- data + 3.4 kbps signalling	57.6 kbps streaming CS- data + 3.4 kbps signalling	12.2 kbps speech(multimode ) + 3.4 kbps signalling
>>>tfci-Coding	16	16	16
>>>puncturingLimit	0.64	0.72	0.92
>>>repetitionPeriodAndLe ngth	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1

Configuration	10.2/6.7/5.9/4.75 kbps speech + 3.4 kbps signalling	7.4/6.7/5.9/4.75 kbps speech + 3.4 kbps signalling
Ref 34 108	N/A	N/A
Default configuration	11	12
identity		
RB INFORMATION		
rb-Identity	RB1: 1, RB2: 2,	RB1: 1, RB2: 2,
2	RB3: 3, RB5: 5,	RB3: 3, RB5: 5,
	RB6: 6, RB7: 7,	RB6: 6, RB7: 7
	RB8: 8	
rlc-InfoChoice	Rlc-info	Rlc-info
>ul-RLC-Mode	RB1: UM	RB1: UM
	RB2- RB3: AM	RB2- RB3: AM
	RB5-RB7: IM	RB5-RB6: TM
>>transmissionRLC-	RB1: N/A	RB1: N/A
Discardividue	NoDiscard	KDZ- KDJ. NoDiscard
	RB5- RB7· N/A	RB5- RB6· N/A
>>>maxDat	RB1: N/A	RB1: N/A
	RB2- RB3: 15	RB2- RB3: 15
	RB5- RB7: N/A	RB5- RB6: N/A
>>transmissionWindowSiz	RB1: N/A	RB1: N/A
е	RB2- RB3: 128 for	RB2- RB3: 128 for
	UEs with more than	UEs with more than
	10 kbyte "total RLC	10 kbyte "total RLC
	AM buffer size" and	AM buffer size" and
	32 otherwise	32 otherwise
time a sport	RB5- RB7: N/A	RB5- RB6: N/A
>>timerRS1	RB1: N/A	RB1: N/A
	RD2- RD3. 300 RB5- RB7: N/A	RD2- RD3. 300 RB5- RB6: N/A
>>max-RST	RB1· N/A	RB1· N/A
>>max=1.01	RB2- RB3: 1	RB2- RB3: 1
	RB5- RB7: N/A	RB5- RB6: N/A
>>pollingInfo	RB1: N/A	RB1: N/A
	RB2- RB3: as below	RB2- RB3: as below
	RB5- RB7: N/A	RB5- RB6: N/A
>>>lastTransmissionPDU- Poll	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>lastRetransmissionPD U-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A	RB1- RB3: N/A
	RB5- RB7: FALSE	RB5- RB6: FALSE
>dI-RLC-Mode	KB1: UM	KB1: UM
	KB2- KB3: AM	KBZ- KB3: AM
	RBD-RB/: IM	КВЭ- КВ0: 111 РВ7: ТМ
>>inSequenceDelivory		
		RB2- RB3. TRUE
	RB5- RB8: N/A	RB5- RB7: N/A
>>receivingWindowSize	RB1: N/A	RB1: N/A
	RB2- RB3: 128 for	RB2- RB3: 128 for
	UEs with more than	UEs with more than
	10 kbyte "total RLC	10 kbyte "total RLC
	AM buffer size" and	AM buffer size" and
	32 otherwise	32 otherwise
	RB5- RB8: N/A	RB5- RB7: N/A
>>dl-RLC-StatusInfo	KB1: N/A	RB1: N/A
	KB2- KB3: as below	RB2- RB3: as below
· · · · timer Ctotus Drokikit	KB2-KB8: N/A	RB2-RB/:N/A
	RDZ- RDJ. 100	RD2- RD3: 100
	RB2- RB2- 200	RB2- RB2- 200

>>segmentationIndication

LogicalChannelMappings

TransportChannelType >>>transportChannelIdentit

>>logicalChannelIdentity

LogicalChannelPriority

>>Mapping option 1

TransportChannelType >>>>transportChannelIden

>>>logicalChannelIdentity

TrCH INFORMATION PER

AddReconfTransChInfoList >Uplink transport channel

>transportChannelIdentity

>>dynamicTF-information

>>>numberOfTbSizeList

>>>>numberOfTransportBl

>>>numberOfTbSizeList

>>>logicalChannelList

>transportFormatSet

>>>tf0/ tf0,1

>>>>rlcSize

>>>tf 1

ocks

>>>rlc-Size

>>>>sizeType

>>>>sizeType

TrCH3: 3, TrCH4: 4

DedicatedTransChT

TrCH1: (0x65)

TrCH2: (0x 99)

TrCH3: (0x 40,

TrCH4: (0x144, 1x144)

TrCH1: type 1: 65

TrCH2: type 1: 99

TrCH3: type 1: 40 TrCH4: 2: type 2,

part1= 2, part2= 0

TrCH3-4: Zero, one

TrCH3- TrCH4: N/A

TrCH1-2: BitMode

TrCH1-2: Zero

TrCH1: (1x39)

TrCH2: (1x 53)

TrCH1: One

TrCH2: One

TrCH1: 1: 39

TrCH2: 1: 53

TrCH1-2: One

FS

1x40)

BitMode

(144)

All

logicalChannelMappingList

rb-MappingInfo

>>rlc-SizeList

>>mac-

>DL-

>>>dl-

tity

TrCH UL-

type

>UL-

>>ul-

у

YY-MM)		
RB1- RB3: N/A	RB1- RB3: N/A	
RB5- RB8: FALSE	RB5- RB7: FALSE	
OneLogicalChannel	OneLogicalChannel	
Dch	Dch	
RB1- RB3: 4	RB1- RB3: 3	
RB5: 1, RB6: 2, RB7: 3,	RB5: 1, RB6: 2	
RB1: 1, RB2: 2,	RB1: 1, RB2: 2,	
RB3: 3 RB5- RB7: N/A	RB3: 3 RB5- RB6: N/A	
RB1- RB3	RB1- RB3	
configured	configured	
RB5- RB7: N/A	RB5- RB6: N/A	
RB1: 1, RB2: 2,	RB1: 1, RB2: 2,	
RB3: 3	RB3: 3	
RB5- RB7: 5	RB5- RB6: 5	
One mapping option	One mapping option	
Dch	Dch	
RB1- RB3: 4	RB1- RB3: 3	
RB5: 1, RB6: 2,	RB5: 1, RB6: 2,	
RB7: 3, RB8: 5	RB7:4	
RB1: 1, RB2: 2,	RB1: 1, RB2: 2,	
RB3: 3	RB3: 3	
RB5- RB8: N/A	RB5- RB7: N/A	
dch	dch	
TrCH1: 1, TrCH2: 2,	TrCH1: 1, TrCH2: 2,	

TrCH3: 3

FS

1x144)

BitMode

(144)

All

DedicatedTransChT

TrCH1: (0x61)

TrCH2: (0x 87)

TrCH3: (0x 144,

TrCH1: type 1: 61

TrCH2: type 1: 87 TrCH3: 2: type 2,

part1= 2, part2= 0

TrCH3: Zero, one

TrCH1-2: Zero

TrCH1: (1x39)

TrCH2: (1x53) TrCH3: N/A

TrCH1: One

TrCH2: One

TrCH1: 1: 39

TrCH1: 1: 53

TrCH1-2: One

TrCH1-2: BitMode

>>>logicalChannelList	TrCH1: all	TrCH1: all
>>>tf 2	TrCH1: (1x42)	TrCH1: (1x42)
	TrCH2: (1x63)	TrCH2: (1x63)
	TrCH3- TrCH4: N/A	TrCH3: N/A
>>>>numberOfTransportBl	TrCH1: One	TrCH1: One
ocks	TrCh2: One	TrCh2: One
>>>>rlc-Size	TrCH1: BitMode	TrCH1: BitMode
>>>>sizeType	TrCH1: type 1: 42	TrCH1: type 1: 42
51	TrCH2: type 1: 63	TrCH2: type 1: 63
>>>>numberOfTbSizeList	TrCH1: One	TrCH1: One
	TrCH2: One	TrCH2: One
>>>>logicalChannelList	TrCH1: all	TrCH1: all
	TrCH2: all	TrCH2: all
>>>tf 3	TrCH1: (1x55)	TrCH1: (1x55)
	TrCH2: (1x76)	TrCH2: (1x76)
	TrCH3- TrCH4· N/A	TrCH3: N/A
>>>>numberOfTransportBl	TrCH1: One	TrCH1: One
ocks	TrCh2: One	TrCh2: One
>>>rlc-Size	TrCH1: BitMode	TrCH1: BitMode
>>>>Size i ype	TrCH2: type 1: 55	TrCH2: type 1: 55
a a a number Of The Sizel ist		
>>>>numberOrrbSizeList	TrCH1: One	TrCH1: One
>>>>iogicalCnannelList		
>>>tf 4	TrCH1: (1x58)	TrCH1: (1x58)
	TrCH2: (1x99)	TrCH2: (1x87)
	TrCH3- TrCH4: N/A	TrCH3: N/A
>>>>numberOfTransportBl	TrCH1: One	TrCH1: One
OCKS	TrCh2: One	TrCh2: One
>>>>rlc-Size	TrCH1: BitMode	TrCH1: BitMode
>>>>sizeType	TrCH1: type 1: 58	TrCH1: type 1: 58
	TrCH2: type 1: 99	TrCH2: type 1: 87
>>>>numberOfTbSizeList	TrCH1: One	TrCH1: One
	TrCH2: One	TrCH2: One
>>>>logicalChannelList	TrCH1: all	TrCH1: all
	TrCH2: all	TrCH2: all
>>>tf 5	TrCH1: (1x65)	TrCH1: (1x61)
	TrCH2- TrCH4: N/A	TrCH2- TrCH4: N/A
>>>>numberOfTransportBl	TrCH1: One	TrCH1: One
ocks		
>>>rlc-Size	TrCH1: BitMode	TrCH1: BitMode
>>>>sizeType	TrCH1: type 1: 65	TrCH1: type 1: 61
>>>>numberOfTbSizeList	TrCH1: One	TrCH1: One
>>>logicalChannelList	TrCH1: all	TrCH1: all
>>semistaticTF-Information		
>>>tti	TrCH1- TrCH3: 20	TrCH1- TrCH2: 20
	TrCH4: 40	TrCH3: 40
>>>channelCodingType	Convolutional	Convolutional
>>>>codingRate	TrCH1- TrCH2	TrCH1- TrCH2
	Third	Third
	TrCH3· Half	TrCH3. Third
	TrCH4: Third	
>>>rateMatchingAttribute		
	TrCH2: 100	TrCH2: 100
	TrCH3: 225	TrCH3: 160
	TrCH4: 160	
>>>crc-Size		
~~~UU-UI2E		
DI		IIUTIJ. 10
UL- AddDaaanfTrana Oblafal ist		
>Downlink transport	acn	acn
channel type		
>dl-		1
· · · · · · · · · · · · · · · · · · ·		

>tfs-SignallingMode	Independent	Independent
	<only on="" td="" tf0="" trch1<=""><td><only on="" td="" tf0="" trch1<=""></only></td></only>	<only on="" td="" tf0="" trch1<=""></only>
	and tf0/tf1 on	and tf0/tf1 on
	TrCH5 are different	TrCH4 are different
	and shown below>	and shown below>
>>transportFormatSet		
>>>dynamic I F-information		
>>>>tf0/ tf0,1	IrCH1: (1x0)	IrCH1: (1x0)
	TrCH5: (0x3, 1x3)	IrCH4: (0x3, 1x3)
>>>>ricSize	BitMode	bitMode
>>>>size l ype	TrCH1: type 1: 0	TrCH1: type 1: 0
>>>>numberOf1bSizeList		TrCH1: One
>>> logicalChannell ist		
		All
>>>Semistanci F-	for TrCH5	for TrOUA
>>>>lll	Convolutional	Convolutional
>>>>channelCodingType		
>>>>couingKate		
>>>rateiviatchingAttribute	Trous: 200	TrCH4: 200
>>>>CIC-SIZe		Troll4: 12
>>UL1rCH-Id	IrCH1: 1, IrCH2: 2,	IrCH1: 1, IrCH2: 2,
	TICH3: 3, TICH4: 4,	TICH3: 3
>dcn-Quality Larget		T-014-7-10 ⁻³
>>bler-QualityValue	IrCH1: 7x10	IrCH1: 7x10
	TrCH2- TrCH5:	IrCH2- IrCH4:
	Absent	Absent
TICH INFORMATION,		
	4	4
>tfcs-ID (IDD only)	1	1
>sharedChannelIndicator	FALSE	FALSE
(TDD only)		
> tfc-Subset	Absent, not required	Absent, not required
>ul-TFCS	Normal IFCI	Normal IFCI
	signalling	signailing
>>explicit i FCS-	Complete	Complete
ConfigurationWode	C#f=0Dit	Cut-CDit
>>>cticSize		CticoBit
>>>>TFCS representation	Addition	Addition
>>>> IFC list		
>>>>>	(TF0, TF0, TF0,	(TF0, TF0, TF0)
>>>>> IFC 1	(TF0, TF0, TF0, TF0)	(TF0, TF0, TF0)
>>>>>>tfc1	(TF0, TF0, TF0, TF0) 0	(TF0, TF0, TF0)
>>>>>ctfc >>>>>>gainFactorInform	(TF0, TF0, TF0, TF0) 0 Computed	(TF0, TF0, TF0) 0 Computed
>>>>>ctfc >>>>>>gainFactorInform ation	(TF0, TF0, TF0, TF0) 0 Computed	(TF0, TF0, TF0) 0 Computed
>>>>>ctfc >>>>>>ctfc >>>>>>gainFactorInform ation >>>>>referenceTFCId	(TF0, TF0, TF0, TF0) 0 Computed 0	(TF0, TF0, TF0) 0 Computed 0
>>>>>ctfc >>>>>>gainFactorInform ation >>>>>referenceTFCId >>>>>TFC 2	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0)	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0)
>>>>>ctfc >>>>>>gainFactorInform ation >>>>>referenceTFCId >>>>>TFC 2	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0)	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0)
>>>>>tFC 1 >>>>>>tfC 1 >>>>>>gainFactorInform ation >>>>>referenceTFCId >>>>>TFC 2 >>>>>ctfc	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 2	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0) 1
>>>>>>tFC 1 >>>>>>gainFactorInform ation >>>>>>referenceTFCId >>>>>TFC 2 >>>>>ctfc >>>>>ctfc	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0) 1 Computed
>>>>>>trC 1 >>>>>>ctfc >>>>>>gainFactorInform ation >>>>>>referenceTFCId >>>>>TFC 2 >>>>>>ctfc >>>>>>ctfc >>>>>>ctfc	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0) 1 Computed
>>>>>>trC 1 >>>>>>gainFactorInform ation >>>>>>referenceTFCId >>>>>TFC 2 >>>>>>tfc >>>>>ctfc >>>>>>ctfc >>>>>>gainFactorInform ation >>>>>βc (FDD only)	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed N/A	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0) 1 Computed N/A
>>>>>tFC 1 >>>>>>gainFactorInform ation >>>>>TFC 2 >>>>>ctfc >>>>>>tfc >>>>>ctfc >>>>>>tfc >>>>>bgainFactorInform ation >>>>>βc (FDD only) >>>>>βd	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed N/A N/A	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0) 1 Computed N/A N/A
>>>>>trC 1 >>>>>>gainFactorInform ation >>>>>TFC 2 >>>>>ctfc >>>>>tfc >>>>>tfc >>>>>tfc >>>>>bfc (FDD only) >>>>>bfd >>>>>referenceTFCId	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed N/A N/A 0	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0) 1 Computed N/A N/A 0
>>>>>tFC 1 >>>>>sgainFactorInform ation >>>>>TFC 2 >>>>>tfc >>>>>tfc >>>>>tfc >>>>>tfc >>>>>tfc >>>>>tfc >>>>>bfc (FDD only) >>>>>bfd >>>>>tffC 3	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0,	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0)
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0, TF0, TF0)	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0)
>>>>> TFC T >>>>>> gainFactorInform ation >>>>> TFC 2 >>>>>> Ctfc >>>>>> ctfc >>>>>> gainFactorInform ation >>>>>> βd >>>>> TFC 3 >>>>> ctfc	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0, TF0) 8	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 8
>>>>>tFC 1 >>>>>>gainFactorInform ation >>>>>TFC 2 >>>>>Ctfc >>>>>>tfc >>>>>>tfc >>>>>ctfc >>>>>gainFactorInform ation >>>>> βd >>>>>teferenceTFCId >>>>> βd >>>>>tffC 3 >>>>>ctfc >>>>>>tfc	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0, TF0) 8 Computed	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 8 Computed
>>>>>strC 1 >>>>>sqainFactorInform ation >>>>>TFC 2 >>>>>stfc >>>>>sqainFactorInform ation >>>>>>tfc >>>>>sqainFactorInform ation >>>>>βd >>>>>stfc 3 >>>>>stfc >>>>>stfc >>>>>sqainFactorInform ation	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0, TF0) 8 Computed	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 8 Computed
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0, TF0) 8 Computed 0	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 8 Computed 0
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0, TF0) 8 Computed 0 (TF3, TF2, TF0,	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 8 Computed 0 (TF3, TF2, TF0)
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	(TF0, TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0, TF0) 8 Computed 0 (TF3, TF2, TF0, TF0)	(TF0, TF0, TF0) 0 Computed 0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 8 Computed 0 (TF3, TF2, TF0)
>>>>>>gainFactorInform	Computed	Computed
--	---------------------------	--------------------
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	0	0
>>>>>TFC 5	(TE4 TE3 TE0	(TF4_TE3_TE0)
	TF0)	(11 1, 11 0, 11 0)
>>>>>ctfc	22	22
>>>>>>gainFactorInform	Computed	Computed
ation		
>>>>>>>>reference I FCId		
>>>>>	(1F5, 1F4, 1F1, TF0)	(15, 154, 150)
>>>>>ctfc	59	29
>>>>>>>sgainFactorInform	Computed	Computed
ation		
>>>>>βc (FDD only)		
_>>>>>>βd		
>>>>>>>referenceTFCId		
>>>>>IFC /	(TF0, TF0, TF0, TF1)	(1F0, 1F0, 1F1)
>>>>>ctfc	60	30
>>>>>>sgainFactorInform	Computed	Computed
ation		
>>>>>>referenceTFCId		
>>>>>IFC 8	(1F1, 1F0, 1F0, 1F0, TE1)	(1F1, 1F0, 1F1)
>>>>>>ctfc	61	31
>>>>>>>>>>	computed	computed
ation		
>>>>>>βc (FDD only)		
>>>>>βd		
>>>>>>referenceTFCId	0	0
>>>>>TFC 9	(TF2, TF1, TF0, TF1)	(TF2, TF1, TF1)
>>>>>>ctfc	68	38
>>>>>>gainFactorInform	computed	computed
ation		
>>>>>>referenceTFCId	0	0
>>>>>TFC 10	(TF3, TF2, TF0, TF1)	(TF3, TF2, TF1)
>>>>>>ctfc	75	45
>>>>>>gainFactorInform	computed	computed
ation		
>>>>>>βc (FDD only)		
>>>>>βd		
>>>>>>>referenceTFCId	0	0
>>>>>TFC 11	(TF4, TF3, TF0, TF1)	(TF4, TF3, TF1)
>>>>>ctfc	82	52
>>>>>>gainFactorInform	computed	computed
ation		
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>		
>>>>>>>>>>>	TF1)	(153, 154, 151)
>>>>>ctfc	97	59
>>>>>>gainFactorInform ation	signalled	signalled
>>>>>Bc (FDD only)	11	11
>>>>>Bd	15	15
>>>>>>referenceTFCId	0	0
> TFC subset list		
>>TFC subset 1	(speech rate 10.2)	(speech rate 7.4)

>>> Allowed transport	(TFC1, TFC2,	(TFC1, TFC2,
format combination list	TFC7, TFC8, TFC6,	TFC7, TFC8, TFC6,
	TFC12)	TFC12)
>>TFC subset 2	(speech rate 6.7)	(speech rate 6.7)
>>> Allowed transport	(TEC1_TEC2	(TEC1_TEC2
format combination list	TEC7 TEC8 TEC5	TEC7 TEC8 TEC5
Torriat combination list	TEC(11)	TEC11)
TEC subset 2	(apacch rate 5.0)	(apacch rate 5.0)
>>TFC Subset 3	(speech late 5.9)	(speech late 5.9)
>>> Allowed transport		
format combination list	TFC7, TFC8, TFC4,	IFC7, IFC8, IFC4,
	IFC10)	TFC10)
>>TFC subset 4	(speech rate 4.75)	(speech rate 4.75)
>>> Allowed transport	(TFC1, TFC2,	(TFC1, TFC2,
format combination list	TFC7, TFC8, TFC3,	TFC7, TFC8, TFC3,
	TFC9)	TFC9)
dl-CommonTransChInfo		
>tfcs-SignallingMode	Independent	Independent
>tfcs-ID (TDD only)	1	1
	FALSE	FALSE
> ttc-Subset	Absent, not required	Absent, not required
>dl-TFCS	Normal TFCI	Normal TFCI
	signalling	signalling
>>explicitTFCS-	Complete	Complete
ConfigurationMode		
>>>ctfcSize	Ctfc8Bit	Ctfc8Bit
>>>>TECS representation	Addition	Addition
	Addition	Addition
	(TE0 TE0 TE0	(TE0 TE0 TE0
>>>>>IFC 1	(1F0, 1F0, 1F0,	(1F0, 1F0, 1F0,
	1F0, 1F0)	1F0)
>>>>>>ctfc	0	0
>>>>>TFC 2	(TF1, TF0, TF0,	(TF1, TF0, TF0,
	TF0, TF0)	TF0)
>>>>>ctfc	1	1
>>>>>TFC 3	(TF2, TF1, TF0,	(TF2, TF1, TF0,
	TEO TEO	(11 <u>1</u> , 111, 110, TEO)
>>>>>>	8	8
<i>>>>>></i> 1FC 4	(1F3, 1F2, 1F0, TE0, TE0)	(1F3, 1F2, 1F0, TF0)
		1F0)
>>>>>>ctfc	15	15
>>>>>TFC 5	(TF4, TF3, TF0,	(TF4, TF3, TF0,
	TF0, TF0)	TF0)
>>>>>>ctfc	22	22
>>>>>TFC 6	(TF5, TF4, TF1,	(TF5, TF4, TF0,
	TFO. TFO)	TF0)
>>>>>ctfc	59	29
		(TE0_TE0_TE1
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	(1F0, 1F0, 1F0, 1F0, 1F0, 1F0, 1F0, 1F0,	(1FO, 1FO, 1FT, TEO)
		1F0)
>>>>>Ctic		30
>>>>>IFC 8	(IF1, IF0, IF0,	(IF1, IF0, IF1,
	IF1, IF0)	IF0)
>>>>>ctfc	61	31
>>>>>TFC 9	(TF2, TF1, TF0,	(TF2, TF1, TF1,
	TF1, TF0)	TF0)
>>>>>ctfc	68	37
>>>>>TFC 10	(TE3 TE2 TE0	(TE3 TE2 TE1
	TF1 TF0	(110, 112, 111, TF0)
>>>>>>	75	55
>>>>>IFC 11	(1+4, 1+3, 1+0, 1+0, 1+0)	(1F4, 1F3, 1F1, TE0)
	IF1, IF0)	IF0)
>>>>>ctfc	82	52
>>>>>TFC 12	(TF5, TF4, TF1,	(TF5, TF4, TF1,
	TF1, TF0)	TF0)
>>>>>ctfc	119	59

		1
>>>>>TFC 13	(TF0, TF0, TF0, TE0, TE1)	(TF0, TF0, TF0,
· · · · · · · offo	120	60
>>>>>IFC 14	(1F1, 1F0, 1F0, 1F0, TF0, TF0, TF0, TF0, TF0, TF0, TF0, T	(1F1, 1F0, 1F0, 1F0, TF4)
	1F0, 1F1)	1F1)
>>>>>>CtfC	121	
>>>>>IFC 15	(IF2, IF1, IF0,	(IF2, IF1, IF0,
-	IF0, IF1)	IF1)
>>>>>>ctfc	128	68
>>>>>TFC 16	(TF3, TF2, TF0,	(TF3, TF2, TF0,
	TF0, TF1)	TF1)
>>>>>>ctfc	135	75
>>>>>TFC 17	(TF4, TF3, TF0,	(TF4, TF3, TF0,
	TF0, TF1)	TF1)
>>>>>ctfc	152	82
>>>>>TFC 18	(TF5, TF4, TF1,	(TF5, TF4, TF0,
	TF0, TF1)	TF1)
>>>>>>ctfc	189	89
>>>>>TFC 19	(TF0, TF0, TF0,	(TF0, TF0, TF1,
	TF1, TF1)	TF1)
>>>>>ctfc	180	90
>>>>>TFC 20	(TE1, TE0, TE0	(TE1, TE0, TE1
	TF1 TF1)	TF1)
>>>>>>tfc	181	91
>>>>>TEC 21	(TE2 TE1 TE0	
>>>>>TFG 21	(IFZ, IFI, IFU, TE1 TE1)	(1F2, 1F1, 1F1, 1F1, TE1)
· · · · · · · · otto	100	
	100	
>>>>>IFC 22	(1F3, 1F2, 1F0, TF4, TF4)	(1F3, 1F2, 1F1, TF4)
	1F1, 1F1)	1F1)
>>>>>>Ctfc	195	105
>>>>>TFC 23	(TF4, TF3, TF0,	(TF4, TF3, TF1,
-	IF1, IF1)	IF1)
>>>>>>ctfc	239	112
>>>>>TFC 24	(TF5, TF4, TF1,	(TF5, TF4, TF1,
	TF1, TF1)	TF1)
>>>>>>ctfc	218	119
PhyCH INFORMATION		
FDD		
UL-DPCH-InfoPredef		
>ul-DPCH-		
PowerControlInfo		
>>powerControlAlgorithm	Algorithm 1	Algorithm 1
>>>tpcStepSize	1 dB	1 dB
>tfci-Existence	TRUE	TRUE
>puncturingl imit	0.88	0.88
CommonInformationPredef		
		<u> </u>
	129	128
	TALOE	TALOE
>>pilotBits	4	4
>>positionFixed	Fixed	Fixed
PhyCH INFORMATION		
3.84 Mcps TDD		
UL-DPCH-InfoPredef		
>ul-DPCH-		
PowerControlInfo		
>>dpch-ConstantValue	0	0
>commonTimeslotInfo		
	1	

frameRelated

repetitionPeriod1

16

0.60

>>secondInterleavingMode

>>repetitionPeriodAndLeng

>>tfci-Coding

th

>>puncturingLimit

repetitionPeriod1

frameRelated

16

0.60

DL-		
CommonInformationPredef		
>dI-DPCH-InfoCommon		
>>commonTimeslotInfo		
>>>secondInterleavingMod	frameRelated	frameRelated
е		
>>>tfci-Coding	16	16
>>>puncturingLimit	0.60	0.60
>>>repetitionPeriodAndLe	repetitionPeriod1	repetitionPeriod1
ngth		
PhyCH INFORMATION		
1.28 Mcps TDD		
UL-DPCH-InfoPredef		
>commonTimeslotInfo		
>>secondInterleavingMode	frame Related	frame Related
>>tfci-Coding	16	16
>>puncturingLimit	0.64	0.64
>>repetitionPeriodAndLeng	repetitionPeriod1	repetitionPeriod1
th		
DL-		
CommonInformationPredef		
>dl-DPCH-InfoCommon		
>>commonTimeslotInfo		
>>>secondInterleavingMod	frame Related	frame Related
е		
>>>tfci-Coding	16	16
>>>puncturingLimit	0.64	0.64
>>>repetitionPeriodAndLe	repetitionPeriod1	repetitionPeriod1
ngth		

Configuration	12.65/8.85/6.6 kbps
	3.4 kbps signalling
	••••••••••••••••••••••••••••••••••••••
Ref 34.108	62
Default configuration	13
identity	
rb-identity	RB1: 1, RB2: 2, DB2: 2, DB5: 5
	RB6: 6 RB8: 8
rlc-InfoChoice	RIC-info
>ul-RLC-Mode	RB1: UM
	RB2- RB3: AM
	RB5-RB6: TM
>>transmissionRLC-	RB1: N/A
DiscardMode	RB2- RB3:
	RB5- RB6: N/A
>>>maxDat	RB1· N/A
	RB2- RB3: 15
	RB5- RB6: N/A
>>transmissionWindowSiz	RB1: N/A
е	RB2- RB3: 128 for
	UEs with more than
	10 kbyte "total RLC
	32 otherwise
	RB5- RB6: N/A
>>timerRST	RB1: N/A
	RB2- RB3: 300
	RB5- RB6: N/A
>>max-RST	RB1: N/A
	RB2- RB3: 1
a a llia a la fa	RB5- RB6: N/A
>>pollinginto	RB1: N/A RB2_RB2: as bolow
	RB5- RB6: N/A
>>>lastTransmissionPDU-	RB2- RB3: FALSE
Poll	
>>>lastRetransmissionPD U-Poll	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A
d PLC Mada	RB5- RB6: FALSE
>di-RLC-iviode	RDT. UNI RR2- RR3' AM
	RB5- RB6: TM
	RB8: TM
>>inSequenceDelivery	RB1: N/A
	RB2- RB3: TRUE
	RB5- RB6: N/A
	RB8: N/A
>>receivingWindowSize	RB1: N/A PB2_PB2: 129 for
	LIFs with more than
	10 kbvte "total RLC
	AM buffer size" and
	32 otherwise
	RB5- RB6: N/A
	RB8: N/A
>>dl-RLC-StatusInfo	RB1: N/A
	RB2- RB3: as below
	κβς- κβς: Ν/Α RB8· Ν/Δ
>>>timerStatusProhibit	RB2- RB3- 100
>>>missingPDU-Indicator	RB2- RB3: FALSE

>>>timerStatusPeriodic	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A
	RB5- RB6: FALSE
	RB8: FALSE
rb-MappingInfo	
>UL-	OneLogicalChannel
LogicalChannelMappings	
>>ul-	Dch
I ransportChannel I ype	
>>>transportChannelIdentit	RB1- RB3: 4
y	RB5: 1, RB6: 2.
>>logicalChannelidentity	RB1: 1, RB2: 2,
	RDJ. J RB5- RB6· N/A
>>rlc_SizeList	RB1- RB3
	configured
	RB5- RB6: N/A
>>mac-	RB1: 1, RB2: 2.
LogicalChannelPriority	RB3: 3
5	RB5- RB6: 5
>DL-	
logicalChannelMappingList	
>>Mapping option 1	One mapping option
>>>dl-	Dch
TransportChannelType	
>>>>transportChannellden	RB1- RB3: 4
tity	RB5: 1, RB6: 2,
	RB8: 5
>>>logicalChannelIdentity	RB1: 1, RB2: 2,
	RB3: 3
	RB5- RB6: N/A
	RB8: N/A
OL- AddReconfTransChinfol ist	
>I Inlink transport channel	dch
	uch
>transportChannelldentity	TrCH1: 1. TrCH2: 2.
	TrCH4: 4
>transportFormatSet	DedicatedTransChT
	FS
>>dynamicTF-information	
>>>tf0/ tf0,1	TrCH1: (0x72)
	TrCH2: (0x 181)
	TrCH4: (0x144,
	1x144)
>>>rlcSize	BitMode
>>>>sizeType	TrCH1: type 1: 72
	TrCH2: type 1: 181
	TrCH4: 2: type 2,
	part1 = 2, part2 = 0
	(144)
>>>>numberOfIbSizeList	TrCH1-2: Zero
	TrCH4: Zero, one
>>>iogicalChannelList	
>>>tf 1	TrCH1: (1x40)
	$\Pi \cup \Pi \angle .$ (1X / δ) Tr $\cap \square A : \mathbb{N} / A$
>>>pumbarOfTransartD	ПСП4. IV/А
	TrCH1: One
0010	TrCH1: One
>>>rlc-Size	TrCH1: One TrCH2: One TrCH1-2: BitModo
>>>>rlc-Size	TrCH1: One TrCH2: One TrCH1-2: BitMode
>>>>rlc-Size >>>>sizeType	TrCH1: One TrCH2: One TrCH1-2: BitMode TrCH1: 1: 40 TrCH2: 1: 78
>>>>rlc-Size >>>>sizeType	TrCH1: One TrCH2: One TrCH1-2: BitMode TrCH1: 1: 40 TrCH2: 1: 78 TrCH1-2: One
>>>>rlc-Size >>>>sizeType >>>>numberOfTbSizeList	TrCH1: One TrCH2: One TrCH1-2: BitMode TrCH1: 1: 40 TrCH2: 1: 78 TrCH1-2: One TrCH1: all

>>>tf 2	TrCH1: (1x54)
	TrCH2: (1x113)
	TrCH4: N/A
>>>>numberOfTransportBl	TrCH1: One
ocks	TrCh2: One
>>>rlc-Size	TrCH1: BitMode
>>>>sizeType	TrCH1: type 1: 54
	TrCH2: type 1: 113
>>>>numberOfThSizeList	TrCH1: One
	TrCH2: One
· · · · logicalChannell ist	
>>>>iogicalChanneiList	
>>>tt 3	TrCH1: (1x64)
	TrCH2: (1x181)
	TrCH4: N/A
>>>>numberOfTransportBl	TrCH1: One
ocks	TrCh2: One
>>>rlc-Size	TrCH1: BitMode
>>>>sizeType	TrCH1: type 1: 64
21	TrCH2: type 1: 181
>>>>numberOfTbSizeList	TrCH1: One
	TrCH2: One
	The first second secon
>>>ti 4	TICHI: $(1X/2)$
	TrCH2: N/A
	TrCH4: N/A
>>>>numberOfTransportBl	TrCH1: One
ocks	
>>>>rlc-Size	TrCH1: BitMode
>>>>sizeType	TrCH1: type 1: 72
>>>>numberOfTbSizeList	TrCH1: One
	TOUL
>>>>logical(Channell ist	IrCH1 all
>>>logicalChannelList	TrCH1: all
>>>logicalChannelList >>semistaticTF-Information	
>>>logicalChannelList >>semistaticTF-Information >>>tti	TrCH1: all TrCH1- TrCH2: 20
>>>logicalChannelList >>semistaticTF-Information >>>tti	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional
<pre>>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate</pre>	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2:
<pre>>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate</pre>	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH1: 200
<pre>>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute</pre>	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH1: 200 TrCH2: 190
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH1: 200 TrCH2: 190 TrCH4: 170
<pre>>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>crc-Size</pre>	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH1: 200 TrCH2: 190 TrCH4: 170 TrCH1: 12
<pre>>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>crc-Size</pre>	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH1: 200 TrCH2: 190 TrCH4: 170 TrCH1: 12 TrCH1: 12 TrCH2: 0
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>crc-Size	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH1: 200 TrCH2: 190 TrCH4: 170 TrCH1: 12 TrCH2: 0 TrCH4: 16
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>crc-Size DL-	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH1: 200 TrCH2: 190 TrCH4: 170 TrCH1: 12 TrCH2: 0 TrCH4: 16
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>crc-Size DL- AddReconfTransChInfoList	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH1: 200 TrCH2: 190 TrCH4: 170 TrCH1: 12 TrCH2: 0 TrCH4: 16
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>crc-Size DL- AddReconfTransChInfoList >Downlink transport	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH1: 200 TrCH2: 190 TrCH4: 170 TrCH1: 12 TrCH2: 0 TrCH4: 16 dch
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>rateMatchingAttribute DL- AddReconfTransChInfoList >Downlink transport channel type	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH1: 200 TrCH2: 190 TrCH4: 170 TrCH1: 12 TrCH2: 0 TrCH4: 16 dch
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>rateMatchingAttribute >>>crc-Size DL- AddReconfTransChInfoList >Downlink transport channel type >dL-	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH1: 200 TrCH2: 190 TrCH4: 170 TrCH1: 12 TrCH2: 0 TrCH4: 16 dch
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>rateMatchingAttribute >>>crc-Size DL- AddReconfTransChInfoList >Downlink transport channel type >dl- TransportChannelIdentity	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH2: 190 TrCH4: 170 TrCH4: 12 TrCH2: 0 TrCH4: 16 dch
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>rateMatchingAttribute >>>crc-Size DL- AddReconfTransChInfoList >Downlink transport channel type >dl- TransportChannelIdentity >tfs_SignallingMode	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH1: 200 TrCH2: 190 TrCH4: 170 TrCH1: 12 TrCH2: 0 TrCH4: 16 dch
<pre>>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>rateMatchingAttribute DL- AddReconfTransChInfoList >Downlink transport channel type >dl- TransportChannelIdentity >tfs-SignallingMode</pre>	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH1: 200 TrCH2: 190 TrCH2: 190 TrCH4: 170 TrCH4: 170 TrCH4: 16 dch
<pre>>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>rateMatchingAttribute DL- AddReconfTransChInfoList >Downlink transport channel type >dl- TransportChannelIdentity >tfs-SignallingMode</pre>	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH2: 190 TrCH2: 190 TrCH4: 170 TrCH4: 170 TrCH4: 16 dch Independent <only on="" tf0="" trch1<br="">and tf0/tf1 on</only>
<pre>>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>crc-Size DL- AddReconfTransChInfoList >Downlink transport channel type >dl- TransportChannelIdentity >tfs-SignallingMode</pre>	IrcH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH2: 190 TrCH4: 170 TrCH4: 170 TrCH4: 16 dch Independent <only on="" td="" tf0="" trch1<=""> and tf0/tf1 on TrCH5 care different</only>
<pre>>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>crc-Size DL- AddReconfTransChInfoList >Downlink transport channel type >dl- TransportChannelIdentity >tfs-SignallingMode</pre>	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH2: 190 TrCH2: 190 TrCH4: 170 TrCH4: 170 TrCH4: 16 dch Independent <only on="" tf0="" trch1<br="">and tf0/tf1 on TrCH5 are different and of our below</only>
>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>rateMatchingAttribute >>>crc-Size DL- AddReconfTransChInfoList >Downlink transport channel type >dl- TransportChannelIdentity >tfs-SignallingMode	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH2: 190 TrCH2: 190 TrCH4: 170 TrCH1: 12 TrCH2: 0 TrCH4: 16 dch Independent <only on="" tf0="" trch1<br="">and tf0/tf1 on TrCH5 are different and shown below></only>
<pre>>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>rateMatchingAttribute DL- AddReconfTransChInfoList >Downlink transport channel type >dl- TransportChannelIdentity >tfs-SignallingMode >>transportFormatSet</pre>	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH2: 190 TrCH2: 190 TrCH4: 170 TrCH1: 12 TrCH2: 0 TrCH4: 16 dch Independent <only on="" tf0="" trch1<br="">and tf0/tf1 on TrCH5 are different and shown below></only>
<pre>>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>rateMatchingAttribute DL- AddReconfTransChInfoList >Downlink transport channel type >dl- TransportChannelIdentity >tfs-SignallingMode >>transportFormatSet >>>dynamicTF-information</pre>	IrcH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH2: 190 TrCH4: 170 TrCH2: 0 TrCH4: 16 dch Independent <only on="" td="" tf0="" trch1<=""> and tf0/tf1 on TrCH5 are different and shown below></only>
<pre>>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>rateMatchingAttribute DL- AddReconfTransChInfoList >Downlink transport channel type >dl- TransportChannelIdentity >tfs-SignallingMode >>>tfs-SignallingMode >>>tfo/ tf0,1</pre>	TrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: Third TrCH2: 190 TrCH2: 190 TrCH4: 170 TrCH2: 0 TrCH2: 0 TrCH4: 16 dch Independent <only on="" tf0="" trch1<br="">and tf0/tf1 on TrCH5 are different and shown below> TrCH1: (1x0)</only>
<pre>>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>rateMatchingAttribute DL- AddReconfTransChInfoList >Downlink transport channel type >dl- TransportChannelIdentity >tfs-SignallingMode >>>tfs-SignallingMode </pre>	IrcH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: 170 TrCH2: 190 TrCH2: 0 TrCH4: 16 dch Independent <only on="" td="" tf0="" trch1<=""> and tf0/tf1 on TrCH5 are different and shown below> TrCH1: (1x0) TrCH5: (0x3, 1x3)</only>
<pre>>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>crc-Size DL- AddReconfTransChInfoList >Downlink transport channel type >dl- TransportChannelIdentity >tfs-SignallingMode >>>tfs-SignallingMode >>>tfo/ tf0,1 >>>>tf0/ tf0,1 >>>>tfo/ tf0,1</pre>	IrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH2: 190 TrCH2: 190 TrCH4: 170 TrCH4: 16 dch Independent <only on="" td="" tf0="" trch1<=""> and tf0/tf1 on TrCH5 are different and shown below> TrCH1: (1x0) TrCH5: (0x3, 1x3) BitMode</only>
<pre>>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>crc-Size DL- AddReconfTransChInfoList >Downlink transport channel type >dl- TransportChannelIdentity >tfs-SignallingMode >>>tfs-SignallingMode >>>tfo/ tf0,1 >>>>tf0/ tf0,1 >>>>tfo/ tf0,1</pre>	IrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH2: 190 TrCH2: 190 TrCH4: 170 TrCH4: 16 dch Independent <only on="" td="" tf0="" trch1<=""> and tf0/tf1 on TrCH5 are different and shown below> </only>
<pre>>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>crc-Size DL- AddReconfTransChInfoList >Downlink transport channel type >dl- TransportChannelIdentity >tfs-SignallingMode >>>tfs-SignallingMode >>>tfo/ tf0,1 >>>>tf0/ tf0,1 >>>>tfo/ tf0,1</pre>	IrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: 170 TrCH2: 190 TrCH4: 170 TrCH4: 16 dch Independent <only on="" td="" tf0="" trch1<=""> and tf0/tf1 on TrCH5 are different and shown below> TrCH1: (1x0) TrCH5: (0x3, 1x3) BitMode TrCH1: type 1: 0 TrCH5: type 1: 3</only>
<pre>>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>crc-Size DL- AddReconfTransChInfoList >Downlink transport channel type >dl- TransportChannelIdentity >tfs-SignallingMode >>tfs-SignallingMode >>>tf0/ tf0,1 >>>>tf0/ tf0,1 >>>>tf0/ tf0,1</pre>	IrCH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: 170 TrCH2: 190 TrCH4: 170 TrCH4: 16 dch Independent <only on="" td="" tf0="" trch1<=""> and tf0/tf1 on TrCH5 are different and shown below> TrCH1: (1x0) TrCH5: (0x3, 1x3) BitMode TrCH1: type 1: 0 TrCH5: type 1: 3 TrCH1: One</only>
<pre>>>>logicalChannelList >>semistaticTF-Information >>>tti >>>channelCodingType >>>codingRate >>>rateMatchingAttribute >>>crc-Size DL- AddReconfTransChInfoList >Downlink transport channel type >dl- TransportChannelIdentity >tfs-SignallingMode >>>tfs-SignallingMode >>>tfo/ tf0,1 >>>>tf0/ tf0,1 >>>>tf0/ tf0,1 >>>>tf0/ tf0,1</pre>	IrcH1: all TrCH1- TrCH2: 20 TrCH4: 40 Convolutional TrCH1- TrCH2: Third TrCH4: Third TrCH4: 170 TrCH2: 190 TrCH4: 170 TrCH4: 16 dch Independent <only on="" td="" tf0="" trch1<=""> and tf0/tf1 on TrCH5 are different and shown below> TrCH1: (1x0) TrCH5: (0x3, 1x3) BitMode TrCH1: type 1: 0 TrCH5: type 1: 3 TrCH1: One TrCH5: Zero one</only>

>>>>logicalChannelList	All
>>>semistaticTF-	same as UL except
Information	for TrCH5
>>>>tti	TrCH5: 20
>>>>channelCodingType	Convolutional
>>>>codingRate	TrCH5: Third
>>>rateMatchingAttribute	TrCH5: 205
>>>>crc-Size	TrCH5: 8
>>ULTrCH-Id	TrCH1: 1, TrCH2: 2,
	TrCH4: 4,
>dch-QualityTarget	
>>bler-QualityValue	TrCH1: 7x10 ⁻³
, , ,	TrCH2: Absent
	TrCH4- TrCH5:
	Absent
TrCH INFORMATION,	
COMMON	
ul-CommonTransChInfo	
>tfcs-ID (TDD only)	1
>sharedChannelIndicator	FALSE
(TDD only)	-
> tfc-Subset	Absent. not required
>ul-TFCS	Normal TFCI
	signalling
>>explicitTFCS-	Complete
ConfigurationMode	
>>>ctfcSize	Ctfc6Bit
>>>>TECS representation	Addition
>>>>TEC liet	Addition
	0 Computed
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	Computed
11/1/11	
	0
>>>>>referenceTFCId	0
>>>>>referenceTFCId >>>>>TFC 2	0 (TF1, TF0, TF0)
>>>>>referenceTFCId >>>>>TFC 2 >>>>>ctfc	0 (TF1, TF0, TF0) 1
>>>>>referenceTFCld >>>>>TFC 2 >>>>>ctfc >>>>>>gainFactorInform	0 (TF1, TF0, TF0) 1 Computed
>>>>>referenceTFCId >>>>>TFC 2 >>>>>ctfc >>>>>>gainFactorInform ation	0 (TF1, TF0, TF0) 1 Computed
>>>>>referenceTFCId >>>>>TFC 2 >>>>>ctfc >>>>>gainFactorInform ation >>>>>βc (FDD only)	0 (TF1, TF0, TF0) 1 Computed N/A
>>>>>referenceTFCId >>>>>TFC 2 >>>>>ctfc >>>>>gainFactorInform ation >>>>>βc (FDD only) >>>>>>βd	0 (TF1, TF0, TF0) 1 Computed N/A N/A
>>>>>referenceTFCId >>>>>TFC 2 >>>>>ctfc >>>>>gainFactorInform ation >>>>>βc (FDD only) >>>>>βd >>>>>referenceTFCId	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0
>>>>>referenceTFCId >>>>>TFC 2 >>>>>ctfc >>>>>gainFactorInform ation >>>>>βc (FDD only) >>>>>βd >>>>>referenceTFCId >>>>>TFC 3	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0)
$\begin{array}{l} \\ >>>>>referenceTFCId \\ \\ >>>>>tFC 2 \\ \\ >>>>>ctfc \\ \\ >>>>>gainFactorInform \\ ation \\ \\ \\ >>>>>\betac (FDD only) \\ \\ \\ >>>>>\betad \\ \\ \\ \\ >>>>>sreferenceTFCId \\ \\ \\ \\ >>>>>tFC 3 \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ $	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7
$\begin{array}{l} \\ >>>>>referenceTFCId \\ \\ >>>>>tFC 2 \\ \\ >>>>>ctfc \\ \\ >>>>>gainFactorInform \\ ation \\ \\ \\ >>>>>\betac (FDD only) \\ \\ \\ \\ >>>>>\betad \\ \\ \\ \\ >>>>>sreferenceTFCId \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed
$\begin{array}{l} \\ >>>>>referenceTFCld \\ \\ >>>>>tfC 2 \\ \\ >>>>>ctfc \\ \\ \\ >>>>>gainFactorInform \\ ation \\ \\ \\ >>>>>\betac (FDD only) \\ \\ \\ \\ >>>>>\betad \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed
$\begin{array}{l} \\ >>>>>referenceTFCld \\ \\ >>>>>tfC 2 \\ \\ >>>>>ctfc \\ \\ >>>>>gainFactorInform \\ ation \\ \\ \\ >>>>>\betac (FDD only) \\ \\ \\ >>>>>\betad \\ \\ \\ \\ >>>>>sreferenceTFCld \\ \\ \\ \\ >>>>>tfC 3 \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ $	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0
$\begin{array}{l} \\ >>>>>referenceTFCld \\ \\ >>>>>tFC 2 \\ \\ >>>>>stfc \\ \\ \\ >>>>>gainFactorInform \\ ation \\ \\ \\ >>>>>\betac (FDD only) \\ \\ \\ \\ >>>>>\betad \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0)
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed
attor >>>>>referenceTFCld >>>>>tFC 2 >>>>>sctfc >>>>>sgainFactorInform ation >>>>> βc (FDD only) >>>>> βd >>>>> βd >>>>>sreferenceTFCld >>>>>sctfc >>>>>sctfc >>>>>sctfc >>>>>sreferenceTFCld >>>>>sreferenceTFCld >>>>>tfc 4 >>>>>tFC 4	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed
ation >>>>>referenceTFCld >>>>>tFC 2 >>>>>sctfc >>>>>sgainFactorInform ation >>>>> βc (FDD only) >>>>> βd >>>>> βd >>>>> βd >>>>>ffC 3 >>>>>tfc >>>>>ctfc >>>>>sctfc	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed
attor >>>>>referenceTFCld >>>>>ctfc >>>>>>gainFactorInform ation >>>>> βc (FDD only) >>>>> βd >>>>> βd >>>>>ffC 3 >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>ctfc >>>>>>ctfc >>>>>>ctfc >>>>>>ctfc	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0 (TF4, TF3, TF0)
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0 (TF4, TF3, TF0) 19
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0 (TF4, TF3, TF0) 19 Computed
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0 (TF4, TF3, TF0) 19 Computed
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0 (TF4, TF3, TF0) 19 Computed 0
$\begin{tabular}{l}{llllllllllllllllllllllllllllllll$	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0 (TF4, TF3, TF0) 19 Computed 0 (TF0, TF0, TF1)
ation >>>>>referenceTFCld >>>>>sctfc >>>>>> $\betac (FDD only)$ >>>>> βd >>>>>ffC 3 >>>>>tfc 3 >>>>>tfc 3 >>>>>tfc 3 >>>>>tfc 3 >>>>>tfc 3 >>>>>tfc 4 >>>>>tfc 4 >>>>>tfc 4 >>>>>tfc 4 >>>>>tfc 4 >>>>>tfc 5 >>>>> βd >>>>>tfc 5 >>>>> βd	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0 (TF4, TF3, TF0) 19 Computed 0 (TF0, TF0, TF1) 20
autori >>>>>TFC 2 >>>>>sctfc >>>>>> $\betac (FDD only)$ >>>>> βd >>>>> $\betac (FDD only)$ >>>>> βd >>>>>TFC 3 >>>>>tfc 3 >>>>>tfc >>>>>tfc >>>>>tfc >>>>>sreferenceTFCld >>>>>sreferenceTFCld >>>>>tfc >>>>>gainFactorInform ation >>>>>tfc >>>>>ffc >>>>>gainFactorInform ation >>>>>ffc >>>>>ffc >>>>>gainFactorInform ation >>>>>ffc >>>>>ffc >>>>>ffc >>>>>sreferenceTFCld >>>>>ffc >>>>>ffc >>>>>ffc >>>>>sreferenceTFCld >>>>>ffc >>>>>ffc >>>>>ffc >>>>>sreferenceTFCld >>>>>ffc >>>>>ffc >>>>>ffc >>>>>ffc >>>>>ffc >>>>>ffc >>>>>ffc >>>>>ffc >>>>>tfc >>>>>ffc >>>>>>ffc >>>>>>tfc >>>>>tfc >>>>>tfc >>>>>tfc >>>>>tfc >>>>>tfc >>>>>>tfc >>>>>tfc >>>>>>tfc >>>>>>tfc >>>>>>tfc >>>>>>tfc >>>>>>>>>>tfc >>>>>>tfc >>>>>>>>>>>>tfc >>>>>>>>>>tfc >>>>>>>>>>>>>>>>>tfc >>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0 (TF4, TF3, TF0) 19 Computed 0 (TF0, TF0, TF1) 20 Computed
ation >>>>>TFC 2 >>>>>sctfc >>>>>> $\betac (FDD only)$ >>>>> βd >>>>> $\betac (FDD only)$ >>>>> βd >>>>>TFC 3 >>>>>tfc 3 >>>>>tfc >>>>>tfc >>>>>tfc >>>>>gainFactorInform ation >>>>>tfc >>>>>tfc >>>>>gainFactorInform ation >>>>>bfc (FDD only) >>>>>ffc 4 >>>>>tfc >>>>>bfc (FDD only) >>>>>bfd >>>>>bfd >>>>>ffc 4 >>>>>bfc (FDD only) >>>>>bfd >>>>>bfc (FDD only) >>>>>bfd >>>>>bfc (FDD only) >>>>>bfd >>>>>bfc (FDD only) >>>>>bfd >>>>>bfc (FDD only) >>>>>bfd >>>>>bfc (FDD only) >>>>>>>>bfd >>>>>bfc (FDD only) >>>>>bfd >>>>>bfc (FDD only) >>>>>bfd >>>>>bfc (FDD only) >>>>>bfc (FDD only) >>>>>>>>>>>bfc (FDD only) >>>>>bfc (FDD only) >>>>>bfc (FDD only) >>>>>bfc (FDD only) >>>>>>>>>>>>>>>>bfc (FDD only) >>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0 (TF4, TF3, TF0) 19 Computed 0 (TF0, TF0, TF1) 20 Computed
autori >>>>>TFC 2 >>>>>sqainFactorInform ation >>>>> βc (FDD only) >>>>> βd >>>>> βd >>>>>TFC 3 >>>>>tfc 3 >>>>>tfc >>>>>tfc >>>>>tfc >>>>>gainFactorInform ation >>>>>tfc >>>>>tfc >>>>>gainFactorInform ation >>>>>bf (FDD only) >>>>> βd >>>>>tfc >>>>>bf (FDD only) >>>>> βd >>>>>tfc >>>>>bf (FDD only) >>>>> βd >>>>>bf (FDD only) >>>>> βd >>>>>tfc >>>>>bf (FDD only) >>>>>bf (FDD only) >>>>>bf (FDD only) >>>>>>>>bf (FDD only) >>>>>>bf (FDD only) >>>>>bf (FDD only) >>>>>>bf (FDD only) >>>>>tfc 6 >>>>>tfc 6 >>>>>tfc 6 >>>>>>tfc (FDD only)	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0 (TF4, TF3, TF0) 19 Computed 0 (TF0, TF0, TF1) 20 Computed
$\begin{tabular}{l}{l} \\ \hline \end{tabular} \\ \hline \end{tildersized} \\ $	0 (TF1, TF0, TF0) 1 Computed N/A N/A 0 (TF2, TF1, TF0) 7 Computed 0 (TF3, TF2, TF0) 13 Computed 0 (TF4, TF3, TF0) 19 Computed 0 (TF0, TF0, TF1) 20 Computed

>>>>>>referenceTFCId	0
>>>>>TFC 7	(TF1, TF0, TF1)
>>>>>>ctfc	21
>>>>>>gainFactorInform	Computed
ation	
>>>>>>referenceTFCId	0
>>>>>TFC 8	(TF2, TF1, TF1)
>>>>>ctfc	27
>>>>>>gainFactorInform	computed
ation	
>>>>>>Bc (FDD only)	
>>>>>>Bd	
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	0
>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	(15, 152, 151)
>>>>>>>CIIC	33
>>>>>>>gainFactorInform	computed
ation	
>>>>>>>>reterence I FCId	
>>>>> IFC 10	(1F4, 1F3, 1F1)
>>>>>>ctfc	39
>>>>>>>gainFactorInform	signalled
ation	
>>>>>>βc (FDD only)	11
>>>>>>Bd	15
>>>>>>>>>	0
> TFC subset list	
> TEC subset 1	(chooch rate 6.6)
>>TFC Subset T	
>>> Allowed transport	(1FO1, 1FO2, TECS, TEC
Ionnal combination list	TEC9
A TEC autoat 2	(appageb rate 9.95)
>>TFC Subset 2	
>>> Allowed transport	
format combination list	1FC3, 1FC4, 1FC6,
TEO I IO	1FC7, 1FC8, 1FC9)
>>TFC subset 3	(speech rate 12.65)
	(TEO4 TEO0
>>> Allowed transport	(TFC1, TFC2,
>>> Allowed transport format combination list	(TFC1, TFC2, TFC3, TFC4, TFC5,
>>> Allowed transport format combination list	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8,
>>> Allowed transport format combination list	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10)
>>> Allowed transport format combination list dl-CommonTransChInfo	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10)
 >>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode 	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10)
 >>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo 	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only)	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only)	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>explicitTFCS-	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>explicitTFCS- ConfigurationMode	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) >tfc-Subset >dl-TFCS configurationMode >>ctfcSize	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) >tfc-Subset >dl-TFCS ConfigurationMode >>>ctfcSize >>>TFCS representation	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>explicitTFCS- ConfigurationMode >>>ctfcSize >>>TFCS representation >>>>TFCS list	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>explicitTFCS- ConfigurationMode >>>ctfcSize >>>TFCS representation >>>>TFCS list	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition
<pre>>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>explicitTFCS- ConfigurationMode >>>ctfcSize >>>TFCS representation >>>>TFCS list >>>>TFC 1</pre>	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition (TF0, TF0, TF0, TE0)
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>explicitTFCS- ConfigurationMode >>>tfcS ist >>>TFCS representation >>>>TFCS list >>>>TFC 1	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition (TF0, TF0, TF0, TF0, TF0) 0
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>explicitTFCS- ConfigurationMode >>>>TFCS representation >>>>TFCS list >>>>>TFC 1 >>>>>>tfc 2	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition (TF0, TF0, TF0, TF0) 0
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>explicitTFCS- ConfigurationMode >>>ctfcSize >>>>TFCS representation >>>>TFCS list >>>>>TFC 1 >>>>>TFC 1	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition (TF0, TF0, TF0, TF0) 0 (TF1, TF0, TF0, TF0, TF0, TF0, TF0, TF0,
<pre>>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>explicitTFCS- ConfigurationMode >>>ctfcSize >>>>TFCS representation >>>>TFCS list >>>>>TFC 1 >>>>>TFC 1 </pre>	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition (TF0, TF0, TF0, TF0) 0 (TF1, TF0, TF0, TF0, TF0,
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>explicitTFCS- ConfigurationMode >>>>TFCS representation >>>>TFCS list >>>>>TFC 1 >>>>>TFC 2 >>>>>ctfc	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition (TF0, TF0, TF0, TF0) 0 (TF1, TF0, TF0, TF0, 1
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>explicitTFCS- ConfigurationMode >>>>TFCS representation >>>>TFCS list >>>>>TFC 1 >>>>>TFC 2 >>>>>>TFC 3	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition (TF0, TF0, TF0, TF0) 0 (TF1, TF0, TF0, TF0, 1 (TF2, TF1, TF0, TF0, TF0, 1
>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) >tfc-Subset >dl-TFCS >>explicitTFCS- ConfigurationMode >>>>TFCS representation >>>>TFCS list >>>>>TFC 1 >>>>>>TFC 2 >>>>>>TFC 3	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition (TF0, TF0, TF0, TF0) 0 (TF1, TF0, TF0, TF0, 1 (TF2, TF1, TF0, TF0)
<pre>>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>>>tfc-Subset >dl-TFCS >>>>TFCS representation >>>>TFCS list >>>>>TFC 1 >>>>>TFC 2 >>>>>TFC 3 >>>>>tfc 3 >>>>>tfc 3</pre>	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition (TF0, TF0, TF0, TF0) 0 (TF1, TF0, TF0, TF0, 1 (TF2, TF1, TF0, TF0) 7
<pre>>>> Allowed transport format combination list dl-CommonTransChInfo >tfcs-SignallingMode ul-CommonTransChInfo >tfcs-ID (TDD only) >sharedChannelIndicator (TDD only) > tfc-Subset >dl-TFCS >>>>tfc-Subset >dl-TFCS >>>>TFCS representation >>>>TFCS list >>>>>TFC 1 >>>>>TFC 1 >>>>>TFC 2 >>>>>TFC 3 >>>>>TFC 3 </pre>	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10) Independent 1 FALSE Absent, not required Normal TFCI signalling Complete Ctfc8Bit Addition (TF0, TF0, TF0, TF0) 0 (TF1, TF0, TF0, TF0, 1 (TF2, TF1, TF0, TF0, 7 (TF3, TF2, TF0,

	13
>>>>>TFC 5	(TF4, TF3, TF0,
	TF0)
>>>>>ctfc	19
>>>>TFC 6	(TF0, TF0, TF1,
	TF0)
>>>>>ctfc	20
>>>>>TFC 7	(TF1, TF0, TF1,
	TF0)
>>>>>ctfc	21
>>>>>TFC 8	(TF2, TF1, TF1,
	TF0)
>>>>>ctfc	27
>>>>>TFC 9	(TF3, TF2, TF1,
	TF0)
>>>>>ctfc	33
>>>>>TFC 10	(TF4, TF3, TF1,
	TF0)
>>>>>ctfc	39
>>>>>TFC 11	(TF0, TF0, TF0,
	(TF1)
>>>>>ctfc	40
>>>>>TFC 12	(TE1, TE0, TE0
	TF1)
>>>>>ctfc	41
>>>>>TFC 13	(TF2 TF1 TF0
	(112, 111, 110, TF1)
	/7
	(TE3 TE2 TE0
	(11 3, 11 2, 11 0, TF1)
	53
	(TE4 TE3 TE0
	(11 4, 11 3, 11 0, TE1)
	59
	(TEO TEO TE1
	(110, 110, 111, TF1)
	60
	(TE1_TE0_TE1
SSSSSTEC 17	(11 1, 11 0, 11 1,
>>>>>TFC 17	TF1)
>>>>>TFC 17	TF1) 61
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>>>>>TFC 17 >>>>>>tfc 17 >>>>>TFC 18 >>>>>tfc 18 >>>>>tfc 19 >>>>>tfc 19	TF1) 61 (TF2, TF1, TF1, TF1) 67 (TF3, TF2, TF1, TF1) 73 (TF4, TF3, TF1)
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>>>>>TFC 17 >>>>>TFC 17 >>>>>TFC 18 >>>>>Ctfc >>>>>TFC 19 >>>>>Ctfc >>>>>TFC 20	TF1) 61 (TF2, TF1, TF1, TF1) 67 (TF3, TF2, TF1, TF1) 73 (TF4, TF3, TF1, TF1) 79
>>>>>TFC 17 >>>>>Ctfc >>>>>Ctfc >>>>>Ctfc >>>>>Ctfc >>>>>TFC 19 >>>>>Ctfc >>>>>TFC 20 PhyCH INEOPMATION	TF1) 61 (TF2, TF1, TF1, TF1) 67 (TF3, TF2, TF1, TF1) 73 (TF4, TF3, TF1, TF1) 79
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>>>>>TFC 17 >>>>>TFC 18 >>>>>tfc 18 >>>>>tfc 19 >>>>>tfc 20 >>>>>tfc PhyCH INFORMATION FDD UL-DPCH-InfoPredef	TF1) 61 (TF2, TF1, TF1, TF1, TF1) 67 (TF3, TF2, TF1, TF1) 73 (TF4, TF3, TF1, TF1) 79
>>>>>TFC 17 >>>>>TFC 17 >>>>>TFC 18 >>>>>TFC 18 >>>>>TFC 19 >>>>>TFC 20 >>>>>tfc PhyCH INFORMATION FDD UL-DPCH-InfoPredef >ul-DPCH- BowerControllate	TF1) 61 (TF2, TF1, TF1, TF1, TF1) 67 (TF3, TF2, TF1, TF1) 73 (TF4, TF3, TF1, TF1) 79
>>>>>TFC 17 >>>>>>TFC 17 >>>>>TFC 18 >>>>>TFC 18 >>>>>TFC 19 >>>>>TFC 20 >>>>>tfc PhyCH INFORMATION FDD UL-DPCH-InfoPredef >ul-DPCH- PowerControlInfo	TF1) 61 (TF2, TF1, TF1, TF1, TF1) 67 (TF3, TF2, TF1, TF1) 73 (TF4, TF3, TF1, TF1) 79
>>>>>TFC 17 >>>>>TFC 18 >>>>>TFC 18 >>>>>TFC 19 >>>>>TFC 20 >>>>>TFC 20 >>>>>TFC 19 UL-DPCH-InfoPredef >ul-DPCH- PowerControlAlgorithm >>powerControlAlgorithm	TF1) 61 (TF2, TF1, TF1, TF1) 67 (TF3, TF2, TF1, TF1) 73 (TF4, TF3, TF1, TF1) 79 Algorithm 1 1 <dp< td=""></dp<>
>>>>>TFC 17 >>>>>TFC 18 >>>>>TFC 18 >>>>>TFC 19 >>>>>TFC 20 >>>>>TFC 20 >>>>>TFC 19 UL-DPCH-InfoPredef >ul-DPCH- PowerControlAlgorithm >>>tpcStepSize >//foi Fuidtages	TF1) 61 (TF2, TF1, TF1, TF1) 67 (TF3, TF2, TF1, TF1) 73 (TF4, TF3, TF1, TF1) 79 Algorithm 1 1_dB TDUE
>>>>>TFC 17 >>>>>TFC 18 >>>>>TFC 18 >>>>>TFC 19 >>>>>TFC 20 >>>>>TFC 20 >>>>>TFC 19 UL-DPCH-InfoPredef >ul-DPCH- PowerControlAlgorithm >>>tpcStepSize >tfci-Existence	TF1) 61 (TF2, TF1, TF1, TF1) 67 (TF3, TF2, TF1, TF1) 73 (TF4, TF3, TF1, TF1) 79 Algorithm 1 1_dB TRUE 0.94
>>>>>TFC 17 >>>>>TFC 18 >>>>>TFC 18 >>>>>TFC 19 >>>>>TFC 20 >>>>>TFC 20 >>>>>TFC 19 UL-DPCH-InfoPredef >ul-DPCH- PowerControlInfo >>powerControlAlgorithm >>>tpcStepSize >tfci-Existence >puncturingLimit	TF1) 61 (TF2, TF1, TF1, TF1) 67 (TF3, TF2, TF1, TF1) 73 (TF4, TF3, TF1, TF1) 79 Algorithm 1 1 1 0.84
>>>>>TFC 17 >>>>>TFC 18 >>>>>TFC 18 >>>>>TFC 19 >>>>>TFC 20 >>>>>TFC 20 >>>>>TFC 20 >>>>>TFC 19 UL-DPCH-InfoPredef >ul-DPCH- PowerControlAlgorithm >>>tpcStepSize >tfci-Existence >puncturingLimit DL-	TF1) 61 (TF2, TF1, TF1, TF1) 67 (TF3, TF2, TF1, TF1) 73 (TF4, TF3, TF1, TF1) 79 Algorithm 1 1_dB TRUE 0.84
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>>>>>TFC 17 >>>>>>TFC 18 >>>>>TFC 18 >>>>>TFC 19 >>>>>tfc 19 >>>>>tfc 20 >>>>>tfc 20 >>>>>tfc PhyCH INFORMATION FDD UL-DPCH-InfoPredef >ul-DPCH- PowerControlAlgorithm >>powerControlAlgorithm >>tpcStepSize >tfci-Existence >puncturingLimit DL- CommonInformationPredef >dl-DPCH-InfoCommon	TF1) 61 (TF2, TF1, TF1, TF1) 67 (TF3, TF2, TF1, TF1) 73 (TF4, TF3, TF1, TF1) 79 Algorithm 1 1_dB TRUE 0.84
>>>>>TFC 17 >>>>>>TFC 18 >>>>>TFC 18 >>>>>TFC 19 >>>>>tfc 19 >>>>>tfc 20 >>>>>tfc 20 >>>>>tfc PhyCH INFORMATION FDD UL-DPCH-InfoPredef >ul-DPCH- PowerControlAlgorithm >>powerControlAlgorithm >>>tpcStepSize >tfci-Existence >puncturingLimit DL- CommonInformationPredef >dl-DPCH-InfoCommon >>spreadingFactor	TF1) 61 (TF2, TF1, TF1, TF1) 67 (TF3, TF2, TF1, TF1) 73 (TF4, TF3, TF1, TF1) 79 Algorithm 1 1_dB TRUE 0.84
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		B (addition	of feature),				R97	(Release	1997)	
		C (function	al modification of	feature)			R98	(Release	1998)	
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							Rel-7	(Release	7)	

Reason for change: # At RAN2 #43 a number of changes relating to HS-DSCH reception were agreed. With respect to the agreed CR one case related to disabling HS-DSCH reception was not included, some aspects related to reception of the of DL HS-PDSCH Info IE should not have been removed, and changes were not complete or correct for TDD.

1. For FDD the serving HS-DSCH RL indicator is now used for enabling and disabling reception. When the serving RL indicator is set to true a new H-RNTI must be signaled otherwise the current H-RNTI is deleted and HS-DSCH reception is disabled. HS-DSCH reception is also disabled when the serving HS-DSCH RL indicator is set to false (see referenced section 8.6.6.4).

In TDD the serving HS-DSCH RL indicator does not exist, but similar actions are necessary for the case of hard handover. It is therefore proposed that upon hard handover when a new target cell is identified by P-CCPCH info the H-RNTI is treated similarly to existing C-RNTI logic and HS-DSCH reception requirements are enforced.

2. Since it is not possible to add/remove many of the required parameters for HS-DSCH reception the description of HS-DSCH reception requirements (8.5.25) includes text describing the methods that may be used for enabling and disabling

Summary of change: ೫	When TDD P-CCPCH info indicates a new target cell (hard handover) and a new H-RNTI is not signaled, the current H-RNTI is deleted and HS-DSCH reception criteria is checked.
	Note that corrections 3 and 4 are common for FDD and TDD operation, and corrections 1, 2 and 5 only effect TDD operation.
	Since the IE HARQ info also applied to TDD, it is proposed to move the text to the common FDD&TDD section.
	5. In the new section describing HS-DSCH reception requirements (8.5.25), text for storing of the IE HARQ info was placed in the FDD only section.
	It is proposed the section on reception of Downlink HS-PDSCH Information is returned to the specification, and actions to be taken for TDD timeslot and midamble configuration IE's, as well as references to sections describing actions to be taken upon reception of other included IE's are maintained. None of the previous text regarding enabling or disabling HS-DSCH reception is needed since this is now covered in the new section on actions related to HS_DSCH Reception (8.5.25).
	4. Also in the agreed CR on HS-DSCH reception the procedural text for IE Downlink HS-PDSCH Information reception (8.6.6.32) was removed. Our understanding for the motivation was mainly changing and clarifying the rules for enabling and disabling HS-DSCH reception (no longer based on the existence of this IE), and providing an improved maintenance of HSDPA parameters. But this section also provided other actions upon reception of the DL HS-PDSCH Info IE that need to be specified.
	It is proposed that HS-DSCH reception is checked and actions taken upon reception of "deleted DL TrCH information" and "Added or Reconfigured DL TrCH information".
	3. The "adding/removing the concerned MAC-d flows" method for enabling and disabling of HS_DSCH reception described in section 8.5.25 requires that when "deleted DL TrCH information" or "Added or Reconfigured DL TrCH information" is received and MAC-d flows are added or removed HS-DSCH reception actions are taken.
	To provide an efficient enable/disable signaling method for TDD, and cover the potentially common case upon entry to a cell that does not support R5 HSDPA, it is proposed to include removal/addition of the H-RNTI upon hard handover to the methods described in section 8.5.25. This is quite similar to the FDD method, removal/addition of the RL with serving HS-DSCH RL indicator.
	HS-DSCH reception. There are 3 methods defined: add/remove RB mapping, add/remove MAC-d flows and for FDD only add/remove the serving HS-DSCH RL. Unfortunately for TDD with the recent change in HS-DSCH reception criteria the most efficient signaling method for disabling and enabling HS-DSCH reception by setting the serving HS_DSCH RL indicator is not available. Before the agreed change a relatively efficient method existed for enabling/disabling HS-DSCH reception by the existence or not of the HS-PDSCH info IE, but with the agreed change this method is no longer available.

		The section on actions to be taken upon reception of Downlink HS-PDSCH
		Information (8.6.32) is returned to the specification. TDD parameters for timeslot and midamble configuration if included are stored and criteria if HS-DSCH reception is evaluated. Also references to sections describing actions to be taken upon reception of HS-SCCH Info and Measurement Feedback Info IE's are returned to the specification. Other previous text for HS-DSCH reception criteria and enabling/disabling is not included since these aspects are not specified in the new HS-DSCH reception section (8.5.25).
		In the procedural text for deleted and added or reconfigured TrCH info, HS-DSCH reception is checked in case the MAC-d flows mapped to the HS-DSCH are added or removed.
		HS-DSCH reception criteria specifies the IE HARQ info is also required for TDD operation.
Consequences if not approved:	ж	Improper management of the TDD H-RNTI
		Unnecessary increased signaling requirement for enabling and disabling HS- DSCH reception in TDD
		Storing of TDD timeslot and midamble configurations will not be specified and the HS-DSCH receprion check specified in section (8.5.25) will fail.
		HS-DSCH reception check needed upon added or removed MAC-d flows may not be evaulated.
		Use of HARQ info will not be correctly specified for TDD
L		
Clauses affected:	£	8223 8525 86559 8656 8658 86632

Clauses affected:	ж 8.2.2.3, 8.5.25, 8.6.5.5a, 8.6.5.6, 8.6.5.8 & 8.6.6.32
Other specs Affected:	Y N % Other core specifications % Test specifications % O&M Specifications %
Other comments:	ж

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- 1) Fill out the above form. The symbols above marked ℜ contain pop-up help information about the field that they are closest to.
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- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

8.2.2.3 Reception of RADIO BEARER SETUP or RADIO BEARER RECONFIGURATION or RADIO BEARER RELEASE or TRANSPORT CHANNEL RECONFIGURATION or PHYSICAL CHANNEL RECONFIGURATION message by the UE

The UE shall:

- 1> be able to receive any of the following messages:
 - 2> RADIO BEARER SETUP message; or
 - 2> RADIO BEARER RECONFIGURATION message; or
 - 2> RADIO BEARER RELEASE message; or
 - 2> TRANSPORT CHANNEL RECONFIGURATION message; or
 - 2> PHYSICAL CHANNEL RECONFIGURATION message;
- 1> be able to perform a hard handover and apply physical layer synchronisation procedure A as specified in [29], even if no prior UE measurements have been performed on the target cell and/or frequency.

In case the reconfiguration procedure is used to remove all existing RL(s) in the active set while new RL(s) are established the UE shall:

- 1> if the UE has a pending "TGPS reconfiguration CFN" at the activation time received in the reconfiguration message and the reconfiguration requests a timing re-initialised hard handover (see subclause 8.3.5.1), the UE may:
 - 2> abort the pending CM activation;
 - 2> set the CM_PATTERN_ACTIVATION_ABORTED to TRUE.
- 1> otherwise:

2> set the CM_PATTERN_ACTIVATION_ABORTED to FALSE.

If the UE receives:

- a RADIO BEARER SETUP message; or
- a RADIO BEARER RECONFIGURATION message; or
- a RADIO BEARER RELEASE message; or
- a TRANSPORT CHANNEL RECONFIGURATION message; or
- a PHYSICAL CHANNEL RECONFIGURATION message:

it shall:

- 1> set the variable ORDERED_RECONFIGURATION to TRUE;
- 1> if the UE will enter the CELL_DCH state from any state other than CELL_DCH state at the conclusion of this procedure:

- 2> perform the physical layer synchronisation procedure A as specified in [29] (FDD only).
- 1> act upon all received information elements as specified in subclause 8.6, unless specified in the following and perform the actions below.

The UE may:

1> maintain a list of the set of cells to which the UE has Radio Links if the IE "Cell ID" is present.

The UE may first release the physical channel configuration used at reception of the reconfiguration message. The UE shall then:

- 1> in FDD, if the IE "PDSCH code mapping" is included but the IE "PDSCH with SHO DCH Info" is not included and if the DCH has only one link in its active set:
 - 2> act upon the IE "PDSCH code mapping" as specified in subclause 8.6; and
 - 2> infer that the PDSCH will be transmitted from the cell from which the downlink DPCH is transmitted.
- 1> enter a state according to subclause 8.6.3.3.

In case the UE receives a RADIO BEARER RECONFIGURATION message including the IE "RB information to reconfigure" that only includes the IE "RB identity", the UE shall:

- 1> handle the message as if IE "RB information to reconfigure" was absent.
- NOTE: The RADIO BEARER RECONFIGURATION message always includes the IE "RB information to reconfigure". UTRAN has to include it even if it does not require the reconfiguration of any RB.

If after state transition the UE enters CELL_DCH state, the UE shall, after the state transition:

- 1> in FDD; or
- 1> in TDD when "Primary CCPCH Info" is included indicating a new target cell and "New C-RNTI" is not specified:
 - 2> remove any C-RNTI from MAC;
 - 2> clear the variable C_RNTI.

If after state transition the UE leaves CELL_DCH state, the UE shall, after the state transition:

- 1> clear any stored IE "Downlink HS-PDSCH information";
- 1> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25.

In FDD, if after state transition the UE leaves CELL_DCH state, the UE shall, after the state transition:

- 1> remove any DSCH-RNTI from MAC;
- 1> clear the variable DSCH_RNTI.

If the UE was in CELL_DCH state upon reception of the reconfiguration message and remains in CELL_DCH state, the UE shall:

1> if the IE "Uplink DPCH Info" is absent, not change its current UL Physical channel configuration;

1> in TDD:

2> if "Primary CCPCH Info" is included indicating a new target cell and "New C-RNTI" is not specified:

3> remove any C-RNTI from MAC;

3> clear the variable C_RNTI.

2> if "Primary CCPCH Info" is included indicating a new target cell and "New H-RNTI" is not specified:

3> remove any H-RNTI from MAC;

3> clear the variable H RNTI;

3> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25.

1> if "DPCH frame offset" is included for one or more RLs in the active set:

- 2> use its value to determine the beginning of the DPCH frame in accordance with the following:
 - 3> if the received IE "DPCH frame offset" is across the value range border compared to the DPCH frame offset currently used by the UE:
 - 4> consider it to be a request to adjust the timing with 256 chips across the frame border (e.g. if the UE receives value 0 while the value currently used is 38144 consider this as a request to adjust the timing with +256 chips).
 - 3> if after taking into account value range borders, the received IE "DPCH frame offset" corresponds to a request to adjust the timing with a step exceeding 256 chips:

4> set the variable INVALID_CONFIGURATION to TRUE.

3> and the procedure ends.

2> adjust the radio link timing accordingly.

- - - - Remaining text in this section is unchanged and omitted - - - -

8.5.25 Actions related to HS_DSCH_RECEPTION variable

The variable HS_DSCH_RECEPTION shall be set to "TRUE" only when all the following conditions are met:

1> the UE is in CELL_DCH state;

1> the variable H_RNTI is set;

1> the UE has a stored IE "HS-SCCH info";

1> the UE has a stored IE "HARQ info";

1> for FDD:

2> one of the radio links in the active set is configured as the serving HS-DSCH radio link;

2> the UE has stored the following IEs:

- IE "Measurement Feedback Info";
- IE "Uplink DPCH Power Control Info" including stored _{ACK, NACK} and Ack-NACK Repetition factor;

-IE "HARQ info";

1>for 3.84 Mcps TDD, the UE has stored the following IE:

- IE "HS-PDSCH Timeslot Configuration";

1> for 1.28 Mcps TDD, the UE has stored the following IE:

- IE "HS-PDSCH Midamble Configuration";

1> there is at least one RB mapped to HS-DSCH;

- 1> at least for one of the RB's mapped to HS-DSCH, there is at least one MAC-hs queue (including the IE "MAC-d PDU size Info") configured for the concerning MAC-d flow;
- Note: To enable or disable HS-DSCH reception, the UTRAN has the possibility to add/remove the concerning HS-DSCH related RB mapping options, add/remove the concerning MAC-d flows or, for FDD, add/remove the serving HS-DSCH radio link<u>or</u>, for TDD add/remove H-RNTI upon hard handover.
- If any of the above conditions is not met and the variable HS_DSCH_RECEPTION is set to TRUE, the UE shall:
 - 1> set the variable HS_DSCH_RECEPTION to FALSE;
 - 1> stop any HS_SCCH reception procedures;
 - 1> stop any HS-DSCH reception procedures;
 - 1> clear the variable H_RNTI and remove any stored H-RNTI;
 - 1> act as if the IE "MAC-hs reset indicator" is received and set to TRUE;
 - 1> release all HARQ resources;
 - 1> no long consider any radio link to be the HS-DSCH serving radio link.
- NOTE: If configured for HS-DSCH and not explicitly indicated as being cleared, the UE will have still stored the IEs "HARQ info", "Added or Reconfigured MAC-d flow", "RB mapping Info" and "Downlink HS-PDSCH information".

Whenever the variable HS_DSCH_RECEPTION is set to TRUE, the UE shall:

1> perform HS_SCCH reception procedures according to the stored HS-SCCH configuration as stated in:

2> subclause 8.6.6.33 for the IE "HS-SCCH Info".

- 1> perform HS-DSCH reception procedures according to the stored HS-PDSCH configuration as stated in:
 - 2> subclause 8.6.3.1b for the IE "H-RNTI";
 - 2> subclause 8.6.5.6b for the IE "HARQ info";
 - 2> subclause 8.6.6.34 for the IE "Measurement Feedback Info".

Whenever the variable HS_DSCH_RECEPTION is set to FALSE, the UE shall:

1> not perform HS_SCCH reception procedures;

1> not perform HS-DSCH reception procedures;

8.6.5.5a Added or reconfigured MAC-d flow

If the IE "Added or reconfigured MAC-d flow" is included, the UE shall:

1> if a MAC-hs queue (identified by the IE "MAC-hs queue Id") is included in both the IE "MAC-hs queue to add or reconfigure list" and the IE "MAC-hs queue to delete list":

2> set the variable INVALID_CONFIGURATION to TRUE.

- 1> for each MAC-hs queue included in the IE "MAC-hs queue to add or reconfigure" list:
 - 2> if the UE has previously stored a mapping between this MAC-hs queue and a MAC-d flow:

3> delete the old mapping.

- 2> map the MAC-d flow indicated in the current message to this MAC-hs queue;
- 2> set the release timer for each of the MAC-hs queues in the MAC-hs entity to the value in the corresponding IE "T1";
- 2> set the MAC-hs receiver window size for each of the MAC-hs queues in the MAC-hs entity to the value in the corresponding IE "MAC-hs window size"; and
- 2> configure MAC-hs with the mapping between MAC-d PDU sizes index and allowed MAC-d PDU sizes as follows:
 - 3> if a MAC-d PDU size has been stored for a MAC-d PDU size index for the corresponding MAC-hs queue and no mapping is provided in the current message for this MAC-d PDU index:
 - 4> continue to use this mapping.
 - 3> if a MAC-d PDU size has been stored for a MAC-d PDU size index for the corresponding MAC-hs queue and a mapping is provided in the current message for this MAC-d PDU index:
 - 4> configure the MAC-hs entity with the mapping indicated in the current message.
- 1> for each MAC-hs queue included in the IE "MAC-hs queue to delete" list:

2> delete any information about the MAC-hs queue identified by the IE "MAC-hs queue Id".

1> if the IE "Added or reconfigured MAC-d flow" is considered valid according to the rules above:

2> perform the actions as specified in subclause 8.5.21.

1> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25.

8.6.5.8 Deleted DL TrCH information

If the IE "Deleted DL TrCH information" is included the UE shall: 1> if a Downlink transport channel is requested to be deleted:

2> delete any information about the transport channel identified by the IE "DL TrCH identity".

1> if a DL MAC-d flow is requested to be deleted:

2> delete any information about the DL HS-DSCH MAC-d flow identified by the IE "MAC-d Flow Identity", i.e. delete any information about MAC-hs queue(s) mapped onto this MAC-d flow.

2> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25

8.6.5.6 Added or Reconfigured DL TrCH information

If the IE "Added or Reconfigured DL TrCH information" is included then for the transport channel identified by the IE "DL Transport Channel Identity" the UE shall:

1> if the choice "DL parameters" is set to 'explicit':

2> perform the actions for the IE "Transport Format Set" as specified in subclause 8.6.5.1.

- 1> if the choice "DL parameters" is set to 'same as uplink':
 - 2> if the IE "UL Transport Channel Identity" indicates an existing or a new UL Transport Channel:
 - 3> store as transport format for this transport channel the transport format associated with the transport channel identified by the IE "UL Transport Channel Identity".
 - 2> else:
 - 3> set the variable INVALID_CONFIGURATION to TRUE.
- 1> if the choice "DL parameters" is set to 'HSDSCH':
 - 2> if the IE "HARQ Info" is included:

3> perform the actions specified in subclause 8.6.5.6b.

2> if the IE "Added or Reconfigured MAC-d Flow" is included:

<u>3> perform the actions as specified in subclause 8.6.5.5a</u>

<u>3> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25.</u>

- 1> if the IE "DCH quality target" is included:
 - 2> perform the actions specified in subclause 8.6.5.4.
- NOTE: The UE stores the DL transport channel configuration until it is explicitly deleted by a message containing the IE "Deleted DL TrCH information" or the UE leaves RRC connected mode.

8.6.6.32 Downlink HS-PDSCH Information

If the IE "Downlink HS-PDSCH Information" is included and the UE would be in CELL_DCH state after completion of this procedure, the UE shall:

1> if the IE "HS-SCCH Info" is included:

2> act as specified in subclause 8.6.6.33.

1> if the IE "Measurement Feedback Info" is included:

2> act as specified in subclause 8.6.6.34.

1> if the IE "HS-DSCH Timeslot Configuration" or "HS-PDSCH Midamble Configuration" is included:

2> store the received configuration.

2> determine the value for the HS_DSCH_RECEPTION variable and take actions as described in subclause 8.5.25.

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Reason for change: # At RAN2 #43 a number of changes relating to HS-DSCH reception were agreed. With respect to the agreed CR one case related to disabling HS-DSCH reception was not included, some aspects related to reception of the of DL HS-PDSCH Info IE should not have been removed, and changes were not complete or correct for TDD.

1. For FDD the serving HS-DSCH RL indicator is now used for enabling and disabling reception. When the serving RL indicator is set to true a new H-RNTI must be signaled otherwise the current H-RNTI is deleted and HS-DSCH reception is disabled. HS-DSCH reception is also disabled when the serving HS-DSCH RL indicator is set to false (see referenced section 8.6.6.4).

In TDD the serving HS-DSCH RL indicator does not exist, but similar actions are necessary for the case of hard handover. It is therefore proposed that upon hard handover when a new target cell is identified by P-CCPCH info the H-RNTI is treated similarly to existing C-RNTI logic and HS-DSCH reception requirements are enforced.

2. Since it is not possible to add/remove many of the required parameters for HS-DSCH reception the description of HS-DSCH reception requirements (8.5.25) includes text describing the methods that may be used for enabling and disabling

Summary of change:	When TDD P-CCPCH info indicates a new target cell (hard handover) and a new H-RNTI is not signaled, the current H-RNTI is deleted and HS-DSCH reception criteria is checked.
	Note that corrections 3 and 4 are common for FDD and TDD operation, and corrections 1, 2 and 5 only effect TDD operation.
	Since the IE HARQ info also applied to TDD, it is proposed to move the text to the common FDD&TDD section.
	5. In the new section describing HS-DSCH reception requirements (8.5.25), text for storing of the IE HARQ info was placed in the FDD only section.
	It is proposed the section on reception of Downlink HS-PDSCH Information is returned to the specification, and actions to be taken for TDD timeslot and midamble configuration IE's, as well as references to sections describing actions to be taken upon reception of other included IE's are maintained. None of the previous text regarding enabling or disabling HS-DSCH reception is needed since this is now covered in the new section on actions related to HS_DSCH Reception (8.5.25).
	4. Also in the agreed CR on HS-DSCH reception the procedural text for IE Downlink HS-PDSCH Information reception (8.6.6.32) was removed. Our understanding for the motivation was mainly changing and clarifying the rules for enabling and disabling HS-DSCH reception (no longer based on the existence of this IE), and providing an improved maintenance of HSDPA parameters. But this section also provided other actions upon reception of the DL HS-PDSCH Info IE that need to be specified.
	It is proposed that HS-DSCH reception is checked and actions taken upon reception of "deleted DL TrCH information" and "Added or Reconfigured DL TrCH information".
	3. The "adding/removing the concerned MAC-d flows" method for enabling and disabling of HS_DSCH reception described in section 8.5.25 requires that when "deleted DL TrCH information" or "Added or Reconfigured DL TrCH information" is received and MAC-d flows are added or removed HS-DSCH reception actions are taken.
	To provide an efficient enable/disable signaling method for TDD, and cover the potentially common case upon entry to a cell that does not support R5 HSDPA, it is proposed to include removal/addition of the H-RNTI upon hard handover to the methods described in section 8.5.25. This is quite similar to the FDD method, removal/addition of the RL with serving HS-DSCH RL indicator.
	HS-DSCH reception. There are 3 methods defined: add/remove RB mapping, add/remove MAC-d flows and for FDD only add/remove the serving HS-DSCH RL. Unfortunately for TDD with the recent change in HS-DSCH reception criteria the most efficient signaling method for disabling and enabling HS-DSCH reception by setting the serving HS_DSCH RL indicator is not available. Before the agreed change a relatively efficient method existed for enabling/disabling HS-DSCH reception by the existence or not of the HS-PDSCH info IE, but with the agreed change this method is no longer available.

		The section on actions to be taken upon reception of Downlink HS-PDSCH Information (8.6.32) is returned to the specification. TDD parameters for timeslot and midamble configuration if included are stored and criteria if HS-DSCH reception is evaluated. Also references to sections describing actions to be taken upon reception of HS-SCCH Info and Measurement Feedback Info IE's are returned to the specification. Other previous text for HS-DSCH reception criteria and enabling/disabling is not included since these aspects are not specified in the
		new HS-DSCH reception section (8.5.25). In the procedural text for deleted and added or reconfigured TrCH info, HS-DSCH reception is checked in case the MAC-d flows mapped to the HS-DSCH are added or removed. HS-DSCH reception criteria specifies the IE HARQ info is also required for TDD
		operation.
Consequences if	ж	Improper management of the TDD H-RNTI
ποι αρριονεά.		Unnecessary increased signaling requirement for enabling and disabling HS- DSCH reception in TDD
		Storing of TDD timeslot and midamble configurations will not be specified and the HS-DSCH reception check specified in section (8.5.25) will fail.
		HS-DSCH reception check needed upon added or removed MAC-d flows may not be evaulated.
		Use of HARQ info will not be correctly specified for TDD
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Clauses affected:	# 8.2.2.3, 8.5.25, 8.6.5.5a, 8.6.5.6, 8.6.5.8 & 8.6.6.32

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- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

8.2.2.3 Reception of RADIO BEARER SETUP or RADIO BEARER RECONFIGURATION or RADIO BEARER RELEASE or TRANSPORT CHANNEL RECONFIGURATION or PHYSICAL CHANNEL RECONFIGURATION message by the UE

The UE shall:

- 1> be able to receive any of the following messages:
 - 2> RADIO BEARER SETUP message; or
 - 2> RADIO BEARER RECONFIGURATION message; or
 - 2> RADIO BEARER RELEASE message; or
 - 2> TRANSPORT CHANNEL RECONFIGURATION message; or
 - 2> PHYSICAL CHANNEL RECONFIGURATION message;
- 1> be able to perform a hard handover and apply physical layer synchronisation procedure A as specified in [29], even if no prior UE measurements have been performed on the target cell and/or frequency.

In case the reconfiguration procedure is used to remove all existing RL(s) in the active set while new RL(s) are established the UE shall:

- 1> if the UE has a pending "TGPS reconfiguration CFN" at the activation time received in the reconfiguration message and the reconfiguration requests a timing re-initialised hard handover (see subclause 8.3.5.1), the UE may:
 - 2> abort the pending CM activation;
 - 2> set the CM_PATTERN_ACTIVATION_ABORTED to TRUE.
- 1> otherwise:

2> set the CM_PATTERN_ACTIVATION_ABORTED to FALSE.

If the UE receives:

- a RADIO BEARER SETUP message; or
- a RADIO BEARER RECONFIGURATION message; or
- a RADIO BEARER RELEASE message; or
- a TRANSPORT CHANNEL RECONFIGURATION message; or
- a PHYSICAL CHANNEL RECONFIGURATION message:

it shall:

- 1> set the variable ORDERED_RECONFIGURATION to TRUE;
- 1> if the UE will enter the CELL_DCH state from any state other than CELL_DCH state at the conclusion of this procedure:

- 2> perform the physical layer synchronisation procedure A as specified in [29] (FDD only).
- 1> act upon all received information elements as specified in subclause 8.6, unless specified in the following and perform the actions below.

The UE may:

1> maintain a list of the set of cells to which the UE has Radio Links if the IE "Cell ID" is present.

The UE may first release the physical channel configuration used at reception of the reconfiguration message. The UE shall then:

- 1> in FDD, if the IE "PDSCH code mapping" is included but the IE "PDSCH with SHO DCH Info" is not included and if the DCH has only one link in its active set:
 - 2> act upon the IE "PDSCH code mapping" as specified in subclause 8.6; and
 - 2> infer that the PDSCH will be transmitted from the cell from which the downlink DPCH is transmitted.
- 1> enter a state according to subclause 8.6.3.3.

In case the UE receives a RADIO BEARER RECONFIGURATION message including the IE "RB information to reconfigure" that only includes the IE "RB identity", the UE shall:

- 1> handle the message as if IE "RB information to reconfigure" was absent.
- NOTE: The RADIO BEARER RECONFIGURATION message always includes the IE "RB information to reconfigure". UTRAN has to include it even if it does not require the reconfiguration of any RB.

If after state transition the UE enters CELL_DCH state, the UE shall, after the state transition:

- 1> in FDD; or
- 1> in TDD when "Primary CCPCH Info" is included indicating a new target cell and "New C-RNTI" is not specified:
 - 2> remove any C-RNTI from MAC;
 - 2> clear the variable C_RNTI.

If after state transition the UE leaves CELL_DCH state, the UE shall, after the state transition:

- 1> clear any stored IE "Downlink HS-PDSCH information";
- 1> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25.

In FDD, if after state transition the UE leaves CELL_DCH state, the UE shall, after the state transition:

- 1> remove any DSCH-RNTI from MAC;
- 1> clear the variable DSCH_RNTI.

If the UE was in CELL_DCH state upon reception of the reconfiguration message and remains in CELL_DCH state, the UE shall:

1> if the IE "Uplink DPCH Info" is absent, not change its current UL Physical channel configuration;

1> in TDD:

2> if "Primary CCPCH Info" is included indicating a new target cell and "New C-RNTI" is not specified:

3> remove any C-RNTI from MAC;

3> clear the variable C_RNTI.

2> if "Primary CCPCH Info" is included indicating a new target cell and "New H-RNTI" is not specified:

3> remove any H-RNTI from MAC;

3> clear the variable H RNTI;

3> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25.

1> if "DPCH frame offset" is included for one or more RLs in the active set:

- 2> use its value to determine the beginning of the DPCH frame in accordance with the following:
 - 3> if the received IE "DPCH frame offset" is across the value range border compared to the DPCH frame offset currently used by the UE:
 - 4> consider it to be a request to adjust the timing with 256 chips across the frame border (e.g. if the UE receives value 0 while the value currently used is 38144 consider this as a request to adjust the timing with +256 chips).
 - 3> if after taking into account value range borders, the received IE "DPCH frame offset" corresponds to a request to adjust the timing with a step exceeding 256 chips:

4> set the variable INVALID_CONFIGURATION to TRUE.

3> and the procedure ends.

2> adjust the radio link timing accordingly.

- - - - Remaining text in this section is unchanged and omitted - - - -

8.5.25 Actions related to HS_DSCH_RECEPTION variable

The variable HS_DSCH_RECEPTION shall be set to "TRUE" only when all the following conditions are met:

1> the UE is in CELL_DCH state;

1> the variable H_RNTI is set;

1> the UE has a stored IE "HS-SCCH info";

1> the UE has a stored IE "HARQ info";

1> for FDD:

2> one of the radio links in the active set is configured as the serving HS-DSCH radio link;

2> the UE has stored the following IEs:

- IE "Measurement Feedback Info";
- IE "Uplink DPCH Power Control Info" including stored _{ACK, NACK} and Ack-NACK Repetition factor;

-IE "HARQ info";

1>for 3.84 Mcps TDD, the UE has stored the following IE:

- IE "HS-PDSCH Timeslot Configuration";

1> for 1.28 Mcps TDD, the UE has stored the following IE:

- IE "HS-PDSCH Midamble Configuration";

1> there is at least one RB mapped to HS-DSCH;

- 1> at least for one of the RB's mapped to HS-DSCH, there is at least one MAC-hs queue (including the IE "MAC-d PDU size Info") configured for the concerning MAC-d flow;
- Note: To enable or disable HS-DSCH reception, the UTRAN has the possibility to add/remove the concerning HS-DSCH related RB mapping options, add/remove the concerning MAC-d flows or, for FDD, add/remove the serving HS-DSCH radio link<u>or</u>, for TDD add/remove H-RNTI upon hard handover.
- If any of the above conditions is not met and the variable HS_DSCH_RECEPTION is set to TRUE, the UE shall:
 - 1> set the variable HS_DSCH_RECEPTION to FALSE;
 - 1> stop any HS_SCCH reception procedures;
 - 1> stop any HS-DSCH reception procedures;
 - 1> clear the variable H_RNTI and remove any stored H-RNTI;
 - 1> act as if the IE "MAC-hs reset indicator" is received and set to TRUE;
 - 1> release all HARQ resources;
 - 1> no long consider any radio link to be the HS-DSCH serving radio link.
- NOTE: If configured for HS-DSCH and not explicitly indicated as being cleared, the UE will have still stored the IEs "HARQ info", "Added or Reconfigured MAC-d flow", "RB mapping Info" and "Downlink HS-PDSCH information".

Whenever the variable HS_DSCH_RECEPTION is set to TRUE, the UE shall:

1> perform HS_SCCH reception procedures according to the stored HS-SCCH configuration as stated in:

2> subclause 8.6.6.33 for the IE "HS-SCCH Info".

- 1> perform HS-DSCH reception procedures according to the stored HS-PDSCH configuration as stated in:
 - 2> subclause 8.6.3.1b for the IE "H-RNTI";
 - 2> subclause 8.6.5.6b for the IE "HARQ info";
 - 2> subclause 8.6.6.34 for the IE "Measurement Feedback Info".

Whenever the variable HS_DSCH_RECEPTION is set to FALSE, the UE shall:

1> not perform HS_SCCH reception procedures;

1> not perform HS-DSCH reception procedures;

8.6.5.5a Added or reconfigured MAC-d flow

If the IE "Added or reconfigured MAC-d flow" is included, the UE shall:

1> if a MAC-hs queue (identified by the IE "MAC-hs queue Id") is included in both the IE "MAC-hs queue to add or reconfigure list" and the IE "MAC-hs queue to delete list":

2> set the variable INVALID_CONFIGURATION to TRUE.

- 1> for each MAC-hs queue included in the IE "MAC-hs queue to add or reconfigure" list:
 - 2> if the UE has previously stored a mapping between this MAC-hs queue and a MAC-d flow:

3> delete the old mapping.

- 2> map the MAC-d flow indicated in the current message to this MAC-hs queue;
- 2> set the release timer for each of the MAC-hs queues in the MAC-hs entity to the value in the corresponding IE "T1";
- 2> set the MAC-hs receiver window size for each of the MAC-hs queues in the MAC-hs entity to the value in the corresponding IE "MAC-hs window size"; and
- 2> configure MAC-hs with the mapping between MAC-d PDU sizes index and allowed MAC-d PDU sizes as follows:
 - 3> if a MAC-d PDU size has been stored for a MAC-d PDU size index for the corresponding MAC-hs queue and no mapping is provided in the current message for this MAC-d PDU index:
 - 4> continue to use this mapping.
 - 3> if a MAC-d PDU size has been stored for a MAC-d PDU size index for the corresponding MAC-hs queue and a mapping is provided in the current message for this MAC-d PDU index:
 - 4> configure the MAC-hs entity with the mapping indicated in the current message.
- 1> for each MAC-hs queue included in the IE "MAC-hs queue to delete" list:

2> delete any information about the MAC-hs queue identified by the IE "MAC-hs queue Id".

1> if the IE "Added or reconfigured MAC-d flow" is considered valid according to the rules above:

2> perform the actions as specified in subclause 8.5.21.

1> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25.

8.6.5.8 Deleted DL TrCH information

If the IE "Deleted DL TrCH information" is included the UE shall: 1> if a Downlink transport channel is requested to be deleted:

2> delete any information about the transport channel identified by the IE "DL TrCH identity".

1> if a DL MAC-d flow is requested to be deleted:

2> delete any information about the DL HS-DSCH MAC-d flow identified by the IE "MAC-d Flow Identity", i.e. delete any information about MAC-hs queue(s) mapped onto this MAC-d flow.

2> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25

8.6.5.6 Added or Reconfigured DL TrCH information

If the IE "Added or Reconfigured DL TrCH information" is included then for the transport channel identified by the IE "DL Transport Channel Identity" the UE shall:

1> if the choice "DL parameters" is set to 'explicit':

2> perform the actions for the IE "Transport Format Set" as specified in subclause 8.6.5.1.

- 1> if the choice "DL parameters" is set to 'same as uplink':
 - 2> if the IE "UL Transport Channel Identity" indicates an existing or a new UL Transport Channel:
 - 3> store as transport format for this transport channel the transport format associated with the transport channel identified by the IE "UL Transport Channel Identity".
 - 2> else:
 - 3> set the variable INVALID_CONFIGURATION to TRUE.
- 1> if the choice "DL parameters" is set to 'HSDSCH':
 - 2> if the IE "HARQ Info" is included:

3> perform the actions specified in subclause 8.6.5.6b.

2> if the IE "Added or Reconfigured MAC-d Flow" is included:

<u>3> perform the actions as specified in subclause 8.6.5.5a</u>

<u>3> determine the value for the HS_DSCH_RECEPTION variable and take the corresponding actions as described in subclause 8.5.25.</u>

- 1> if the IE "DCH quality target" is included:
 - 2> perform the actions specified in subclause 8.6.5.4.
- NOTE: The UE stores the DL transport channel configuration until it is explicitly deleted by a message containing the IE "Deleted DL TrCH information" or the UE leaves RRC connected mode.

8.6.6.32 Downlink HS-PDSCH Information

If the IE "Downlink HS-PDSCH Information" is included and the UE would be in CELL_DCH state after completion of this procedure, the UE shall:

1> if the IE "HS-SCCH Info" is included:

2> act as specified in subclause 8.6.6.33.

1> if the IE "Measurement Feedback Info" is included:

2> act as specified in subclause 8.6.6.34.

1> if the IE "HS-DSCH Timeslot Configuration" or "HS-PDSCH Midamble Configuration" is included:

2> store the received configuration.

2> determine the value for the HS_DSCH_RECEPTION variable and take actions as described in subclause 8.5.25.

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Source:	ж	RAN WG	2								
Work item code:	Ж	HSDPA-L	.23					<i>Date:</i> ೫	4/10/	/2004	
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Reason for change: अ	MAC-hs reset was not defined as a standalone feature and was meant to always be used in conjunction with a serving cell change. In case it is used, there will be desynchronization in the MAC-hs protocol between the UE and the Node-B, leading to out-of-sequence delivery of data and ciphering desynchronization.
Summary of change: ೫	Specified that the UE behaviour is unspecified in the case where a MAC-hs reset is triggered without a change in the serving HS-DSCH cell.
Consequences if 第 not approved:	UE complexity will be increased un-necessarily for a scheme that is not supported across our specifications.
	Isolated impact analysis:
	UE does not implement the change: no impact.
	Network does not implement the change: the UE behaviour will not be predictable
	in case a Reset is performed without a cell change.

Rel-6

(Release 6)

Clauses affected: Other specs affected:	¥ 8.6.6.27 ¥ N H Other core specifications H Test specifications V N V
Other comments:	X

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at http://www.3gpp.org/specs/CR.htm. Below is a brief summary:

- 1) Fill out the above form. The symbols above marked **#** contain pop-up help information about the field that they are closest to.
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under <u>ftp://ftp.3gpp.org/specs/</u> For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.
- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

8.6.6.27 Downlink information common for all radio links

If the IE "Downlink information common for all radio links " is included the UE shall:

- 1> if the IE "Downlink DPCH info common for all RL" is included:
 - 2> perform actions as specified in subclause 8.6.6.28.
- 1> if the IE choice "mode" is set to 'FDD':
 - 2> perform actions for the IE "DPCH compressed mode info" as specified in subclause 8.6.6.15;
 - 2> perform actions for the IE "Tx Diversity mode" as specified in subclause 8.6.6.24;
 - 2> if the IE "SSDT information" is included:
 - 3> perform actions as specified in subclause 8.6.6.25.
- 1> if the IE "Default DPCH Offset value" is included:

2> perform actions as specified in the subclause 8.6.6.21.

- 1> if the IE "MAC-hs reset indicator" is included:
 - 2> if the serving HS-DSCH radio link is the same radio link as prior to the reception of the message:

3> the UE behaviour is unspecified;

2> reset the MAC-hs entity [15].

<....>

10.3.6.24 Downlink information common for all radio links

Information Element/Group	Need	Multi	Type and	Semantics	Version
name			reference	description	
Downlink DPCH info common	OP		Downlink		
for all RL			DPCH info		
			common for		
			all RL		
			10.3.6.18		
CHOICE mode	MP				
>FDD					
>>DPCH compressed mode info	OP		DPCH		
			compressed		
			mode info		
			10.3.6.33		
>>TX Diversity Mode	MD		TX Diversity	Default value is	
			Mode	the existing value	
			10.3.6.86	of TX Diversity	
				mode	
>>SSDT information	OP		SSDT		
			information		
			10.3.6.77		
>TDD				(no data)	
>>CHOICE TDD option	MP				REL-4
>>>3.84 Mcps TDD				(no data)	REL-4
>>>1.28 Mcps TDD					REL-4
>>>>TSTD indicator	MP		TSTD		REL-4
			indicator		
			10.3.6.85a		
Default DPCH Offset Value	OP		Default		

Information Element/Group	Need	Multi	Type and	Semantics	Version
name			reference	description	
			DPCH Offset		
			Value,		
			10.3.6.16		
MAC-hs reset indicator	CV-		Enumerated	TRUE Indicates	REL-5
	messageT		(true)	the MAC-hs entity	
	ype			needs to be reset.	

Condition	Explanation
MessageType	The IE is not needed in the HANDOVER TO UTRAN COMMAND and the RRC CONNECTION SETUP messages. Otherwise, it is optional.

<...>

10.3.6.27 Downlink information for each radio link

Information Element/Group	Need Multi		Type and	Semantics description	Version
Choice mode	MP		Telefence	description	
>FDD					
>>Primary CPICH info	MP		Primary CPICH info 10.3.6.60		
>>Cell ID	OP		Cell ID 10.3.2.2		REL-4
>>PDSCH with SHO DCH Info	OP		PDSCH with SHO DCH Info 10.3.6.47		
>>PDSCH code mapping	OP		PDSCH code mapping 10.3.6.43		
>>Serving HS-DSCH radio link indicator	CV- not_rrcCon nectionSet up		Boolean	The value "TRUE" indicates that this radio link is the serving HS-DSCH radio link	REL-5
>TDD					
>>Primary CCPCH info	MP		Primary CCPCH info 10.3.6.57		
Downlink DPCH info for each RL	OP		Downlink DPCH info for each RL 10.3.6.21		
SCCPCH Information for FACH	OP		SCCPCH Information for FACH 10.3.6.70		

Condition	Explanation				
not_rrcConnectionSetup	This IE is not needed in the RRC CONNECTION				
	SETUP message. Otherwise it is mandatory present.				

						CR-Form-v7						
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For <u>HELP</u> o	n u	sing this for	m, see	e bottom of this	s page	or l	ook	at the	e pop-up text	over the	э ж syr	nbols.
Proposed chang	je a	affects:	JICC a	apps#	ME		Rad	dio A	ccess Networ	k <mark>X</mark> (Core Ne	etwork
Title:	ж	MAC-hs F	Reset p	procedure								
Source:	ж	RAN WG	2									
Work item code	: Ж	HSDPA-L	23						<i>Date:</i> ೫	4/10/2	2004	
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Reason for change: 🖁	MAC-hs reset was not defined as a standalone feature and was meant to always be used in conjunction with a serving cell change. In case it is used, there will be desynchronization in the MAC-hs protocol between the UE and the Node-B, leading to out-of-sequence delivery of data and ciphering desynchronization.						
Summary of change: #	Specified that the UE behaviour is unspecified in the case where a MAC-hs reset is triggered without a change in the serving HS-DSCH cell.						
Consequences if 🔰 🖁	UE complexity will be increased un-necessarily for a scheme that is not						
not approved:	supported across our specifications.						
Clauses affected:	8.6.6.27						
Other specs affected:	Y N Other core specifications # Test specifications # O&M Specifications •						
Other comments: #							

Rel-4

Rel-5

Rel-6

(Release 4)

(Release 5)

(Release 6)

Detailed explanations of the above categories can

be found in 3GPP TR 21.900.

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8.6.6.27 Downlink information common for all radio links

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- 1> if the IE "Downlink DPCH info common for all RL" is included:
 - 2> perform actions as specified in subclause 8.6.6.28.
- 1> if the IE choice "mode" is set to 'FDD':
 - 2> perform actions for the IE "DPCH compressed mode info" as specified in subclause 8.6.6.15;
 - 2> perform actions for the IE "Tx Diversity mode" as specified in subclause 8.6.6.24;
 - 2> if the IE "SSDT information" is included:
 - 3> perform actions as specified in subclause 8.6.6.25.
- 1> if the IE "Default DPCH Offset value" is included:

2> perform actions as specified in the subclause 8.6.6.21.

- 1> if the IE "MAC-hs reset indicator" is included:
 - 2> if the serving HS-DSCH radio link is the same radio link as prior to the reception of the message:

3> the UE behaviour is unspecified;

2> reset the MAC-hs entity [15].

<....>

10.3.6.24 Downlink information common for all radio links

Information Element/Group	Need	Multi	Type and	Semantics description	Version
Downlink DPCH info common for all RL	OP		Downlink DPCH info common for all RL 10.3.6.18		
CHOICE mode	MP				
>FDD					
>>DPCH compressed mode info	OP		DPCH compressed mode info 10.3.6.33		
>>TX Diversity Mode	MD		TX Diversity Mode 10.3.6.86	Default value is the existing value of TX Diversity mode	
>>SSDT information	OP		SSDT information 10.3.6.77		
>TDD				(no data)	
>>CHOICE TDD option	MP				REL-4
>>>3.84 Mcps TDD				(no data)	REL-4
>>>1.28 Mcps TDD					REL-4
>>>>TSTD indicator	MP		TSTD indicator 10.3.6.85a		REL-4
Default DPCH Offset Value	OP		Default		

Information Element/Group	Need	Multi	Type and	Semantics	Version
name			reference	description	
			DPCH Offset		
			Value,		
			10.3.6.16		
MAC-hs reset indicator	CV-		Enumerated	TRUE Indicates	REL-5
	messageT		(true)	the MAC-hs entity	
	ype			needs to be reset.	

Condition	Explanation
MessageType	The IE is not needed in the HANDOVER TO UTRAN COMMAND and the RRC CONNECTION SETUP messages. Otherwise, it is optional.

<...>

10.3.6.27 Downlink information for each radio link

Information Element/Group	Need	Multi	Type and	Semantics description	Version
Choice mode	MP		Telefence	description	
>FDD					
>>Primary CPICH info	MP		Primary CPICH info 10.3.6.60		
>>Cell ID	OP		Cell ID 10.3.2.2		REL-4
>>PDSCH with SHO DCH Info	OP		PDSCH with SHO DCH Info 10.3.6.47		
>>PDSCH code mapping	OP		PDSCH code mapping 10.3.6.43		
>>Serving HS-DSCH radio link indicator	CV- not_rrcCon nectionSet up		Boolean	The value "TRUE" indicates that this radio link is the serving HS-DSCH radio link	REL-5
>TDD					
>>Primary CCPCH info	MP		Primary CCPCH info 10.3.6.57		
Downlink DPCH info for each RL	OP		Downlink DPCH info for each RL 10.3.6.21		
SCCPCH Information for FACH	OP		SCCPCH Information for FACH 10.3.6.70		

Condition	Explanation
not_rrcConnectionSetup	This IE is not needed in the RRC CONNECTION
	SETUP message. Otherwise it is mandatory present.