TSG RAN Meeting #26 Vouliagmeni Athens, Greece, 08 - 10 December 2004

RP-040438

TitleFor information - draft MBMS changes for TS25.402SourceTSG RAN WG3Agenda Item8.4

RAN3 Tdoc	Spec	curr. Vers.	new Vers.	CR	Rev	Cat	Rel	Title	Work item
R3-041735	25.402	6.0.0	-	-	-	-	Rel-6	Draft MBMS changes for TS 25.402	MBMS-RAN

3GPP TSG-RAN3 Meeting #45 Shin-Yokohama, Japan, 15th – 19th November 2004

	CHANGE REQUEST	CR-Form-v7.1
ж	25.402 CR XXX #rev - ^{# 0}	Current version: 6.0.0 [#]
For <u>HELP</u> on	using this form, see bottom of this page or look at the	pop-up text over the
Proposed change	e affects: UICC apps೫ ME Radio Acc	cess Network X Core Network
Title:	# Draft MBMS changes to 25.402	
Source:	# RAN WG3	
Work item code:	第 MBMS-RAN	<i>Date:</i>
Category:	 B Use <u>one</u> of the following categories: F (correction) A (corresponds to a correction in an earlier release) B (addition of feature), C (functional modification of feature) D (editorial modification) Detailed explanations of the above categories can be found in 3GPP <u>TR 21.900</u>. 	Release: #REL-6Use one of the following releases:Ph2(GSM Phase 2)R96R97(Release 1996)R97R98(Release 1998)R99Release 1999)Rel-4Release 4)Rel-5Release 5)Rel-6Rel-7(Release 7)

Reason for change: ೫	Allow setup of synchronised MBMS transport channels
Summary of change: ℜ	One reference is added
	Some MBMS related abbreviations are added
	Minor changes to the definition of [FDD – Chip Offset]
	Introduction of MBMS Frame Offset [FDD – SCCPCH Frame Offset]
	Introduction of MBMS OFFSET "MOFF"
	Introduction of new MBMS related descriptive chapter
Consequences if # not approved:	Combining methods in UE might work very inefficiently.

Clauses affected:	2, 3.3, 5 and new chapter 11	
	YN	
Other specs	X Other core specifications	
	TS25.410	
	TS25.413	
	TS25.420	
	TS25.430	
	TS25.423	
	TS25.433	
affected:	X Test specifications	
	X O&M Specifications	

Other comments: # This CR will be part of a bunch of CRs, introducing MBMS.

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at <u>http://www.3gpp.org/specs/CR.htm</u>. Below is a brief summary:

- 1) Fill out the above form. The symbols above marked **#** contain pop-up help information about the field that they are closest to.
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under <u>ftp://ftp.3gpp.org/specs/</u> For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.
- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

2 References

.....Text omitted

[26] 3GPP TS 25.346: "Introduction of MBMS in the Radio Access Network"

3.3 Abbreviations

.....Text omitted

UMTS USCH UTRAN	Universal Mobile Telecommunications System Uplink Shared CHannel UMTS Terrestrial Radio Access Network
MBMS	Multimedia Broadcast Multicast Service
MCCH	MBMS point-to-multipoint Control Channel
MTCH	MBMS point-to-multipoint Traffic Channel
p-t-p	Point-to-Point
<u>p-t-m</u>	Point-to-Multipoint

5 Synchronisation Counters and Parameters

This clause defines counters and parameters used in the different UTRAN synchronisation procedures. The parameters used only by FDD has been indicated with the notation [FDD – parameter].

BFN	Node B Frame Number counter. This is the Node B common frame number counter. [FDD -BFN is optionally frequency-locked to a Network synchronisation reference]. Range: 0 4095 frames.
RFN	RNC Frame Number counter. This is the RNC node common frame number counter. RFN is optionally frequency-locked to a Network synchronisation reference. Range: 0 4095 frames.
SFN	Cell System Frame Number counter. SFN is sent on BCH. SFN is used for paging groups and system information scheduling etc. In FDD SFN = BFN adjusted with T_cell. In TDD, if Inter Node B synchronisation port is used, SFN is locked to the BFN (i.e. SFN mod 256 = BFN mod 256). Range: 0 4095 frames.
CFN	Connection Frame Number (counter). CFN is the frame counter used for the L2/transport channel synchronisation between UE and UTRAN. A CFN value is associated to each TBS and it is passed together with it through the MAC-L1 SAP. CFN provides a common frame reference (at L2) to be used e.g. for synchronised transport channel reconfiguration (see [2] and [3]).
	The duration of the CFN cycle is longer than the maximum allowed transport delay between MAC and L1 (in UTRAN side, between SRNC and Node B, because the L1 functions that handle the transport channel synchronisation are in the Node B). Range: 0 255 frames. When used for PCH the range is 0 4095 frames.

Frame Offset	Frame Offset is a radio link specific L1 parameter used to map the CFN, used in transport channel, into the SFN that defines the specific radio frame for the transmission on the air interface.	1 the
	At the L1/L2 interaction, the mapping is performed as:	
	- SFN mod 256 = (CFN + Frame Offset) mod 256 (from L2 to L1)	(5.1);
	- CFN = (SFN - Frame Offset) mod 256 (from L1 to L2)	(5.2).
	The resolution of all three parameters is 1 frame. Frame Offset and CFN have the same range (0255) and only the 8 least significant bits of the SFN are used. The operations above are modulo 256.	
	In the UTRAN, the Frame Offset parameter is calculated by the SRNC and prov to the Node B.	vided
OFF	The parameter OFF is calculated by the UE and reported to the UTRAN only we the UTRAN has requested the UE to send this parameter. In the neighbouring callist, the UTRAN indicates for each cell if the Frame Offset is already known by UTRAN or shall be measured and reported by the UE.	ell
	OFF has a resolution of 1 frame and a range of 0 255.	
	Five different cases are discerned related to the determination of the OFF value the UE:	by
	1. The UE changes from common channel state to dedicated channel state: 1 R In this case OFF is zero.	L.
	2. [FDD -The UE changes from common channel state to dedicated channel state several RL's. OFF is in this case defined as being the difference between SFN of the candidate cells and the SFN of the camping cell. Again the UE sets OF zero for the cell to which the UE sends an UL RRC message (cell #1). F cells #2 to n, the UE sets OFF to the difference between the SFN of cell#2 and the SFN of cell#1. This could be seen as if a virtual dedicated physical channel (DPCH) alreading a ligned with cell #1].	FF to For L,n
	3. The UE adds another RL or moves to another cell in dedicated channel state OFF is in this case defined as being the time difference between the CFN an SFN of the cell in which the RL is to be added. In case this difference canno measured, a value as in [FDD - 13] [TDD - 14] shall be reported instead.	d the
	4. The UE is coming from another RAN and goes to dedicated channel state: 1 This case is identical to case 1).	RL.
	5. [FDD - The UE is coming from another RAN or another frequency in the sar RAN and goes to dedicated channel state: several RL's. This case is identical to case 2), with one exception: OFF will not be zero fo cell to which the UE sends an UL RRC message (the measurement informat: will be received via the CN in this case) but for a reference cell selected by t UE. All other reported OFF values will be relative to the SFN of this selecter reference cell].	or the ion the
[FDD – DOFF _{FDD}]	The DOFF _{FDD} (FDD Default DPCH Offset value) is used to define Frame Offset Chip Offset at first RL setup. The resolution should be good enough to spread o load over Iub and load in Node B (based on certain load distributing algorithms) addition it is used to spread out the location of Pilot Symbol in order to reduce t peak DL power since Pilot symbol is always transmitting at the fixed location w a slot (the largest number of chips for one symbol is 512 chips).	out). In the

The SRNC sends a DOFF_{FDD} parameter to the UE when the new RL will make the UE change its state (from Cell_FACH state or other when coming from another RAN) to Cell_DCH state.

Resolution: 512 chips; Range:0 .. 599 (< 80 ms).

 $[\textbf{TDD} - \textbf{DOFF}_{\textbf{TDD}}] \qquad \text{The DOFF}_{\textbf{TDD}} \ (\textbf{TDD Default DPCH Offset value}) \ \text{is used to define Frame Offset at first RL setup, in order to spread out load over /Iur and load in Node B (based on certain load distributing algorithms).}$

The SRNC sends a DOFF_{TDD} parameter to the UE when the new RL will make the UE change its state (from Cell_FACH state or other when coming from another RAN) to the Cell_DCH state.

Resolution: 1 frame; Range: 0 .. 7 frames.

[FDD - MOFF] MOFF is a parameter, which shows the time offset of given transport channel at air interface relative to P-CCPCH of reference cell. Range: 0 .. (38400*4096-256) chips.

[FDD – Chip Offset]	The Chip Offset is used as offset for the DL DPCH relative to the PCCPCH timing.
	In case of MBMS, this parameter is used as offset for S-CCPCH relative to P-
	<u>CCPCH timing.</u>
	The Chip Offset parameter has a resolution of 1 chip and a range of 0 38399 (< 10 ms).
	The Chip Offset parameter is calculated by the SRNC and provided to the Node B.
	Frame Offset + Chip Offset (sent via NBAP) are in Node B rounded together to closest
	256 chip boundary. The 256 chip boundary is used regardless of the used spreading factor, also when the spreading factor is 512. The rounded value (which is calculated in Node B) controls the DL DPCH <u>respectively S-CCPCH</u> air-interface timing.
	The "Frame Offset + Chip Offset" 256 chip boundary rounding rules for Node B to consider for each DL DPCH are:
	 IF (Frame Offset x 38 400 + Chip Offset) modulo 256 [chips] = {1127} THEN round (Frame Offset x 38 400 + Chip Offset) modulo 256 frames down to closest 256 chip boundary.
	 IF (Frame Offset x 38 400 + Chip Offset) modulo 256 [chips] = {128255} THEN round (Frame Offset x 38 400 + Chip Offset) modulo 256 frames up to closest 256 chip boundary.
	3. IF (Frame Offset x 38 400 + Chip Offset) modulo 256 [chips] = 0 THEN "Frame Offset x 38 400 + Chip Offset" is already on a 256 chip boundary.
[FDD – SCCPCH Fr	
	<u>SCCPCH Frame offset relates to Offset of S-CCPCH of a given cell relative to P-CCPCH of reference cell .</u> Range: Range: 04095 frames.
_[FDD – DPCH Frame	e Offset]

The DPCH Frame Offset is used as offset for the DL DPCH relative to the PCCPCH timing at both the Node B and the UE. The DPCH Frame Offset parameter has a resolution of 256 chips and a range of $0 \dots 38144$ chips (< 10 ms).

The DPCH Frame Offset is equivalent to Chip Offset rounded to the closest 256 chip boundary. It is calculated by the SRNC and sent to the UE by the SRNC for each radio link in the active set.

The DPCH Frame Offset controls the DL DPCH air-interface timing. It enables the DL DPCHs for radio links in the Active Set to be received at the UE at approximately the same time, which can then be soft combined during soft handover.

[FDD – Tm] The reported Tm parameter has a resolution of 1 chip and a range of 0...38399. The Tm shall always be sent by the UE.

Five different cases are discerned related to the determination of the Tm value by the UE:

1. The UE changes from common channel state to dedicated channel state: 1 RL.

In this case the Tm will be zero.

2. The UE changes from common channel state to dedicated channel state: several RL's.

Tm is in this case defined as being the time difference between the received PCCPCH path of the source cell and the received PCCPCH paths of the other target cells. Again the UE sets Tm to zero for the cell to which the UE sends an UL RRC message (cell #1). For cells #2 to n, the UE sets Tm to the time difference of the PCCPCH reception timing of cell#2,n from the PCCPCH reception timing of cell#1.

- 3. The UE adds another RL in dedicated channel state (macro-diversity). Tm is in this case defined as being the time difference between " $T_{UETX} T_o$ " and the earliest received PCCPCH path of the target cell. T_{UETX} is the time when the UE transmits an uplink DPCCH frame, hence " $T_{UETX} T_o$ " is the nominal arrival time for the first path of a received DPCH.
- 4. The UE is coming from another RAN and goes to dedicated channel state: 1 RL.

This case is identical to case 1.

- 5. The UE is coming from another RAN or another frequency in the same RAN and goes to dedicated channel state: several RL's. This case is identical to case 2, with one exception: Tm will not be zero for the cell to which the UE sends an UL RRC message (the measurement information will be received via the CN in this case) but for a reference cell selected by the UE. All other reported Tm values will be relative to the timing of the PCCPCH in this cell.
- [FDD T_cell] T_cell represents the Timing delay used for defining the start of SCH, CPICH and the DL Scrambling Code(s) in a cell relative BFN. The main purpose is to avoid having overlapping SCHs in different cells belonging to the same Node B. A SCH burst is 256 chips long. SFN in a cell is delayed T_cell relative BFN.

Resolution: 256 chips. Range: 0 .. 9 x 256 chips.

T1 RNC specific frame number (RFN) that indicates the time when RNC sends the DL NODE SYNCHRONISATION control frame through the SAP to the transport layer.

Resolution: 0.125 ms; Range: 0 .. 40959.875 ms.

T2 Node B specific frame number (BFN) that indicates the time when Node B receives the correspondent DL NODE SYNCHRONISATION control frame through the SAP from the transport layer.

Resolution: 0.125 ms; Range: 0 .. 40959.875 ms.

Τ3	Node B specific frame number (BFN) that indicates the time when Node B sends the UL NODE SYNCHRONISATION control frame through the SAP to the transport layer.
	Resolution: 0.125 ms; Range: 0 40959.875 ms.
T4	RNC specific frame number (RFN) that indicates the time when RNC receives the UL NODE SYNCHRONISATION control frame. Used in RNC locally. Not standardised over Iub.
TOAWS	TOAWS (Time of Arrival Window Startpoint) is the window startpoint. DL DATA FRAMES are expected to be received after this window startpoint. TOAWS is defined with a positive value relative Time of Arrival Window Endpoint (TOAWE) (see Figure 10). A data frame arriving before TOAWS gives a TIMING ADJUSTMENT control frame response. The resolution is 1 ms, the range is: {0 CFN length/2 -1 ms}.
TOAWE	TOAWE (Time of Arrival Window Endpoint) is the window endpoint. DL DATA FRAMES are expected to be received before this window endpoint (see Figure 10). TOAWE is defined with a positive value relative Latest Time of Arrival (LTOA). A data frame arriving after TOAWE gives a TIMING ADJUSTMENT control frame response. The resolution is 1 ms, the range is: {0 CFN length -1 ms}.
LTOA	LTOA (Latest Time of Arrival) is the latest time instant a Node B can receive a data frame and still be able to process it. Data frames received after LTOA can not be processed (discarded). LTOA is defined internally in Node B to be a processing time before the data frame is sent in air-interface. The processing time (Tproc) could be vendor and service dependent. LTOA is the reference for TOAWE (see Figure 14).
ΤΟΑ	TOA (Time of Arrival) is the time difference between the TOAWE and when a data frame is received. A positive TOA means that data frames are received before TOAWE, a negative TOA means that data frames are received after TOAWE. Data frames that are received after TOAWE but before LTOA are processed by Node B. TOA has a resolution of 125 μ s. TOA is positive when data frames are received before TOAWE (see Figure 12). The range is: {0 +CFN length/2 -125 μ s}. TOA is negative when data frames are received after TOAWE. The range is: {-125 μ sCFN length/2}.

11 MBMS related Transport Channel Synchronisation

11.1 General

<u>Point-to-multipoint transmission is used to transfer MBMS specific control/user plane information between the network and several UEs in RRC Connected or Idle Mode. In ptm mode, FACH is used as a transport channel for MTCH and MCCH. SCCPCH is used as a physical channel for FACH carrying MTCH or MCCH.</u>

11.2 FDD MBMS related Transport Channel Synchronisation

For FDD the synchronisation of MBMS related transport channels could be achieved by following steps:

Node synchronisation (as described in chapter 6)
 After carrying out this procedure, RNC is able to calculate BFN assuming that the DL and UL propagation delay have the same value. Since, for every cell the timing delay of cell (Tcell) is known to

2. Calculations for Alignment of Air Interface Transmission

<u>CRNC</u>, the CRNC is further able to calculate for every cell the three parameters Frame offset, chip offset and also the Round Trip Delay (RTD: delay between RNC and Node B).

At the beginning of these calculations, CRNC has as a result of earlier performed Node s the timing relation between RNC and every Node B. Additionally CRNC knows the value	
<u>T_cell for every cell. This includes the information about offset between SFNs of two co offset between RFN and BFNs. Having this information, CRNC is able to calculate the p MBMS related transport channels (control/data) in several cells in such a way, that these</u>	ells and also parameters for
channels are time aliened. This time alignment of MBMS transport channels in different various combining methods and eases the requirement on UE capabilities. This mechanis below (see also figure 1) :	cells enables
The offset between BFNs of two given Node Bs can be calculated: BFN(X) BFN(Y) time difference [chips] = (RFN BFN Offset X - RFN BFN Offset	<u>Y) * 38400</u> (11)
And the offset between SFNs of two given cells is given by:	
<u>SFN(m)</u> SFN(n) time difference [chips] = (RFN BFN Offset m*3840 + TCell m) - (RFN BFN Offset n*3840 + TCell n)	(11
For a transport channel in a given cell x, the time difference between P-CCPCH and S-C by:	<u>CPCH is give</u>
<u>MOFF x [chips] = SCCPCH Frame Offset * 38400 + FDD SCCPCH Offset * 256</u> The Range is: (0 157 286 144)= [0 (4096 * 38400 - 256)]	(11
The timing relation between two transmission channels in different cells x and Y can be described by:	<u>generally</u>
<u>MOFF x [chips] = SFN X-SFN Y time difference + MOFF Y) mod 157286400</u> Note: The mode operation is used to keep the offset within the allowed range (wrap arou	
<u>One gets further:</u> <u>MOFF Y [chips] = (157286400 + MOFF X - SFN X-SFN Y time difference) mod 15</u>	5 <u>7286400</u> (11
Replacing the offset between two SFNs by equation (11.2) results in:	
<u>MOFF Y [chips] = (157286400 + MOFF X – (RFN BFN Offset Y*38400) - TCell 2</u> <u>RFN BFN Offset X*38400 + TCell X) mod 157286400 = A</u>	<u>+</u> (11
Finally the farme offset and chip offset of S-CCPCH (transport channel in cell y) relative of reference cell can be calculated by the following operations:	to P-CCPC
FDD SCCPCH Frame Offset = A div 38400FDD SCCPCH Offset = A mod 38400	(11
For every trasport channel the equation (11.9) is valid:	
MOFF = (FDD_SCCPCH_Frame_Offset * 38400) + FDD_SCCPCH_Offset	(11
RNC calculates according to (11.7) and (11.8) the Frame Offset and Chip Offset information to calculate the respective CFN for the common transport channel frame pro	
By doing this procedure for every cell in the cell group, time aliened transmission channel	els through o

By doing this procedure for every cell in the cell group, time aliened transmission channels through out the cell group is achieved.

11.3 TDD MBMS related Transport Channel Synchronisation

For TDD the synchronisation of MBMS related transport channels could be achieved using methods similar to those described in section 6.1.2 which are used to achieve Inter Node B Node Synchronisation.