Title: Rel-6 CR to 25.306 on Correction to memory handling in the UE

Source: TSG-RAN WG2

Agenda item: 8.10

Spec C	CR	Rev	Phase	Subject	Cat	Version-Current	Version-New	Workitem	Doc-2nd-Level
25.306 0	96	-	Rel-6	Correction to memory handling in the UE	F	6.1.0	6.2.0	TEI6	R2-041242

3GPP TSG-RAN2 Meeting #42 Montréal, Canada, 10th – 14th May 2004

CHANGE REQUEST													
¥ 25	5.306 CR	<mark>096</mark> ⊭rev	₌ ⊯ Cui	rrent version	6.1.0 ³⁸								
For <u>HELP</u> on using	this form, see bot	tom of this page or	look at the po	p-up text ove	er the % symbols.								
Proposed change affects: UICC apps# ME X Radio Access Network X Core Network													
Title: # Co	orrection to memo	ry handling in the U	E										
Source: # R/	AN WG2												
Work item code:	ΞΙ6			Date:	/lay 2004								
Det	B (addition of feat C (functional mod. D (editorial modificalied explanations of found in 3GPP TR 2 It is currently m fulfilled at all time fficient AM RL PDUs rather the RLC entities op in the Node B r	a correction in an earlure), ification of feature) cation) of the above categories 1.900. andated that the cone in the UE. As it is C memory handling an on Rx/Tx window erating in parallel anight be unnecessaritated in §4.3 is rem	ndition stated s, this condition in the UE show sizes. As a cond/or memory rily limited.	Ise one of the 2 (GS R96 (R6 R97 (R6 R98 (R6 R99 (R6 R6-5 (R6 R6 R	o restrictive, as an ed on received/sent e, the amount of AM or re-ordering queues								
Consequences if # not approved:		of allowed UE confi f resources.	gurations, dec	graded perfo	rmance on HS-								
Clauses affected:	4.3												
Other specs # affected:	X Test spec	e specifications cifications ecifications	X										
Other comments: #	ρ												

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at http://www.3gpp.org/specs/CR.htm. Below is a brief summary:

- 1) Fill out the above form. The symbols above marked \(\mathcal{H} \) contain pop-up help information about the field that they are closest to.
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under ftp://ftp.3gpp.org/specs/ For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.
- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

4.3 RLC and MAC-hs parameters

Total RLC AM and MAC-hs buffer size

When HS-DSCH is not configured this is defined as the maximum total buffer size across all RLC AM entities supported by the UE. When HS-DSCH is configured this is defined as the maximum total buffer size across all MAC-hs reordering entities and all RLC AM entities supported by the UE. The memory signalled in this capability iscan be dynamically shared by RLC AM entities and MAC-hs reordering entities at any time. UTRAN controls that the UE capability can be fulfilled through the following parameters:

- 1. The number of RLC AM entities configured (no explicit RRC parameter);
- 2. UL PDU size;
- 3. DL PDU size;
- Transmission window size (in number of PDUs);
- 5. Receiving window size (in number of PDUs);

The following criterion must be fulfilled in the configuration at all times:

```
#RLC _ AM_ e n tities

\sum_{i=1}^{\infty} Transm \ i \ s \ s \ i \ o \ n\_window \ \_ \ size_i \quad \bullet \quad (UL\_AMD\_PDU\_ \ size_i \quad - \ AMD\_Header\_size \ ) + \\
\frac{\#RLC\_AM\_e \ n \ tities}{\sum_{i=1}^{\infty} Receiving\_window\_size_i} \quad \bullet \quad (DL\_AMD\_PDU\_ \ size_i \quad - \ AMD\_Header\_size) + \\
\leq Total\_buffer\_size
```

In order to evaluate memory consumption in the UE, it shall be assumed that:

- a stored AMD PDU of N octets requires a memory equal to (N-2) octets,
- -a stored MAC-hs PDU of N bits requires a memory equal to (N 10) bits.

The UE shall only consider itself in a memory shortage situation as defined in [9] [10] when the amount of stored AM RLC PDUs and MAC-hs PDUs exceeds its capability.

Maximum number of AM entities

This is defined as the maximum number of RLC AM entities supported by the UE.

Maximum RLC AM Window Size

This is defined as the maximum transmission and receiving window size of RLC AM entities supported by the UE.