TSG-RAN Meeting #22 **RP-030629**
Maui, USA, 09-12 December 2003

Title: **Inclusion of a default configuration identity for AMR-WB: 25.331 Rel-5 CR**

Source: **TSG-RAN WG2**

Agenda item: **7.3.2**

Spec	CR	Rev	Phase	Subject	Cat	Version-Current	Version-New	Doc-2nd-Level	Workitem
25.331	2144	-	Rel-5	Inclusion of a default configuration identity for AMR-WB	F	5.6.0	5.7.0	R2-032631	TEI5

3GPP TSG-RAN2 Meeting #39
San Diego, USA, 17th – 21st Nov 2003

Tdoc ⌘ R2-032631

CR-Form-v7

CHANGE REQUEST

⌘ **25.331** **CR** **2144** ⌘ **rev** **-** ⌘ Current version: **5.6.0** ⌘

*For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.*

Proposed change affects: UICC apps⌘ ☐ ME ☒ Radio Access Network ☒ Core Network ☐

Title:	⌘	Inclusion of a default configuration identity for AMR-WB
Source:	⌘	RAN WG2
Work item code:	⌘	TEI5
Date:	⌘	17/11/2003
Category:	⌘	**F**
Release:	⌘	Rel-5

Use one of the following categories:
F *(correction)*
A *(corresponds to a correction in an earlier release)*
B *(addition of feature),*
C *(functional modification of feature)*
D *(editorial modification)*
Detailed explanations of the above categories can be found in 3GPP TR 21.900.

Use one of the following releases:

2	*(GSM Phase 2)*
R96	*(Release 1996)*
R97	*(Release 1997)*
R98	*(Release 1998)*
R99	*(Release 1999)*
Rel-4	*(Release 4)*
Rel-5	*(Release 5)*
Rel-6	*(Release 6)*

Reason for change:	⌘	It is proposed to add the AMR-WB configuration described in 34.108 (reference 62) to the set of default configurations defined in 25.331 §13.7.
Summary of change:	⌘	The allowed range of the Default configuration identity IE is extended from 0 .. 12 to 0 .. 13. Furthermore, the AMR-WB configuration (#62) stated in 34.108 is included as Default configuration #13 in §13.7. **Isolated impact analysis:** One configuration added to the set of default configurations.
Consequences if not approved:	⌘	It will not be possible to set up an AMR-WB RAB by pointing to a default configurations in Rel5.

Clauses affected:	⌘	10.3.4.0, 13.7, 11

Other specs affected: ⌘	Y	N		⌘
		X	Other core specifications	
		X	Test specifications	
		X	O&M Specifications	

Other comments:	⌘	

How to create CRs using this form:
Comprehensive information and tips about how to create CRs can be found at http://www.3gpp.org/specs/CR.htm.
Below is a brief summary:

1) Fill out the above form. The symbols above marked ⌘ contain pop-up help information about the field that they are closest to.

2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under ftp://ftp.3gpp.org/specs/ For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.

3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

10.3.4 Radio Bearer Information elements

10.3.4.0 Default configuration identity

This information element identifies a default radio parameter configuration.

Information Element/Group name	Need	Multi	Type and reference	Semantics description	Version
Default configuration identity	MP		Integer (0..10	The corresponding default configurations are specified in 13.7	
			11, 12~~)~~		REL-4
			13)		REL-5

13.7 Parameter values for default radio configurations

The UE shall support the use of the default radio configurations that are specified in the following.

NOTE 1: These configurations are based on [41] and cover a number of RAB and signalling connection configurations.

In the table that is used to specify the parameter values for these default configurations, the following principles are used:

- Optional IEs that are not used are omitted;
- In case no parameter value is specified in a column, this means the value given the previous (left side) column applies.

NOTE 2: If needed, signalling radio bearer RB4 is established after the completion of handover.

NOTE 3: For each default configuration, the value of FDD, 3.84 Mcps TDD and 1.28 Mcps TDD parameters are specified. All parameters apply to FDD, 3.84 Mcps TDD and 1.28 Mcps TDD modes, unless explicitly stated otherwise. It should be noted that in this respect default configurations differ from pre-defined configurations, which only include parameter values for one mode.

NOTE 4: The transport format sizes, indicated in the following table, concern the RLC PDU size, since all configurations concern dedicated channels. The transport block sizes indicated in TS 34.108 are different since these include the size of the MAC header.

Configuration	**3.4 kbps signalling**	**13.6 kbps signalling**	**7.95 kbps speech + 3.4 kbps signalling**	**12.2 kbps speech + 3.4 kbps signalling**
Ref 34.108	2	3	6	4
Default configuration identity	0	1	2	3
RB INFORMATION				
rb-Identity	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6, RB7: 7
rlc-InfoChoice	Rlc-info	Rlc-info	Rlc-info	Rlc-info
>ul-RLC-Mode	RB1: UM RB2- RB3: AM	RB1: UM RB2- RB3: AM	RB1: UM RB2- RB3: AM RB5-RB6: TM	RB1: UM RB2- RB3: AM RB5-RB7: TM
>>transmissionRLC-DiscardMode	RB1: N/A RB2- RB3: NoDiscard	RB1: N/A RB2- RB3: NoDiscard	RB1: N/A RB2- RB3: NoDiscard RB5- RB6: N/A	RB1: N/A RB2- RB3: NoDiscard RB5- RB7: N/A
>>>maxDat	RB1: N/A RB2- RB3: 15	RB1: N/A RB2- RB3: 15	RB1: N/A RB2- RB3: 15 RB5- RB6: N/A	RB1: N/A RB2- RB3: 15 RB5- RB7: N/A
>>transmissionWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB6: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB7: N/A
>>timerRST	RB1: N/A RB2- RB3: 300	RB1: N/A RB2- RB3: 300	RB1: N/A RB2- RB3: 300 RB5- RB6: N/A	RB1: N/A RB2- RB3: 300 RB5- RB7: N/A
>>max-RST	RB1: N/A RB2- RB3: 1	RB1: N/A RB2- RB3: 1	RB1: N/A RB2- RB3: 1 RB5- RB6: N/A	RB1: N/A RB2- RB3: 1 RB5- RB7: N/A
>>pollingInfo	RB1: N/A RB2- RB3: as below	RB1: N/A RB2- RB3: as below	RB1: N/A RB2- RB3: as below RB5- RB6: N/A	RB1: N/A RB2- RB3: as below RB5- RB7: N/A
>>>lastTransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE

Configuration	3.4 kbps signalling	13.6 kbps signalling	7.95 kbps speech + 3.4 kbps signalling	12.2 kbps speech + 3.4 kbps signalling
>>>lastRetransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300	RB2- RB3: 100	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A	RB1- RB3: N/A	RB1- RB3: N/A RB5- RB6: FALSE	RB1- RB3: N/A RB5- RB7: FALSE
>dl-RLC-Mode	RB1: UM RB2- RB3: AM	RB1: UM RB2- RB3: AM	RB1: UM RB2- RB3: AM RB5- RB6: TM	RB1: UM RB2- RB3: AM RB5- RB7: TM
>>inSequenceDelivery	RB1: N/A RB2- RB3: TRUE	RB1: N/A RB2- RB3: TRUE	RB1: N/A RB2- RB3: TRUE RB5- RB6: N/A	RB1: N/A RB2- RB3: TRUE RB5- RB7: N/A
>>receivingWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB6: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB7: N/A
>>dl-RLC-StatusInfo	RB1: N/A RB2- RB3: as below	RB1: N/A RB2- RB3: as below	RB1: N/A RB2- RB3: as below RB5- RB6: N/A	RB1: N/A RB2- RB3: as below RB5- RB7: N/A
>>>timerStatusProhibit	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100
>>>missingPDU-Indicator	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerStatusPeriodic	RB2- RB3: 300	RB2- RB3: 100	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A	RB1- RB3: N/A	RB1- RB3: N/A RB5- RB6: FALSE	RB1- RB3: N/A RB5- RB7: FALSE
rb-MappingInfo				
>UL-LogicalChannelMappings	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel
>>ul-TransportChannelType	Dch	Dch	Dch	Dch
>>>transportChannelIdentity	RB1- RB3: 1	RB1- RB3: 1	RB1- RB3: 3 RB5: 1, RB6: 2	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3
>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
>>rlc-SizeList	RB1- RB3: configured	RB1- RB3: configured	RB1- RB3: configured RB5- RB6: N/A	RB1- RB3: configured RB5- RB7: N/A
>>mac-LogicalChannelPriority	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: 5	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: 5
>DL-logicalChannelMappingList				
>>Mapping option 1	One mapping option	One mapping option	One mapping option	One mapping option
>>>dl-TransportChannelType	Dch	Dch	Dch	Dch
>>>>transportChannelIdentity	RB1- RB3: 1	RB1- RB3: 1	RB1- RB3: 3 RB5: 1, RB6: 2	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3
>>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
TrCH INFORMATION PER TrCH				
UL-AddReconfTransChInfoList				
>Uplink transport channel type	dch	dch	dch	dch
>transportChannelIdentity	TrCH1: 1	TrCH1: 1	TrCH1: 1, TrCH2: 2, TrCH3: 3	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4

Configuration	3.4 kbps signalling	13.6 kbps signalling	7.95 kbps speech + 3.4 kbps signalling	12.2 kbps speech + 3.4 kbps signalling
>transportFormatSet	DedicatedTransChTFS	DedicatedTransChTFS	DedicatedTransChTFS	DedicatedTransChTFS
>>dynamicTF-information				
>>>tf0/ tf0,1	TrCH1: (0x144, 1x144)	TrCH1: (0x144, 1x144)	TrCH1: (0x75) TrCH2: (0x 84 1x84) TrCH3: (0x144, 1x144)	TrCH1: (0x81) TrCH2: (0x 103, 1x103) TrCH3: (0x 60, 1x60) TrCH4: (0x144, 1x144)
>>>>rlcSize	BitMode	BitMode	BitMode	BitMode
>>>>>sizeType	TrCH1: type 2, part1= 2, part2= 0 (144)	TrCH1: type 2, part1= 2, part2= 0 (144)	TrCH1: type 1: 75 TrCH2: type 1: 84 TrCH3: 2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 1: 81 TrCH2: type 1: 103 TrCH3: type 1: 60 TrCH4: 2: type 2, part1= 2, part2= 0 (144)
>>>>numberOfTbSizeList	TrCH1: Zero, one	TrCH1: Zero, one	TrCH1: Zero TrCH2-3: Zero, one	TrCH1: Zero TrCH2-4: Zero, one
>>>>logicalChannelList	All	All	All	All
>>>tf 1	N/A	N/A	TrCH1: (1x39) TrCH2- TrCH4: N/A	TrCH1: (1x39) TrCH2- TrCH4: N/A
>>>>numberOfTransportBlocks			TrCH1: One	TrCH1: One
>>>>rlc-Size			TrCH1: BitMode	TrCH1: BitMode
>>>>>sizeType			TrCH1: 1: 39	TrCH1: 1: 39
>>>>numberOfTbSizeList			TrCH1: One	TrCH1: One
>>>>logicalChannelList			TrCH1: all	TrCH1: all
>>>tf 2	N/A	N/A	TrCH1: (1x75) TrCH2- TrCH3: N/A	TrCH1: (1x81) TrCH2- TrCH4: N/A
>>>>numberOfTransportBlocks			TrCH1: One	TrCH1: One
>>>>rlc-Size			TrCH1: BitMode	TrCH1: BitMode
>>>>>sizeType			TrCH1: type 1: 75	TrCH1: type 1: 81
>>>>numberOfTbSizeList			TrCH1: One	TrCH1: One
>>>>logicalChannelList			TrCH1: all	TrCH1: all
>>semistaticTF-Information				
>>>tti	TrCH1: 40	TrCH1: 10	TrCH1- TrCH2: 20 TrCH3: 40	TrCH1- TrCH3: 20 TrCH4: 40
>>>channelCodingType	Convolutional	Convolutional	Convolutional	Convolutional
>>>>codingRate	TrCH1: Third	TrCH1: Third	TrCH1- TrCH2: Third TrCH3: Third	TrCH1- TrCH2: Third TrCH3: Half TrCH4: Third
>>>rateMatchingAttribute	TrCH1: 160	TrCH1: 160	TrCH1: 200 TrCH2: 190 TrCH3: 160	TrCH1: 200 TrCH2: 190 TrCH3: 235 TrCH4: 160
>>>crc-Size	TrCH1: 16	TrCH1: 16	TrCH1: 12 TrCH2: 0 TrCH3: 16	TrCH1: 12 TrCH2- TrCH3: 0 TrCH4: 16
DL-AddReconfTransChInfoList				
>Downlink transport channel type	dch	dch	dch	dch
>dl-TransportChannelIdentity (should be as for UL)	TrCH1: 1	TrCH1: 1	TrCH1: 1, TrCH2: 2, TrCH3: 3	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>tfs-SignallingMode	SameAsUL	SameAsUL	Explicit <Only tf0 on TrCH1 is different and shown below>	Explicit <Only tf0 on TrCH1 is different and shown below>

Configuration	3.4 kbps signalling	13.6 kbps signalling	7.95 kbps speech + 3.4 kbps signalling	12.2 kbps speech + 3.4 kbps signalling
>>transportFormatSet			DedicatedTransChTFS	DedicatedTransChTFS
>>>dynamicTF-information				
>>>>tf0/ tf0,1			TrCH1: (1x0)	TrCH1: (1x0)
>>>>rlcSize			BitMode	bitMode
>>>>>sizeType			TrCH1: type 1: 0	TrCH1: type 1: 0
>>>>numberOfTbSizeList			TrCH1: One	TrCH1: One
>>>>logicalChannelList			All	All
>>ULTrCH-Id	TrCH1: 1	TrCH1: 1	TrCH1: 1, TrCH2: 2, TrCH3: 3	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>dch-QualityTarget				
>>bler-QualityValue	TrCH1: $5x10^{-2}$	TrCH1: $5x10^{-2}$	TrCH1: $7x10^{-3}$ TrCH2- TrCH3: Absent	TrCH1: $7x10^{-3}$ TrCH2- TrCH4: Absent
TrCH INFORMATION, COMMON				
ul-CommonTransChInfo				
>tfcs-ID (TDD only)	1	1	1	1
>sharedChannelIndicator (TDD only)	FALSE	FALSE	FALSE	FALSE
>tfc-Subset	Absent, not required	Absent, not required	Absent, not required	Absent, not required
>ul-TFCS	Normal TFCI signalling	Normal TFCI signalling	Normal TFCI signalling	Normal TFCI signalling
>>explicitTFCS-ConfigurationMode	Complete	Complete	Complete	Complete
>>>ctfcSize	Ctfc2Bit	Ctfc2Bit	Ctfc4Bit	Ctfc6Bit
>>>>TFCS representation	Addition	Addition	Addition	Addition
>>>>>TFCS list				
>>>>>>TFCS 1	(TF0)	(TF0)	(TF0, TF0, TF0)	(TF0, TF0, TF0, TF0)
>>>>>>>ctfc	0	0	0	0
>>>>>>>gainFactorInformation	Computed	Computed	Computed	Computed
>>>>>>>>referenceTFCId	0	0	0	0
>>>>>>TFCS 2	(TF1)	(TF1)	(TF1, TF0, TF0)	(TF1, TF0, TF0, TF0)
>>>>>>>ctfc	1	1	1	1
>>>>>>>gainFactorInformation	Signalled	Signalled	Computed	Computed
>>>>>>>>βc (FDD only)	11	11	N/A	N/A
>>>>>>>>βd	15	15	N/A	N/A
>>>>>>>>referenceTFCId	0	0	0	0
>>>>>>TFCS 3			(TF2, TF1, TF0)	(TF2, TF1, TF1, TF0)
>>>>>>>ctfc			5	11
>>>>>>>gainFactorInformation			Computed	Computed
>>>>>>>>referenceTFCId			0	0
>>>>>>TFCS 4			(TF0, TF0, TF1)	(TF0, TF0, TF0, TF1)
>>>>>>>ctfc			6	12
>>>>>>>gainFactorInformation			Computed	Computed
>>>>>>>>βc (FDD only)			N/A	N/A
>>>>>>>>βd			N/A	N/A
>>>>>>>>referenceTFCId			0	0
>>>>>>TFCS 5			(TF1, TF0, TF1)	(TF1, TF0, TF0, TF1)
>>>>>>>ctfc			7	13

Configuration	3.4 kbps signalling	13.6 kbps signalling	7.95 kbps speech + 3.4 kbps signalling	12.2 kbps speech + 3.4 kbps signalling
>>>>>>>gainFactorInformation			Computed	Computed
>>>>>>>>referenceTFCId			0	0
>>>>>>TFCS 6			(TF2, TF1, TF1)	(TF2, TF1, TF1, TF1)
>>>>>>>ctfc			11	23
>>>>>>>gainFactorInformation			Signalled	Signalled
>>>>>>>>βc (FDD only)			11	11
>>>>>>>>βd			15	15
>>>>>>>>referenceTFCId			0	0
dl-CommonTransChInfo				
>tfcs-SignallingMode	Same as UL	Same as UL	Same as UL	Same as UL
PhyCH INFORMATION FDD				
UL-DPCH-InfoPredef				
>ul-DPCH-PowerControlInfo				
>>powerControlAlgorithm	Algorithm 1	Algorithm 1	Algorithm 1	Algorithm 1
>>>tpcStepSize	1	1	1	1
>tfci-Existence	TRUE	TRUE	TRUE	TRUE
>puncturingLimit	1	1	1	0.88
DL-CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>spreadingFactor	256	128	128	128
>>tfci-Existence	FALSE	FALSE	FALSE	FALSE
>>pilotBits	4	4	4	4
>>positionFixed	N/A	N/A	Fixed	Fixed
PhyCH INFORMATION 3.84 Mcps TDD				
UL-DPCH-InfoPredef				
>ul-DPCH-PowerControlInfo				
>>dpch-ConstantValue	0	0	0	0
>commonTimeslotInfo				
>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>tfci-Coding	4	4	16	16
>>puncturingLimit	1	0.92	0.52	0.88
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
DL-CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>commonTimeslotInfo				
>>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	4	4	16	16
>>>puncturingLimit	1	0.92	0.52	0.92
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
PhyCH INFORMATION 1.28 Mcps TDD				
UL-DPCH-InfoPredef				
>commonTimeslotInfo				
>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>tfci-Coding	4	4	16	16
>>puncturingLimit	1	0.64	0.80	0.60
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1

Configuration	3.4 kbps signalling	13.6 kbps signalling	7.95 kbps speech + 3.4 kbps signalling	12.2 kbps speech + 3.4 kbps signalling
DL-CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>commonTimeslotInfo				
>>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	4	4	16	16
>>>puncturingLimit	1	0.64	0.80	0.60
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1

Configuration	28.8 kbps conv. CS- data + 3.4 kbps signalling	32 kbps conv. CS-data + 3.4 kbps signalling	64kbps conv. CS-data + 3.4 kbps signalling	14.4 kbps streaming CS-data + 3.4 kbps signalling
Ref 34.108	12	14	13	15
Default configuration identity	4	5	6	7
RB INFORMATION				
rb-Identity	RB1: 1, RB2: 2, RB3: 3, RB5: 5	RB1: 1, RB2: 2, RB3: 3, RB5: 5	RB1: 1, RB2: 2, RB3: 3, RB5: 5	RB1: 1, RB2: 2, RB3: 3, RB5: 5
rlc-InfoChoice	Rlc-info	Rlc-info	Rlc-info	Rlc-info
>ul-RLC-Mode	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM
>>transmissionRLC-DiscardMode	RB1: N/A RB2- RB3: NoDiscard RB5: N/A	RB1: N/A RB2- RB3: NoDiscard RB5: N/A	RB1: N/A RB2- RB3: NoDiscard RB5: N/A	RB1: N/A RB2- RB3: NoDiscard RB5: N/A
>>>maxDat	RB1: N/A RB2- RB3: 15 RB5: N/A	RB1: N/A RB2- RB3: 15 RB5: N/A	RB1: N/A RB2- RB3: 15 RB5: N/A	RB1: N/A RB2- RB3: 15 RB5: N/A
>>transmissionWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A
>>timerRST	RB1: N/A RB2- RB3: 300 RB5: N/A	RB1: N/A RB2- RB3: 300 RB5: N/A	RB1: N/A RB2- RB3: 300 RB5: N/A	RB1: N/A RB2- RB3: 300 RB5: N/A
>>max-RST	RB1: N/A RB2- RB3: 1 RB5: N/A	RB1: N/A RB2- RB3: 1 RB5: N/A	RB1: N/A RB2- RB3: 1 RB5: N/A	RB1: N/A RB2- RB3: 1 RB5: N/A
>>pollingInfo	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A
>>>lastTransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>lastRetransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE
>dl-RLC-Mode	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM
>>inSequenceDelivery	RB1: N/A RB2- RB3: TRUE RB5: N/A	RB1: N/A RB2- RB3: TRUE RB5: N/A	RB1: N/A RB2- RB3: TRUE RB5: N/A	RB1: N/A RB2- RB3: TRUE RB5: N/A

Configuration	28.8 kbps conv. CS- data + 3.4 kbps signalling	32 kbps conv. CS-data + 3.4 kbps signalling	64kbps conv. CS-data + 3.4 kbps signalling	14.4 kbps streaming CS-data + 3.4 kbps signalling
>>receivingWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A
>>dl-RLC-StatusInfo	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A
>>>timerStatusProhibit	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100
>>>missingPDU-Indicator	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerStatusPeriodic	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE
rb-MappingInfo				
>UL-LogicalChannelMappings	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel
>>ul-TransportChannelType	Dch	Dch	Dch	Dch
>>>transportChannelIdentity	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1
>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A
>>rlc-SizeList	RB1- RB3: configured RB5: N/A	RB1- RB3: configured RB5: N/A	RB1- RB3: configured RB5: N/A	RB1- RB3: configured RB5: N/A
>>mac-LogicalChannelPriority	RB1: 1, RB2: 2, RB3: 3 RB5: 5	RB1: 1, RB2: 2, RB3: 3 RB5: 5	RB1: 1, RB2: 2, RB3: 3 RB5: 5	RB1: 1, RB2: 2, RB3: 3 RB5: 5
>DL-logicalChannelMappingList				
>>Mapping option 1	One mapping option	One mapping option	One mapping option	One mapping option
>>>dl-TransportChannelType	Dch	Dch	Dch	Dch
>>>>transportChannelIdentity	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1
>>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A
TrCH INFORMATION PER TrCH				
UL-AddReconfTransChInfoList				
>Uplink transport channel type	dch	dch	dch	dch
>transportChannelIdentity	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2
>transportFormatSet	DedicatedTransChTFS	DedicatedTransChTFS	DedicatedTransChTFS	DedicatedTransChTFS
>>dynamicTF-information				
>>>tf0/ tf0,1	TrCH1: (0x576, 1x576, 2x576) TrCH2: (0x144, 1x144)	TrCH1: (0x640, 1x640) TrCH2: (0x144, 1x144)	TrCH1: (0x640, 2x640) TrCH2: (0x144, 1x144)	TrCH1: (0x576, 1x576) TrCH2: (0x144, 1x144)
>>>>rlcSize	TrCH1: OctetMode TrCH2:BitMode	TrCH1: OctetMode TrCH2:BitMode	TrCH1: OctetMode TrCH2:BitMode	TrCH1: OctetMode TrCH2:BitMode

Configuration	28.8 kbps conv. CS- data + 3.4 kbps signalling	32 kbps conv. CS-data + 3.4 kbps signalling	64kbps conv. CS-data + 3.4 kbps signalling	14.4 kbps streaming CS-data + 3.4 kbps signalling
>>>>>sizeType	TrCH1: type 2, part1= 9, part2= 2 (576) TrCH2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 2, part1= 11, part2= 2 (640) TrCH2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 2, part1= 11, part2= 2 (640) TrCH2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 2, part1= 9, part2= 2 (576) TrCH2: type 2, part1= 2, part2= 0 (144)
>>>>numberOfTbSizeList	TrCH1: Zero,1, 2 TrCH2: Zero, one	TrCH1: Zero, one TrCH2: Zero, one	TrCH1: Zero, 2 TrCH2: Zero, one	TrCH1: Zero, one, TrCH2: Zero, one
>>>>logicalChannelList	All	All	All	All
>>semiStaticTF-Information				
>>>tti	TrCH1: 40 TrCH2: 40	TrCH1: 20 TrCH2: 40	TrCH1: 20 TrCH2: 40	TrCH1: 40 TrCH2: 40
>>>channelCodingType	TrCH1: Turbo TrCH2: Convolutional	TrCH1: Turbo TrCH2: Convolutional	TrCH1: Turbo TrCH2: Convolutional	TrCH1: Turbo TrCH2: Convolutional
>>>>codingRate	TrCH1: N/A TrCH2: Third	TrCH1: N/A TrCH2: Third	TrCH1: N/A TrCH2: Third	TrCH1: N/A TrCH2: Third
>>>rateMatchingAttribute	TrCH1: 180 TrCH2: 160	TrCH1: 185 TrCH2: 160	TrCH1: 170 TrCH2: 160	TrCH1: 165 TrCH2: 160
>>>crc-Size	TrCH1: 16 TrCH2: 16	TrCH1: 16 TrCH2: 16	TrCH1: 16 TrCH2: 16	TrCH1: 16 TrCH2: 16
DL-AddReconfTransChInfoList				
>Downlink transport channel type	dch	dch	dch	dch
>dl-TransportChannelIdentity (should be as for UL)	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2
>tfs-SignallingMode	SameAsUL	SameAsUL	SameAsUL	SameAsUL
>>transportFormatSet				
>>>dynamicTF-information				
>>>>tf0/ tf0,1				
>>>>rlcSize				
>>>>>sizeType				
>>>>numberOfTbSizeList				
>>>>logicalChannelList				
>>ULTrCH-Id	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2
>dch-QualityTarget				
>>bler-QualityValue	TrCH1: $2x10^{-3}$ TrCH2: Absent	TrCH1: $2x10^{-3}$ TrCH2: Absent	TrCH1: $2x10^{-3}$ TrCH2: Absent	TrCH1: $1x10^{-2}$ TrCH2: Absent
TrCH INFORMATION, COMMON				
ul-CommonTransChInfo				
>tfcs-ID (TDD only)	1	1	1	1
>sharedChannelIndicator (TDD only)	FALSE	FALSE	FALSE	FALSE
>tfc-Subset	Absent, not required	Absent, not required	Absent, not required	Absent, not required
>ul-TFCS	Normal TFCI signalling	Normal TFCI signalling	Normal TFCI signalling	Normal TFCI signalling
>>explicitTFCS-ConfigurationMode	Complete	Complete	Complete	Complete
>>>ctfcSize	Ctfc4Bit	Ctfc2Bit	Ctfc2Bit	Ctfc4Bit
>>>>TFCS representation	Addition	Addition	Addition	Addition
>>>>>TFCS list				
>>>>>>TFCS 1	(TF0, TF0)	(TF0, TF0)	(TF0, TF0)	(TF0, TF0)
>>>>>>>ctfc	0	0	0	0
>>>>>>>gainFactorInformation	Computed	Computed	Computed	Computed
>>>>>>>>referenceTFCId	0	0	0	0

Configuration	28.8 kbps conv. CS- data + 3.4 kbps signalling	32 kbps conv. CS-data + 3.4 kbps signalling	64kbps conv. CS-data + 3.4 kbps signalling	14.4 kbps streaming CS-data + 3.4 kbps signalling
>>>>>>TFCS 2	(TF1, TF0)	(TF1, TF0)	(TF1, TF0)	(TF1, TF0)
>>>>>>>ctfc	1	1	1	1
>>>>>>>gainFactorInformation	Computed	Computed	Computed	Computed
>>>>>>>>βc (FDD only)	N/A	N/A	N/A	N/A
>>>>>>>>βd	N/A	N/A	N/A	N/A
>>>>>>>>referenceTFCId	0	0	0	0
>>>>>>TFCS 3	(TF2, TF0)	(TF0, TF1)	(TF0, TF1)	(TF0, TF1)
>>>>>>>ctfc	2	2	2	2
>>>>>>>gainFactorInformation	Computed	Computed	Computed	Computed
>>>>>>>>referenceTFCId	0	0	0	0
>>>>>>TFCS 4	(TF0, TF1)	(TF1, TF1)	(TF1, TF1)	(TF1, TF1)
>>>>>>>ctfc	3	3	3	3
>>>>>>>gainFactorInformation	Computed	Signalled	Signalled	Signalled
>>>>>>>>βc (FDD only)	N/A	8	8	11
>>>>>>>>βd	N/A	15	15	15
>>>>>>>>referenceTFCId	0	0	0	0
>>>>>>TFCS 5	(TF1, TF1)	N/A	N/A	
>>>>>>>ctfc	4			
>>>>>>>gainFactorInformation	Computed			
>>>>>>>>referenceTFCId	0			
>>>>>>TFCS 6	(TF2, TF1)	N/A	N/A	
>>>>>>>ctfc	5			
>>>>>>>gainFactorInformation	Signalled			
>>>>>>>>βc (FDD only)	8			
>>>>>>>>βd	15			
>>>>>>>>referenceTFCId	0			
>>>>>>TFCS 7				
>>>>>>>ctfc				
>>>>>>>gainFactorInformation				
>>>>>>>>referenceTFCId				
>>>>>>TFCS 8				
>>>>>>>ctfc				
>>>>>>>gainFactorInformation				
>>>>>>>>referenceTFCId				
>>>>>>TFCS 9				
>>>>>>>ctfc				
>>>>>>>gainFactorInformation				
>>>>>>>>referenceTFCId				
>>>>>>TFCS 10				
>>>>>>>ctfc				
>>>>>>>gainFactorInformation				
>>>>>>>>βc (FDD only)				
>>>>>>>>βd				
>>>>>>>>referenceTFCId				
dl-CommonTransChInfo				
>tfcs-SignallingMode	Same as UL	Same as UL	Same as UL	Same as UL
PhyCH INFORMATION FDD				
UL-DPCH-InfoPredef				

Configuration	28.8 kbps conv. CS- data + 3.4 kbps signalling	32 kbps conv. CS-data + 3.4 kbps signalling	64kbps conv. CS-data + 3.4 kbps signalling	14.4 kbps streaming CS-data + 3.4 kbps signalling
>ul-DPCH-PowerControlInfo				
>>powerControlAlgorithm	Algorithm 1	Algorithm 1	Algorithm 1	Algorithm 1
>>>tpcStepSize	1	1	1	1
>tfci-Existence	TRUE	TRUE	TRUE	TRUE
>puncturingLimit	0.92	0.8	0.92	1
DL-CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>spreadingFactor	64	64	32	128
>>tfci-Existence	TRUE	TRUE	TRUE	TRUE
>>pilotBits	8	8	8	8
>>positionFixed	Flexible	Flexible	Flexible	Flexible
PhyCH INFORMATION 3.84 Mcps TDD				
UL-DPCH-InfoPredef				
>ul-DPCH-PowerControlInfo				
>>dpch-ConstantValue	0	0	0	0
>commonTimeslotInfo				
>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>tfci-Coding	16	8	8	8
>>puncturingLimit	0.44	0.8	0.56	0.8
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
DL-CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>commonTimeslotInfo				
>>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	16	8	8	8
>>>puncturingLimit	0.44	0.64	0.56	0.8
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
PhyCH INFORMATION 1.28 Mcps TDD				
UL-DPCH-InfoPredef				
>commonTimeslotInfo				
>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>tfci-Coding	16	8	8	8
>>puncturingLimit	0.64	0.60	0.64	1
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
DL-CommonInformationPredef				
>dl-DPCH-InfoCommon				
>>commonTimeslotInfo				
>>>secondInterleavingMode	frameRelated	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	16	8	8	8
>>>puncturingLimit	0.64	0.60	0.64	0.88
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1

Configuration	28.8 kbps streaming CS-data + 3.4 kbps signalling	57.6 kbps streaming CS-data + 3.4 kbps signalling	12.2 kbps speech(multimode) + 3.4 kbps signalling
Ref 34.108	16	17	1a
Default configuration identity	8	9	10
RB INFORMATION			
rb-Identity	RB1: 1, RB2: 2, RB3: 3, RB5: 5	RB1: 1, RB2: 2, RB3: 3, RB5: 5	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6, RB7: 7
rlc-InfoChoice	Rlc-info	Rlc-info	Rlc-info
>ul-RLC-Mode	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5-RB7: TM
>>transmissionRLC-DiscardMode	RB1: N/A RB2- RB3: NoDiscard RB5: N/A	RB1: N/A RB2- RB3: NoDiscard RB5: N/A	RB1: N/A RB2- RB3: NoDiscard RB5- RB7: N/A
>>>maxDat	RB1: N/A RB2- RB3: 15 RB5: N/A	RB1: N/A RB2- RB3: 15 RB5: N/A	RB1: N/A RB2- RB3: 15 RB5- RB7: N/A
>>transmissionWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB7: N/A
>>timerRST	RB1: N/A RB2- RB3: 300 RB5: N/A	RB1: N/A RB2- RB3: 300 RB5: N/A	RB1: N/A RB2- RB3: 300 RB5- RB7: N/A
>>max-RST	RB1: N/A RB2- RB3: 1 RB5: N/A	RB1: N/A RB2- RB3: 1 RB5: N/A	RB1: N/A RB2- RB3: 1 RB5- RB7: N/A
>>pollingInfo	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5- RB7: N/A
>>>lastTransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>lastRetransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5- RB7: FALSE
>dl-RLC-Mode	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5: TM	RB1: UM RB2- RB3: AM RB5- RB7: TM
>>inSequenceDelivery	RB1: N/A RB2- RB3: TRUE RB5: N/A	RB1: N/A RB2- RB3: TRUE RB5: N/A	RB1: N/A RB2- RB3: TRUE RB5- RB7: N/A
>>receivingWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB7: N/A
>>dl-RLC-StatusInfo	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5: N/A	RB1: N/A RB2- RB3: as below RB5- RB7: N/A
>>>timerStatusProhibit	RB2- RB3: 100	RB2- RB3: 100	RB2- RB3: 100
>>>missingPDU-Indicator	RB2- RB3: FALSE	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerStatusPeriodic	RB2- RB3: 300	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5: FALSE	RB1- RB3: N/A RB5- RB7: FALSE

Configuration	28.8 kbps streaming CS-data + 3.4 kbps signalling	57.6 kbps streaming CS-data + 3.4 kbps signalling	12.2 kbps speech(multimode) + 3.4 kbps signalling
rb-MappingInfo			
>UL-LogicalChannelMappings	OneLogicalChannel	OneLogicalChannel	OneLogicalChannel
>>ul-TransportChannelType	Dch	Dch	Dch
>>>transportChannelIdentity	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3
>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
>>rlc-SizeList	RB1- RB3: configured RB5: N/A	RB1- RB3: configured RB5: N/A	RB1- RB3: configured RB5- RB7: N/A
>>mac-LogicalChannelPriority	RB1: 1, RB2: 2, RB3: 3 RB5: 5	RB1: 1, RB2: 2, RB3: 3 RB5: 5	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: 5
>DL-logicalChannelMappingList			
>>Mapping option 1	One mapping option	One mapping option	One mapping option
>>>dl-TransportChannelType	Dch	Dch	Dch
>>>>transportChannelIdentity	RB1- RB3: 2 RB5: 1	RB1- RB3: 2 RB5: 1	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3
>>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
TrCH INFORMATION PER TrCH			
UL-AddReconfTransChInfoList			
>Uplink transport channel type	dch	dch	dch
>transportChannelIdentity	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>transportFormatSet	DedicatedTransChTFS	DedicatedTransChTFS	DedicatedTransChTFS
>>dynamicTF-information			
>>>tf0/ tf0,1	TrCH1: (0x576, 1x576, 2x576) TrCH2: (0x144, 1x144)	TrCH1: (0x576, 1x576, 2x576, 3x576, 4x576) TrCH2: (0x144, 1x144)	TrCH1: (0x81) TrCH2: (0x 103 TrCH3: (0x 60) TrCH4: (0x144)
>>>>rlcSize	TrCH1: OctetMode TrCH2:BitMode	TrCH1: OctetMode TrCH2:BitMode	BitMode
>>>>>sizeType	TrCH1: type 2, part1= 9, part2= 2 (576) TrCH2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 2, part1= 9, part2= 2 (576) TrCH2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 1: 81 TrCH2: type 1: 103 TrCH3: type 1: 60 TrCH4: 2: type 2, part1= 2, part2= 0 (144)
>>>>numberOfTbSizeList	TrCH1: Zero, one, 2 TrCH2: Zero, one	TrCH1: Zero, one, 2, 3, 4 TrCH2: Zero, one	TrCH1-4: Zero
>>>>logicalChannelList	All	All	All
>>>tf 1			TrCH1: (1x39) TrCH2: (1x53) TrCH3: (1x60) TrCH4: (1x144)

Configuration	28.8 kbps streaming CS-data + 3.4 kbps signalling	57.6 kbps streaming CS-data + 3.4 kbps signalling	12.2 kbps speech(multimode) + 3.4 kbps signalling
>>>>numberOfTransportBlocks			TrCH1-3: One
>>>>rlc-Size			TrCH1-3: BitMode
>>>>>sizeType			TrCH1: 1: 39 TrCH2: 1: 53 TrCH3: 1: 60
>>>>numberOfTbSizeList			TrCH1-3: One
>>>>logicalChannelList			TrCH1-3: all
>>>tf 2			TrCH1: (1x42) TrCH2: (1x63) TrCH3- TrCH4: N/A
>>>>numberOfTransportBlocks			TrCH1-2: One
>>>>rlc-Size			TrCH1: BitMode
>>>>>sizeType			TrCH1: type 1: 42 TrCH2: type 1: 63
>>>>numberOfTbSizeList			TrCH1-2: One
>>>>logicalChannelList			TrCH1: all
>>>tf 3			TrCH1: (1x55) TrCH2: (1x84) TrCH3- TrCH4: N/A
>>>>numberOfTransportBlocks			TrCH1-2: One
>>>>rlc-Size			TrCH1: BitMode
>>>>>sizeType			TrCH1: type 1: 55 TrCH2: type 1: 84
>>>>numberOfTbSizeList			TrCH1-2: One
>>>>logicalChannelList			TrCH1: all
>>>tf 4			TrCH1: (1x75) TrCH2: (1x103) TrCH3- TrCH4: N/A
>>>>numberOfTransportBlocks			TrCH1-2: One
>>>>rlc-Size			TrCH1: BitMode
>>>>>sizeType			TrCH1: type 1: 75 TrCH2: type 1: 103
>>>>numberOfTbSizeList			TrCH1-2: One
>>>>logicalChannelList			TrCH1: all
>>>tf 5			TrCH1: (1x81) TrCH2- TrCH4: N/A
>>>>numberOfTransportBlocks			TrCH1: One
>>>>rlc-Size			TrCH1: BitMode
>>>>>sizeType			TrCH1: type 1: 81
>>>>numberOfTbSizeList			TrCH1: One
>>>>logicalChannelList			TrCH1: all
>>semiStaticTF-Information			
>>>tti	TrCH1: 40 TrCH2: 40	TrCH1: 40 TrCH2: 40	TrCH1- TrCH3: 20 TrCH4: 40
>>>channelCodingType	TrCH1: Turbo TrCH2: Convolutional	TrCH1: Turbo TrCH2: Convolutional	Convolutional
>>>>codingRate	TrCH1: N/A TrCH2: Third	TrCH1: N/A TrCH2: Third	TrCH1- TrCH2: Third TrCH3: Half TrCH4: Third
>>>rateMatchingAttribute	TrCH1: 155 TrCH2: 160	TrCH1: 145 TrCH2: 160	TrCH1: 200 TrCH2: 190 TrCH3: 235 TrCH4: 160

Configuration	28.8 kbps streaming CS-data + 3.4 kbps signalling	57.6 kbps streaming CS-data + 3.4 kbps signalling	12.2 kbps speech(multimode) + 3.4 kbps signalling
>>>crc-Size	TrCH1: 16 TrCH2: 16	TrCH1: 16 TrCH2: 16	TrCH1: 12 TrCH2- TrCH3: 0 TrCH4: 16
DL-AddReconfTransChInfoList			
>Downlink transport channel type	dch	dch	dch
>dl-TransportChannelIdentity (should be as for UL)	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>tfs-SignallingMode	SameAsUL	SameAsUL	Independent <Only tf0 on TrCH1 is different and shown below>
>>transportFormatSet			DedicatedTransChTFS
>>>dynamicTF-information			
>>>>tf0/ tf0,1			TrCH1: (1x0)
>>>>rlcSize			bitMode
>>>>>sizeType			TrCH1: type 1: 0
>>>>numberOfTbSizeList			TrCH1: One
>>>>logicalChannelList			All
>>ULTrCH-Id	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4
>dch-QualityTarget			
>>bler-QualityValue	TrCH1: $1x10^{-2}$ TrCH2: Absent	TrCH1: $1x10^{-2}$ TrCH2: Absent	TrCH1: $7x10^{-3}$ TrCH2- TrCH4: Absent
TrCH INFORMATION, COMMON			
ul-CommonTransChInfo			
>tfcs-ID (TDD only)	1	1	1
>sharedChannelIndicator (TDD only)	FALSE	FALSE	FALSE
>tfc-Subset	Absent, not required	Absent, not required	Absent, not required
>ul-TFCS	Normal TFCI signalling	Normal TFCI signalling	Normal TFCI signalling
>>explicitTFCS-ConfigurationMode	Complete	Complete	Complete
>>>ctfcSize	Ctfc4Bit	Ctfc4Bit	Ctfc8Bit
>>>>TFCS representation	Addition	Addition	Addition
>>>>>TFCS list			
>>>>>>TFCS 1	(TF0, TF0)	(TF0, TF0)	(TF0, TF0, TF0, TF0)
>>>>>>>ctfc	0	0	0
>>>>>>>gainFactorInformation	Computed	Computed	Computed
>>>>>>>>referenceTFCId	0	0	0
>>>>>>TFCS 2	(TF1, TF0)	(TF1, TF0)	(TF1, TF0, TF0, TF0)
>>>>>>>ctfc	1	1	1
>>>>>>>gainFactorInformation	Computed	Computed	Computed
>>>>>>>>βc (FDD only)	N/A	N/A	N/A
>>>>>>>>βd	N/A	N/A	N/A
>>>>>>>>referenceTFCId	0	0	0
>>>>>>TFCS 3	(TF2, TF0)	(TF2, TF0)	(TF2, TF1, TF0, TF0)
>>>>>>>ctfc	2	2	8

Configuration	28.8 kbps streaming CS-data + 3.4 kbps signalling	57.6 kbps streaming CS-data + 3.4 kbps signalling	12.2 kbps speech(multimode) + 3.4 kbps signalling
>>>>>>>gainFactorInformation	Computed	Computed	Computed
>>>>>>>>referenceTFCId	0	0	0
>>>>>>TFCS 4	(TF0, TF1)	(TF3, TF0)	(TF3, TF2, TF0, TF0)
>>>>>>>ctfc	3	3	15
>>>>>>>gainFactorInformation	Computed	Computed	Computed
>>>>>>>>βc (FDD only)	N/A	N/A	N/A
>>>>>>>>βd	N/A	N/A	N/A
>>>>>>>>referenceTFCId	0	0	0
>>>>>>TFCS 5	(TF1, TF1)	(TF4, TF0)	(TF4, TF3, TF0, TF0)
>>>>>>>ctfc	4	4	22
>>>>>>>gainFactorInformation	Computed	Computed	Computed
>>>>>>>>referenceTFCId	0	0	0
>>>>>>TFCS 6	(TF2, TF1)	(TF0, TF1)	(TF5, TF4, TF1, TF0)
>>>>>>>ctfc	5	5	59
>>>>>>>gainFactorInformation	Signalled	Computed	Computed
>>>>>>>>βc (FDD only)	8	N/A	N/A
>>>>>>>>βd	15	N/A	N/A
>>>>>>>>referenceTFCId	0	0	0
>>>>>>TFCS 7		(TF1, TF1)	(TF0,TF0,TF0,TF1)
>>>>>>>ctfc		6	60
>>>>>>>gainFactorInformation		Computed	Computed
>>>>>>>>referenceTFCId		0	0
>>>>>>TFCS 8		(TF2, TF1)	(TF1,TF0,TF0,TF1)
>>>>>>>ctfc		7	61
>>>>>>>gainFactorInformation		Computed	Computed
>>>>>>>>referenceTFCId		0	0
>>>>>>TFCS 9		(TF3, TF1)	(TF2,TF1,TF0,TF1)
>>>>>>>ctfc		8	68
>>>>>>>gainFactorInformation		Computed	Computed
>>>>>>>>referenceTFCId		0	0
>>>>>>TFCS 10		(TF4, TF1)	(TF3,TF2,TF0,TF1)
>>>>>>>ctfc		9	75
>>>>>>>gainFactorInformation		Signalled	Computed
>>>>>>>>βc (FDD only)		8	N/A
>>>>>>>>βd		15	N/A
>>>>>>>>referenceTFCId		0	0
>>>>>>TFCS 11			(TF4,TF3,TF0,TF1)
>>>>>>>ctfc			82
>>>>>>>gainFactorInformation			Computed
>>>>>>>>referenceTFCId			0
>>>>>>TFCS 12			(TF5,TF4,TF1,TF1)
>>>>>>>ctfc			119
>>>>>>>gainFactorInformation			Signalled
>>>>>>>>βc (FDD only)			11
>>>>>>>>βd			15
>>>>>>>>referenceTFCId			0

Configuration	28.8 kbps streaming CS-data + 3.4 kbps signalling	57.6 kbps streaming CS-data + 3.4 kbps signalling	12.2 kbps speech(multimode) + 3.4 kbps signalling
dl-CommonTransChInfo			
>tfcs-SignallingMode	Same as UL	Same as UL	Same as UL
PhyCH INFORMATION FDD			
UL-DPCH-InfoPredef			
>ul-DPCH-PowerControlInfo			
>>powerControlAlgorithm	Algorithm 1	Algorithm 1	Algorithm 1
>>>tpcStepSize	1	1	1
>tfci-Existence	TRUE	TRUE	TRUE
>puncturingLimit	1	1	0.88
DL-CommonInformationPredef			
>dl-DPCH-InfoCommon			
>>spreadingFactor	64	32	128
>>tfci-Existence	TRUE	TRUE	FALSE
>>pilotBits	8	8	4
>>positionFixed	Flexible	Flexible	Fixed
PhyCH INFORMATION 3.84 Mcps TDD			
UL-DPCH-InfoPredef			
>ul-DPCH-PowerControlInfo			
>>dpch-ConstantValue	0	0	0
>commonTimeslotInfo			
>>secondInterleavingMode	frameRelated	frameRelated	frameRelated
>>tfci-Coding	16	16	16
>>puncturingLimit	0.44	0.48	0.88
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
DL-CommonInformationPredef			
>dl-DPCH-InfoCommon			
>>commonTimeslotInfo			
>>>secondInterleavingMode	frameRelated	frameRelated	frameRelated
>>>tfci-Coding	16	16	16
>>>puncturingLimit	0.44	0.48	0.92
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1
PhyCH INFORMATION 1.28 Mcps TDD			
UL-DPCH-InfoPredef			
>commonTimeslotInfo			
>>secondInterleavingMode	frameRelated	frameRelated	
>>tfci-Coding	16	16	
>>puncturingLimit	0.64	0.72	
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	
DL-CommonInformationPredef			
>dl-DPCH-InfoCommon			
>>commonTimeslotInfo			
>>>secondInterleavingMode	frameRelated	frameRelated	frameRelated

Configuration	28.8 kbps streaming CS-data + 3.4 kbps signalling	57.6 kbps streaming CS-data + 3.4 kbps signalling	12.2 kbps speech(multimode) + 3.4 kbps signalling
>>>tfci-Coding	16	16	16
>>>puncturingLimit	0.64	0.72	0.92
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1	repetitionPeriod1

Configuration	10.2/6.7/5.9/4.75 kbps speech + 3.4 kbps signalling	7.4/6.7/5.9/4.75 kbps speech + 3.4 kbps signalling
Ref 34.108	N/A	N/A
Default configuration identity	11	12
RB INFORMATION		
rb-Identity	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6, RB7: 7, RB8: 8	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6, RB7: 7
rlc-InfoChoice	Rlc-info	Rlc-info
>ul-RLC-Mode	RB1: UM RB2- RB3: AM RB5-RB7: TM	RB1: UM RB2- RB3: AM RB5-RB6: TM
>>transmissionRLC-DiscardMode	RB1: N/A RB2- RB3: NoDiscard RB5- RB7: N/A	RB1: N/A RB2- RB3: NoDiscard RB5- RB6: N/A
>>>maxDat	RB1: N/A RB2- RB3: 15 RB5- RB7: N/A	RB1: N/A RB2- RB3: 15 RB5- RB6: N/A
>>transmissionWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB7: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB6: N/A
>>timerRST	RB1: N/A RB2- RB3: 300 RB5- RB7: N/A	RB1: N/A RB2- RB3: 300 RB5- RB6: N/A
>>max-RST	RB1: N/A RB2- RB3: 1 RB5- RB7: N/A	RB1: N/A RB2- RB3: 1 RB5- RB6: N/A
>>pollingInfo	RB1: N/A RB2- RB3: as below RB5- RB7: N/A	RB1: N/A RB2- RB3: as below RB5- RB6: N/A
>>>lastTransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>lastRetransmissionPDU-Poll	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A RB5- RB7: FALSE	RB1- RB3: N/A RB5- RB6: FALSE
>dl-RLC-Mode	RB1: UM RB2- RB3: AM RB5- RB7: TM RB8: TM	RB1: UM RB2- RB3: AM RB5- RB6: TM RB7: TM
>>inSequenceDelivery	RB1: N/A RB2- RB3: TRUE RB5- RB8: N/A	RB1: N/A RB2- RB3: TRUE RB5- RB7: N/A
>>receivingWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB8: N/A	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB7: N/A
>>dl-RLC-StatusInfo	RB1: N/A RB2- RB3: as below RB5- RB8: N/A	RB1: N/A RB2- RB3: as below RB5- RB7: N/A
>>>timerStatusProhibit	RB2- RB3: 100	RB2- RB3: 100
>>>missingPDU-Indicator	RB2- RB3: FALSE	RB2- RB3: FALSE
>>>timerStatusPeriodic	RB2- RB3: 300	RB2- RB3: 300

>>segmentationIndication	RB1- RB3: N/A RB5- RB8: FALSE	RB1- RB3: N/A RB5- RB7: FALSE
rb-MappingInfo		
>UL-LogicalChannelMappings	OneLogicalChannel	OneLogicalChannel
>>ul-TransportChannelType	Dch	Dch
>>>transportChannelIdentity	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3,	RB1- RB3: 3 RB5: 1, RB6: 2
>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: N/A
>>rlc-SizeList	RB1- RB3: configured RB5- RB7: N/A	RB1- RB3: configured RB5- RB6: N/A
>>mac-LogicalChannelPriority	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: 5	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: 5
>DL-logicalChannelMappingList		
>>Mapping option 1	One mapping option	One mapping option
>>>dl-TransportChannelType	Dch	Dch
>>>>transportChannelIdentity	RB1- RB3: 4 RB5: 1, RB6: 2, RB7: 3, RB8: 5	RB1- RB3: 3 RB5: 1, RB6: 2, RB7:4
>>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5- RB8: N/A	RB1: 1, RB2: 2, RB3: 3 RB5- RB7: N/A
TrCH INFORMATION PER TrCH		
UL-AddReconfTransChInfoList		
>Uplink transport channel type	dch	dch
>transportChannelIdentity	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4	TrCH1: 1, TrCH2: 2, TrCH3: 3
>transportFormatSet	DedicatedTransChTFS	DedicatedTransChTFS
>>dynamicTF-information		
>>>tf0/ tf0,1	TrCH1: (0x65) TrCH2: (0x 99) TrCH3: (0x 40, 1x40) TrCH4: (0x144, 1x144)	TrCH1: (0x61) TrCH2: (0x 87) TrCH3: (0x 144, 1x144)
>>>>rlcSize	BitMode	BitMode
>>>>>sizeType	TrCH1: type 1: 65 TrCH2: type 1: 99 TrCH3: type 1: 40 TrCH4: 2: type 2, part1= 2, part2= 0 (144)	TrCH1: type 1: 61 TrCH2: type 1: 87 TrCH3: 2: type 2, part1= 2, part2= 0 (144)
>>>>numberOfTbSizeList	TrCH1-2: Zero TrCH3-4: Zero, one	TrCH1-2: Zero TrCH3: Zero, one
>>>>logicalChannelList	All	All
>>>tf 1	TrCH1: (1x39) TrCH2: (1x 53) TrCH3- TrCH4: N/A	TrCH1: (1x39) TrCH2: (1x53) TrCH3: N/A
>>>>numberOfTransportBlocks	TrCH1: One TrCH2: One	TrCH1: One TrCH2: One
>>>>rlc-Size	TrCH1-2: BitMode	TrCH1-2: BitMode
>>>>>sizeType	TrCH1: 1: 39 TrCH2: 1: 53	TrCH1: 1: 39 TrCH1: 1: 53
>>>>numberOfTbSizeList	TrCH1-2: One	TrCH1-2: One

>>>>logicalChannelList	TrCH1: all	TrCH1: all
>>>tf 2	TrCH1: (1x42) TrCH2: (1x63) TrCH3- TrCH4: N/A	TrCH1: (1x42) TrCH2: (1x63) TrCH3: N/A
>>>>numberOfTransportBlocks	TrCH1: One TrCh2: One	TrCH1: One TrCh2: One
>>>>rlc-Size	TrCH1: BitMode	TrCH1: BitMode
>>>>>sizeType	TrCH1: type 1: 42 TrCH2: type 1: 63	TrCH1: type 1: 42 TrCH2: type 1: 63
>>>>numberOfTbSizeList	TrCH1: One TrCH2: One	TrCH1: One TrCH2: One
>>>>logicalChannelList	TrCH1: all TrCH2: all	TrCH1: all TrCH2: all
>>>tf 3	TrCH1: (1x55) TrCH2: (1x76) TrCH3- TrCH4: N/A	TrCH1: (1x55) TrCH2: (1x76) TrCH3: N/A
>>>>numberOfTransportBlocks	TrCH1: One TrCh2: One	TrCH1: One TrCh2: One
>>>>rlc-Size	TrCH1: BitMode	TrCH1: BitMode
>>>>>sizeType	TrCH1: type 1: 55 TrCH2: type 1: 76	TrCH1: type 1: 55 TrCH2: type 1: 76
>>>>numberOfTbSizeList	TrCH1: One TrCH2: One	TrCH1: One TrCH2: One
>>>>logicalChannelList	TrCH1: all TrCH2: all	TrCH1: all TrCH2: all
>>>tf 4	TrCH1: (1x58) TrCH2: (1x99) TrCH3- TrCH4: N/A	TrCH1: (1x58) TrCH2: (1x87) TrCH3: N/A
>>>>numberOfTransportBlocks	TrCH1: One TrCh2: One	TrCH1: One TrCh2: One
>>>>rlc-Size	TrCH1: BitMode	TrCH1: BitMode
>>>>>sizeType	TrCH1: type 1: 58 TrCH2: type 1: 99	TrCH1: type 1: 58 TrCH2: type 1: 87
>>>>numberOfTbSizeList	TrCH1: One TrCH2: One	TrCH1: One TrCH2: One
>>>>logicalChannelList	TrCH1: all TrCH2: all	TrCH1: all TrCH2: all
>>>tf 5	TrCH1: (1x65) TrCH2- TrCH4: N/A	TrCH1: (1x61) TrCH2- TrCH4: N/A
>>>>numberOfTransportBlocks	TrCH1: One	TrCH1: One
>>>>rlc-Size	TrCH1: BitMode	TrCH1: BitMode
>>>>>sizeType	TrCH1: type 1: 42	TrCH1: type 1: 42
>>>>numberOfTbSizeList	TrCH1: One	TrCH1: One
>>>>logicalChannelList	TrCH1: all	TrCH1: all
>>semistaticTF-Information		
>>>tti	TrCH1- TrCH3: 20 TrCH4: 40	TrCH1- TrCH2: 20 TrCH3: 40
>>>channelCodingType	Convolutional	Convolutional
>>>>codingRate	TrCH1- TrCH2: Third TrCH3: Half TrCH4: Third	TrCH1- TrCH2: Third TrCH3: Third
>>>rateMatchingAttribute	TrCH1: 200 TrCH2: 190 TrCH3: 235 TrCH4: 160	TrCH1: 200 TrCH2: 190 TrCH3: 160
>>>crc-Size	TrCH1: 12 TrCH2- TrCH3: 0 TrCH4: 16	TrCH1: 12 TrCH2: 0 TrCH3: 16
DL-AddReconfTransChInfoList		
>Downlink transport channel type	dch	dch
>dl-TransportChannelIdentity		

>tfs-SignallingMode	Independent <Only tf0 on TrCH1 and tf0/tf1 on TrCH5 are different and shown below>	Independent <Only tf0 on TrCH1 and tf0/tf1 on TrCH4 are different and shown below>
>>transportFormatSet		
>>>dynamicTF-information		
>>>>tf0/ tf0,1	TrCH1: (1x0) TrCH5: (0x3, 1x3)	TrCH1: (1x0) TrCH4: (0x3, 1x3)
>>>>rlcSize	BitMode	bitMode
>>>>>sizeType	TrCH1: type 1: 0 TrCH5: type 1: 3	TrCH1: type 1: 0 TrCH4: type 1: 3
>>>>numberOfTbSizeList	TrCH1: One TrCH5: Zero, one	TrCH1: One TrCH4: Zero, one
>>>>logicalChannelList	All	All
>>>semistaticTF-Information	same as UL except for TrCH5	same as DL except for TrCH4
>>>>tti	TrCH5: 20	TrCH4: 20
>>>>channelCodingType	Convolutional	Convolutional
>>>>>codingRate	TrCH5: Third	TrCH4: Third
>>>>rateMatchingAttribute	TrCH5: 200	TrCH4: 200
>>>>crc-Size	TrCH5: 12	TrCH4: 12
>>ULTrCH-Id	TrCH1: 1, TrCH2: 2, TrCH3: 3, TrCH4: 4,	TrCH1: 1, TrCH2: 2, TrCH3: 3
>dch-QualityTarget		
>>bler-QualityValue	TrCH1: $7x10^{-3}$ TrCH2- TrCH5: Absent	TrCH1: $7x10^{-3}$ TrCH2- TrCH4: Absent
TrCH INFORMATION, COMMON		
ul-CommonTransChInfo		
>tfcs-ID (TDD only)	1	1
>sharedChannelIndicator (TDD only)	FALSE	FALSE
> tfc-Subset	Absent, not required	Absent, not required
>ul-TFCS	Normal TFCI signalling	Normal TFCI signalling
>>explicitTFCS-ConfigurationMode	Complete	Complete
>>>ctfcSize	Ctfc6Bit	Ctfc6Bit
>>>>TFCS representation	Addition	Addition
>>>>>TFC list		
>>>>>>TFC 1	(TF0, TF0, TF0, TF0)	(TF0, TF0, TF0)
>>>>>>>ctfc	0	0
>>>>>>>gainFactorInformation	Computed	Computed
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 2	(TF1, TF0, TF0, TF0)	(TF1, TF0, TF0)
>>>>>>>ctfc	1	1
>>>>>>>gainFactorInformation	Computed	Computed
>>>>>>>>βc (FDD only)	N/A	N/A
>>>>>>>>βd	N/A	N/A
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 3	(TF2, TF1, TF0, TF0)	(TF2, TF1, TF0)
>>>>>>>ctfc	8	8
>>>>>>>gainFactorInformation	Computed	Computed
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 4	(TF3, TF2, TF0, TF0)	(TF3, TF2, TF0)
>>>>>>>ctfc	15	15

>>>>>>>gainFactorInformation	Computed	Computed
>>>>>>>>βc (FDD only)		
>>>>>>>>βd		
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 5	(TF4, TF3, TF0, TF0)	(TF4, TF3, TF0)
>>>>>>>ctfc	22	22
>>>>>>>gainFactorInformation	Computed	Computed
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 6	(TF5, TF4, TF1, TF0)	(TF5, TF4, TF0)
>>>>>>>ctfc	59	29
>>>>>>>gainFactorInformation	Computed	Computed
>>>>>>>>βc (FDD only)		
>>>>>>>>βd		
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 7	(TF0, TF0, TF0, TF1)	(TF0, TF0, TF1)
>>>>>>>ctfc	60	30
>>>>>>>gainFactorInformation	Computed	Computed
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 8	(TF1, TF0, TF0, TF1)	(TF1, TF0, TF1)
>>>>>>>ctfc	61	31
>>>>>>>gainFactorInformation	computed	computed
>>>>>>>>βc (FDD only)		
>>>>>>>>βd		
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 9	(TF2, TF1, TF0, TF1)	(TF2, TF1, TF1)
>>>>>>>ctfc	68	38
>>>>>>>gainFactorInformation	computed	computed
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 10	(TF3, TF2, TF0, TF1)	(TF3, TF2, TF1)
>>>>>>>ctfc	75	45
>>>>>>>gainFactorInformation	computed	computed
>>>>>>>>βc (FDD only)		
>>>>>>>>βd		
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 11	(TF4, TF3, TF0, TF1)	(TF4, TF3, TF1)
>>>>>>>ctfc	82	52
>>>>>>>gainFactorInformation	computed	computed
>>>>>>>>referenceTFCId	0	0
>>>>>>TFC 12	(TF5, TF4, TF1, TF1)	(TF5, TF4, TF1)
>>>>>>>ctfc	97	59
>>>>>>>gainFactorInformation	signalled	signalled
>>>>>>>>βc (FDD only)	11	11
>>>>>>>>βd	15	15
>>>>>>>>referenceTFCId	0	0
> TFC subset list		
>>TFC subset 1	(speech rate 10.2)	(speech rate 7.4)

>>> Allowed transport format combination list	(TFC1, TFC2, TFC7, TFC8, TFC6, TFC12)	(TFC1, TFC2, TFC7, TFC8, TFC6, TFC12)
>>TFC subset 2	(speech rate 6.7)	(speech rate 6.7)
>>> Allowed transport format combination list	(TFC1, TFC2, TFC7, TFC8, TFC5, TFC11)	(TFC1, TFC2, TFC7, TFC8, TFC5, TFC11)
>>TFC subset 3	(speech rate 5.9)	(speech rate 5.9)
>>> Allowed transport format combination list	(TFC1, TFC2, TFC7, TFC8, TFC4, TFC10)	(TFC1, TFC2, TFC7, TFC8, TFC4, TFC10)
>>TFC subset 4	(speech rate 4.75)	(speech rate 4.75)
>>> Allowed transport format combination list	(TFC1, TFC2, TFC7, TFC8, TFC3, TFC9)	(TFC1, TFC2, TFC7, TFC8, TFC3, TFC9)
dl-CommonTransChInfo		
>tfcs-SignallingMode	Independent	Independent
ul-CommonTransChInfo		
>tfcs-ID (TDD only)	1	1
>sharedChannelIndicator (TDD only)	FALSE	FALSE
> tfc-Subset	Absent, not required	Absent, not required
>dl-TFCS	Normal TFCI signalling	Normal TFCI signalling
>>explicitTFCS-ConfigurationMode	Complete	Complete
>>>ctfcSize	Ctfc6Bit	Ctfc6Bit
>>>>TFCS representation	Addition	Addition
>>>>>TFCS list		
>>>>>>TFC 1	(TF0, TF0, TF0, TF0, TF0)	(TF0, TF0, TF0, TF0)
>>>>>>>ctfc	0	0
>>>>>>TFC 2	(TF1, TF0, TF0, TF0, TF0)	(TF1, TF0, TF0, TF0)
>>>>>>>ctfc	1	1
>>>>>>TFC 3	(TF2, TF1, TF0, TF0, TF0)	(TF2, TF1, TF0, TF0)
>>>>>>>ctfc	8	8
>>>>>>TFC 4	(TF3, TF2, TF0, TF0, TF0)	(TF3, TF2, TF0, TF0)
>>>>>>>ctfc	15	15
>>>>>>TFC 5	(TF4, TF3, TF0, TF0, TF0)	(TF4, TF3, TF0, TF0)
>>>>>>>ctfc	22	22
>>>>>>TFC 6	(TF5, TF4, TF1, TF0, TF0)	(TF5, TF4, TF0, TF0)
>>>>>>>ctfc	59	29
>>>>>>TFC 7	(TF0, TF0, TF0, TF1, TF0)	(TF0, TF0, TF1, TF0)
>>>>>>>ctfc	60	30
>>>>>>TFC 8	(TF1, TF0, TF0, TF1, TF0)	(TF1, TF0, TF1, TF0)
>>>>>>>ctfc	61	31
>>>>>>TFC 9	(TF2, TF1, TF0, TF1, TF0)	(TF2, TF1, TF1, TF0)
>>>>>>>ctfc	68	37
>>>>>>TFC 10	(TF3, TF2, TF0, TF1, TF0)	(TF3, TF2, TF1, TF0)
>>>>>>>ctfc	75	55
>>>>>>TFC 11	(TF4, TF3, TF0, TF1, TF0)	(TF4, TF3, TF1, TF0)
>>>>>>>ctfc	82	52
>>>>>>TFC 12	(TF5, TF4, TF1, TF1, TF0)	(TF5, TF4, TF1, TF0)
>>>>>>>ctfc	119	59

>>>>>>TFC 13	(TF0, TF0, TF0, TF0, TF1)	(TF0, TF0, TF0, TF1)
>>>>>>>ctfc	120	60
>>>>>>TFC 14	(TF1, TF0, TF0, TF0, TF1)	(TF1, TF0, TF0, TF1)
>>>>>>>ctfc	121	61
>>>>>>TFC 15	(TF2, TF1, TF0, TF0, TF1)	(TF2, TF1, TF0, TF1)
>>>>>>>ctfc	128	68
>>>>>>TFC 16	(TF3, TF2, TF0, TF0, TF1)	(TF3, TF2, TF0, TF1)
>>>>>>>ctfc	135	75
>>>>>>TFC 17	(TF4, TF3, TF0, TF0, TF1)	(TF4, TF3, TF0, TF1)
>>>>>>>ctfc	152	82
>>>>>>TFC 18	(TF5, TF4, TF1, TF0, TF1)	(TF5, TF4, TF0, TF1)
>>>>>>>ctfc	189	89
>>>>>>TFC 19	(TF0, TF0, TF0, TF1, TF1)	(TF0, TF0, TF1, TF1)
>>>>>>>ctfc	180	90
>>>>>>TFC 20	(TF1, TF0, TF0, TF1, TF1)	(TF1, TF0, TF1, TF1)
>>>>>>>ctfc	181	91
>>>>>>TFC 21	(TF2, TF1, TF0, TF1, TF1)	(TF2, TF1, TF1, TF1)
>>>>>>>ctfc	188	98
>>>>>>TFC 22	(TF3, TF2, TF0, TF1, TF1)	(TF3, TF2, TF1, TF1)
>>>>>>>ctfc	195	105
>>>>>>TFC 23	(TF4, TF3, TF0, TF1, TF1)	(TF4, TF3, TF1, TF1)
>>>>>>>ctfc	239	112
>>>>>>TFC 24	(TF5, TF4, TF1, TF1, TF1)	(TF5, TF4, TF1, TF1)
>>>>>>>ctfc	218	119
PhyCH INFORMATION FDD		
UL-DPCH-InfoPredef		
>ul-DPCH-PowerControlInfo		
>>powerControlAlgorithm	Algorithm 1	Algorithm 1
>>>tpcStepSize	1	1
>tfci-Existence	TRUE	TRUE
>puncturingLimit	0.88	0.88
DL-CommonInformationPredef		
>dl-DPCH-InfoCommon		
>>spreadingFactor	128	128
>>tfci-Existence	FALSE	FALSE
>>pilotBits	4	4
>>positionFixed	Fixed	Fixed
PhyCH INFORMATION 3.84 Mcps TDD		
UL-DPCH-InfoPredef		
>ul-DPCH-PowerControlInfo		
>>dpch-ConstantValue	0	0
>commonTimeslotInfo		
>>secondInterleavingMode	frameRelated	frameRelated
>>tfci-Coding	16	16
>>puncturingLimit	0.60	0.60
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1

DL-CommonInformationPredef		
>dl-DPCH-InfoCommon		
>>commonTimeslotInfo		
>>>secondInterleavingMode	frameRelated	frameRelated
>>>tfci-Coding	16	16
>>>puncturingLimit	0.60	0.60
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1
PhyCH INFORMATION 1.28 Mcps TDD		
UL-DPCH-InfoPredef		
>commonTimeslotInfo		
>>secondInterleavingMode	frame Related	frame Related
>>tfci-Coding	16	16
>>puncturingLimit	0.64	0.64
>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1
DL-CommonInformationPredef		
>dl-DPCH-InfoCommon		
>>commonTimeslotInfo		
>>>secondInterleavingMode	frame Related	frame Related
>>>tfci-Coding	16	16
>>>puncturingLimit	0.64	0.64
>>>repetitionPeriodAndLength	repetitionPeriod1	repetitionPeriod1

Configuration	12.65/8.85/6.6 kbps speech + 3.4 kbps signalling
Ref 34.108	62
Default configuration identity	13
RB INFORMATION	
rb-Identity	RB1: 1, RB2: 2, RB3: 3, RB5: 5, RB6: 6, RB8: 8
rlc-InfoChoice	Rlc-info
>ul-RLC-Mode	RB1: UM RB2- RB3: AM RB5-RB6: TM
>>transmissionRLC-DiscardMode	RB1: N/A RB2- RB3: NoDiscard RB5- RB6: N/A
>>>maxDat	RB1: N/A RB2- RB3: 15 RB5- RB6: N/A
>>transmissionWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB6: N/A
>>timerRST	RB1: N/A RB2- RB3: 300 RB5- RB6: N/A
>>max-RST	RB1: N/A RB2- RB3: 1 RB5- RB6: N/A
>>pollingInfo	RB1: N/A RB2- RB3: as below RB5- RB6: N/A
>>>lastTransmissionPDU-Poll	RB2- RB3: FALSE
>>>lastRetransmissionPDU-Poll	RB2- RB3: FALSE
>>>timerPollPeriodic	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A RB5- RB6: FALSE
>dl-RLC-Mode	RB1: UM RB2- RB3: AM RB5- RB6: TM RB8: TM
>>inSequenceDelivery	RB1: N/A RB2- RB3: TRUE RB5- RB6: N/A RB8: N/A
>>receivingWindowSize	RB1: N/A RB2- RB3: 128 for UEs with more than 10 kbyte "total RLC AM buffer size" and 32 otherwise RB5- RB6: N/A RB8: N/A
>>dl-RLC-StatusInfo	RB1: N/A RB2- RB3: as below RB5- RB6: N/A RB8: N/A
>>>timerStatusProhibit	RB2- RB3: 100
>>>missingPDU-Indicator	RB2- RB3: FALSE

>>>timerStatusPeriodic	RB2- RB3: 300
>>segmentationIndication	RB1- RB3: N/A RB5- RB6: FALSE RB8: FALSE
rb-MappingInfo	
>UL-LogicalChannelMappings	OneLogicalChannel
>>ul-TransportChannelType	Dch
>>>transportChannelIdentity	RB1- RB3: 4 RB5: 1, RB6: 2.
>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: N/A
>>rlc-SizeList	RB1- RB3: configured RB5- RB6: N/A
>>mac-LogicalChannelPriority	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: 5
>DL-logicalChannelMappingList	
>>Mapping option 1	One mapping option
>>>dl-TransportChannelType	Dch
>>>>transportChannelIdentity	RB1- RB3: 4 RB5: 1, RB6: 2, RB8: 5
>>>logicalChannelIdentity	RB1: 1, RB2: 2, RB3: 3 RB5- RB6: N/A RB8: N/A
TrCH INFORMATION PER TrCH	
UL-AddReconfTransChInfoList	
>Uplink transport channel type	dch
>transportChannelIdentity	TrCH1: 1, TrCH2: 2, TrCH4: 4
>transportFormatSet	DedicatedTransChTFS
>>dynamicTF-information	
>>>tf0/ tf0,1	TrCH1: (0x72) TrCH2: (0x 181) TrCH4: (0x144, 1x144)
>>>>rlcSize	BitMode
>>>>>sizeType	TrCH1: type 1: 72 TrCH2: type 1: 181 TrCH4: 2: type 2, part1= 2, part2= 0 (144)
>>>>numberOfTbSizeList	TrCH1-2: Zero TrCH4: Zero, one
>>>>logicalChannelList	All
>>>tf 1	TrCH1: (1x40) TrCH2: (1x 78) TrCH4: N/A
>>>>numberOfTransportBlocks	TrCH1: One TrCH2: One
>>>>rlc-Size	TrCH1-2: BitMode
>>>>>sizeType	TrCH1: 1: 40 TrCH2: 1: 78
>>>>numberOfTbSizeList	TrCH1-2: One
>>>>logicalChannelList	TrCH1: all

>>>tf 2	TrCH1: (1x54) TrCH2: (1x113) TrCH4: N/A
>>>>numberOfTransportBlocks	TrCH1: One TrCh2: One
>>>>rlc-Size	TrCH1: BitMode
>>>>>sizeType	TrCH1: type 1: 54 TrCH2: type 1: 113
>>>>numberOfTbSizeList	TrCH1: One TrCH2: One
>>>>logicalChannelList	TrCH1: all TrCH2: all
>>>tf 3	TrCH1: (1x64) TrCH2: (1x181) TrCH4: N/A
>>>>numberOfTransportBlocks	TrCH1: One TrCh2: One
>>>>rlc-Size	TrCH1: BitMode
>>>>>sizeType	TrCH1: type 1: 64 TrCH2: type 1: 181
>>>>numberOfTbSizeList	TrCH1: One TrCH2: One
>>>>logicalChannelList	TrCH1: all TrCH2: all
>>>tf 4	TrCH1: (1x72) TrCH2: N/A TrCH4: N/A
>>>>numberOfTransportBlocks	TrCH1: One
>>>>rlc-Size	TrCH1: BitMode
>>>>>sizeType	TrCH1: type 1: 72
>>>>numberOfTbSizeList	TrCH1: One
>>>>logicalChannelList	TrCH1: all
>>semistaticTF-Information	
>>>tti	TrCH1- TrCH2: 20 TrCH4: 40
>>>channelCodingType	Convolutional
>>>>codingRate	TrCH1- TrCH2: Third TrCH4: Third
>>>rateMatchingAttribute	TrCH1: 200 TrCH2: 190 TrCH4: 170
>>>crc-Size	TrCH1: 12 TrCH2: 0 TrCH4: 16
DL-AddReconfTransChInfoList	
>Downlink transport channel type	dch
>dl-TransportChannelIdentity	
>tfs-SignallingMode	Independent <Only tf0 on TrCH1 and tf0/tf1 on TrCH5 are different and shown below>
>>transportFormatSet	
>>>dynamicTF-information	
>>>>tf0/ tf0,1	TrCH1: (1x0) TrCH5: (0x3, 1x3)
>>>>rlcSize	BitMode
>>>>>sizeType	TrCH1: type 1: 0 TrCH5: type 1: 3
>>>>numberOfTbSizeList	TrCH1: One TrCH5: Zero, one

>>>>logicalChannelList	All
>>>semistaticTF-Information	same as UL except for TrCH5
>>>>tti	TrCH5: 20
>>>>channelCodingType	Convolutional
>>>>>codingRate	TrCH5: Third
>>>>rateMatchingAttribute	TrCH5: 205
>>>>crc-Size	TrCH5: 8
>>ULTrCH-Id	TrCH1: 1, TrCH2: 2, TrCH4: 4,
>dch-QualityTarget	
>>bler-QualityValue	TrCH1: $7x10^{-3}$ TrCH2: Absent TrCH4- TrCH5: Absent
TrCH INFORMATION, COMMON	
ul-CommonTransChInfo	
>tfcs-ID (TDD only)	1
>sharedChannelIndicator (TDD only)	FALSE
> tfc-Subset	Absent, not required
>ul-TFCS	Normal TFCI signalling
>>explicitTFCS-ConfigurationMode	Complete
>>>ctfcSize	Ctfc6Bit
>>>>TFCS representation	Addition
>>>>>TFC list	
>>>>>>TFC 1	(TF0, TF0, TF0)
>>>>>>>ctfc	0
>>>>>>>gainFactorInformation	Computed
>>>>>>>>referenceTFCId	0
>>>>>>TFC 2	(TF1, TF0, TF0)
>>>>>>>ctfc	1
>>>>>>>gainFactorInformation	Computed
>>>>>>>>βc (FDD only)	N/A
>>>>>>>>βd	N/A
>>>>>>>>referenceTFCId	0
>>>>>>TFC 3	(TF2, TF1, TF0)
>>>>>>>ctfc	7
>>>>>>>gainFactorInformation	Computed
>>>>>>>>referenceTFCId	0
>>>>>>TFC 4	(TF3, TF2, TF0)
>>>>>>>ctfc	13
>>>>>>>gainFactorInformation	Computed
>>>>>>>>βc (FDD only)	
>>>>>>>>βd	
>>>>>>>>referenceTFCId	0
>>>>>>TFC 5	(TF4, TF3, TF0)
>>>>>>>ctfc	19
>>>>>>>gainFactorInformation	Computed
>>>>>>>>referenceTFCId	0
>>>>>>TFC 6	(TF0, TF0, TF1)
>>>>>>>ctfc	20
>>>>>>>gainFactorInformation	Computed
>>>>>>>>βc (FDD only)	

>>>>>>>>βd	
>>>>>>>>referenceTFCId	0
>>>>>>TFC 7	(TF1, TF0, TF1)
>>>>>>>ctfc	21
>>>>>>>gainFactorInformation	Computed
>>>>>>>>referenceTFCId	0
>>>>>>TFC 8	(TF2, TF1, TF1)
>>>>>>>ctfc	27
>>>>>>>gainFactorInformation	computed
>>>>>>>>βc (FDD only)	
>>>>>>>>βd	
>>>>>>>>referenceTFCId	0
>>>>>>TFC 9	(TF3, TF2, TF1)
>>>>>>>ctfc	33
>>>>>>>gainFactorInformation	computed
>>>>>>>>referenceTFCId	0
>>>>>>TFC 10	(TF4, TF3, TF1)
>>>>>>>ctfc	39
>>>>>>>gainFactorInformation	signalled
>>>>>>>>βc (FDD only)	11
>>>>>>>>βd	15
>>>>>>>>referenceTFCId	0
> TFC subset list	
>>TFC subset 1	(speech rate 6.6)
>>> Allowed transport format combination list	(TFC1, TFC2, TFC3, TFC6, TFC7, TFC8)
>>TFC subset 2	(speech rate 8.85)
>>> Allowed transport format combination list	(TFC1, TFC2, TFC3, TFC4, TFC6, TFC7, TFC8, TFC9)
>>TFC subset 3	(speech rate 12.65)
>>> Allowed transport format combination list	(TFC1, TFC2, TFC3, TFC4, TFC5, TFC6, TFC7, TFC8, TFC9, TFC10)
dl-CommonTransChInfo	
>tfcs-SignallingMode	Independent
ul-CommonTransChInfo	
>tfcs-ID (TDD only)	1
>sharedChannelIndicator (TDD only)	FALSE
> tfc-Subset	Absent, not required
>dl-TFCS	Normal TFCI signalling
>>explicitTFCS-ConfigurationMode	Complete
>>>ctfcSize	Ctfc8Bit
>>>>TFCS representation	Addition
>>>>>TFCS list	
>>>>>>TFC 1	(TF0, TF0, TF0, TF0)
>>>>>>>ctfc	0
>>>>>>TFC 2	(TF1, TF0, TF0, TF0)
>>>>>>>ctfc	1
>>>>>>TFC 3	(TF2, TF1, TF0, TF0)
>>>>>>>ctfc	7

>>>>>>TFC 4	(TF3, TF2, TF0, TF0)
>>>>>>>ctfc	13
>>>>>>TFC 5	(TF4, TF3, TF0, TF0)
>>>>>>>ctfc	19
>>>>>>TFC 6	(TF0, TF0, TF1, TF0)
>>>>>>>ctfc	20
>>>>>>TFC 7	(TF1, TF0, TF1, TF0)
>>>>>>>ctfc	21
>>>>>>TFC 8	(TF2, TF1, TF1, TF0)
>>>>>>>ctfc	27
>>>>>>TFC 9	(TF3, TF2, TF1, TF0)
>>>>>>>ctfc	33
>>>>>>TFC 10	(TF4, TF3, TF1, TF0)
>>>>>>>ctfc	39
>>>>>>TFC 11	(TF0, TF0, TF0, TF1)
>>>>>>>ctfc	40
>>>>>>TFC 12	(TF1, TF0, TF0, TF1)
>>>>>>>ctfc	41
>>>>>>TFC 13	(TF2, TF1, TF0, TF1)
>>>>>>>ctfc	47
>>>>>>TFC 14	(TF3, TF2, TF0, TF1)
>>>>>>>ctfc	53
>>>>>>TFC 15	(TF4, TF3, TF0, TF1)
>>>>>>>ctfc	59
>>>>>>TFC 16	(TF0, TF0, TF1, TF1)
>>>>>>>ctfc	60
>>>>>>TFC 17	(TF1, TF0, TF1, TF1)
>>>>>>>ctfc	61
>>>>>>TFC 18	(TF2, TF1, TF1, TF1)
>>>>>>>ctfc	67
>>>>>>TFC 19	(TF3, TF2, TF1, TF1)
>>>>>>>ctfc	73
>>>>>>TFC 20	(TF4, TF3, TF1, TF1)
>>>>>>>ctfc	79
PhyCH INFORMATION FDD	
UL-DPCH-InfoPredef	
>ul-DPCH-PowerControlInfo	
>>powerControlAlgorithm	Algorithm 1
>>>tpcStepSize	1
>tfci-Existence	TRUE
>puncturingLimit	0.84
DL-CommonInformationPredef	
>dl-DPCH-InfoCommon	
>>spreadingFactor	128
>>tfci-Existence	FALSE

>>pilotBits	4
>>positionFixed	Fixed

========== change starts here ==========

```
-- ************************************************
--
-- HANDOVER TO UTRAN COMMAND
--
-- ************************************************


HandoverToUTRANCommand ::= CHOICE {
    r3                              SEQUENCE {
        handoverToUTRANCommand-r3       HandoverToUTRANCommand-r3-IEs,
        v4xyNonCriticalExtensions       SEQUENCE {
            handoverToUTRANCommand-v4xyext  HandoverToUTRANCommand-v4xyext-IEs,
            nonCriticalExtensions           SEQUENCE {} OPTIONAL
        }   OPTIONAL
    },
    criticalExtensions              CHOICE {
        r4                              SEQUENCE {
            handoverToUTRANCommand-r4       HandoverToUTRANCommand-r4-IEs,
            nonCriticalExtensions           SEQUENCE {}     OPTIONAL
        },
        criticalExtensions              SEQUENCE {}
        criticalExtensions              CHOICE {
            r5                              SEQUENCE {
                handoverToUTRANCommand-r5       HandoverToUTRANCommand-r5-IEs,
                nonCriticalExtensions           SEQUENCE {}     OPTIONAL
            },
            criticalExtensions              SEQUENCE {}
        }
    }
}

HandoverToUTRANCommand-r3-IEs ::= SEQUENCE {
    -- User equipment IEs
        new-U-RNTI                      U-RNTI-Short,
        -- dummy is not used in this version of specification, it should
        -- not be sent and if received it should be ignored.
        dummy                           ActivationTime                      OPTIONAL,
        cipheringAlgorithm              CipheringAlgorithm                  OPTIONAL,
    -- Radio bearer IEs
    -- Specification mode information
        specificationMode               CHOICE {
            complete                        SEQUENCE {
                srb-InformationSetupList        SRB-InformationSetupList,
                rab-InformationSetupList        RAB-InformationSetupList            OPTIONAL,
                ul-CommonTransChInfo            UL-CommonTransChInfo,
                ul-AddReconfTransChInfoList     UL-AddReconfTransChInfoList,
                dl-CommonTransChInfo            DL-CommonTransChInfo,
                dl-AddReconfTransChInfoList     DL-AddReconfTransChInfoList,
                ul-DPCH-Info                    UL-DPCH-Info,
                modeSpecificInfo                CHOICE {
                    fdd                             SEQUENCE {
                        dl-PDSCH-Information            DL-PDSCH-Information OPTIONAL,
                        cpch-SetInfo                    CPCH-SetInfo        OPTIONAL
                    },
                    tdd                             NULL
                },
                dl-CommonInformation            DL-CommonInformation,
                dl-InformationPerRL-List        DL-InformationPerRL-List,
                frequencyInfo                   FrequencyInfo
            },
            preconfiguration                SEQUENCE {
-- All IEs that include an FDD/TDD choice are split in two IEs for this message,
-- one for the FDD only elements and one for the TDD only elements, so that one
-- FDD/TDD choice in this level is sufficient.
                preConfigMode                   CHOICE {
                    predefinedConfigIdentity        PredefinedConfigIdentity,
                    defaultConfig                   SEQUENCE {
                        defaultConfigMode               DefaultConfigMode,
                        defaultConfigIdentity           DefaultConfigIdentity
                    }
                },
                rab-Info                        RAB-Info-Post       OPTIONAL,
                modeSpecificInfo                CHOICE {
                    fdd                             SEQUENCE {
                        ul-DPCH-Info                    UL-DPCH-InfoPostFDD,
                        dl-CommonInformationPost        DL-CommonInformationPost,
```

```
                        dl-InformationPerRL-List        DL-InformationPerRL-ListPostFDD,
                        frequencyInfo                   FrequencyInfoFDD
                    },
                    tdd                                 SEQUENCE {
                        ul-DPCH-Info                    UL-DPCH-InfoPostTDD,
                        dl-CommonInformationPost        DL-CommonInformationPost,
                        dl-InformationPerRL             DL-InformationPerRL-PostTDD,
                        frequencyInfo                   FrequencyInfoTDD,
                        primaryCCPCH-TX-Power           PrimaryCCPCH-TX-Power
                    }

                }
            }
        },
    -- Physical channel IEs
        maxAllowedUL-TX-Power           MaxAllowedUL-TX-Power
}

HandoverToUTRANCommand-v4xyext-IEs ::= SEQUENCE {
    -- Physical channel IEs
        -- ssdt-UL extends SSDT-Information, which is included in
        -- DL-CommonInformation. FDD only.
        ssdt-UL                             SSDT-UL-r4                          OPTIONAL,
        cell-id                             CellIdentity                        OPTIONAL
}

HandoverToUTRANCommand-r4-IEs ::= SEQUENCE {
    -- User equipment IEs
        new-U-RNTI                      U-RNTI-Short,
        cipheringAlgorithm              CipheringAlgorithm                  OPTIONAL,
    -- Radio bearer IEs
    -- Specification mode information
        specificationMode               CHOICE {
            complete                        SEQUENCE {
                srb-InformationSetupList        SRB-InformationSetupList,
                rab-InformationSetupList        RAB-InformationSetupList-r4         OPTIONAL,
                ul-CommonTransChInfo            UL-CommonTransChInfo,
                ul-AddReconfTransChInfoList     UL-AddReconfTransChInfoList,
                dl-CommonTransChInfo            DL-CommonTransChInfo,
                dl-AddReconfTransChInfoList     DL-AddReconfTransChInfoList,
                ul-DPCH-Info                    UL-DPCH-Info-r4,
                modeSpecificInfo                CHOICE {
                    fdd                             SEQUENCE {
                        dl-PDSCH-Information            DL-PDSCH-Information OPTIONAL,
                        cpch-SetInfo                    CPCH-SetInfo        OPTIONAL
                    },
                    tdd                             NULL
                },
                dl-CommonInformation            DL-CommonInformation-r4,
                dl-InformationPerRL-List        DL-InformationPerRL-List-r4,
                frequencyInfo                   FrequencyInfo
            },
            preconfiguration                SEQUENCE {
-- All IEs that include an FDD/TDD choice are split in two IEs for this message,
-- one for the FDD only elements and one for the TDD only elements, so that one
-- FDD/TDD choice in this level is sufficient.
                preConfigMode                   CHOICE {
                    predefinedConfigIdentity        PredefinedConfigIdentity,
                    defaultConfig                   SEQUENCE {
                        defaultConfigMode               DefaultConfigMode,
                        defaultConfigIdentity           DefaultConfigIdentity-r4
                    }
                },
                rab-Info                        RAB-Info-Post       OPTIONAL,
                modeSpecificInfo                CHOICE {
                    fdd                             SEQUENCE {
                        ul-DPCH-Info                    UL-DPCH-InfoPostFDD,
                        dl-CommonInformationPost        DL-CommonInformationPost,
                        dl-InformationPerRL-List        DL-InformationPerRL-ListPostFDD,
                        frequencyInfo                   FrequencyInfoFDD
                    },
                    tdd                             CHOICE {
                        tdd384                          SEQUENCE {
                            ul-DPCH-Info                    UL-DPCH-InfoPostTDD,
                            dl-InformationPerRL             DL-InformationPerRL-PostTDD,
                            frequencyInfo                   FrequencyInfoTDD,
                            primaryCCPCH-TX-Power           PrimaryCCPCH-TX-Power
                        },
```

```
                        tdd128                          SEQUENCE {
                            ul-DPCH-Info                    UL-DPCH-InfoPostTDD-LCR-r4,
                            dl-InformationPerRL             DL-InformationPerRL-PostTDD-LCR-r4,
                            frequencyInfo                   FrequencyInfoTDD,
                            primaryCCPCH-TX-Power           PrimaryCCPCH-TX-Power
                        }
                    }
                }
            }
        },
    -- Physical channel IEs
        maxAllowedUL-TX-Power           MaxAllowedUL-TX-Power
}
```

```
HandoverToUTRANCommand-r5-IEs ::= SEQUENCE {
    -- User equipment IEs
        new-U-RNTI                      U-RNTI-Short,
        cipheringAlgorithm              CipheringAlgorithm                  OPTIONAL,
    -- Radio bearer IEs
    -- Specification mode information
        specificationMode               CHOICE {
            complete                        SEQUENCE {
                srb-InformationSetupList        SRB-InformationSetupList,
                rab-InformationSetupList        RAB-InformationSetupList-r4         OPTIONAL,
                ul-CommonTransChInfo            UL-CommonTransChInfo,
                ul-AddReconfTransChInfoList     UL-AddReconfTransChInfoList,
                dl-CommonTransChInfo            DL-CommonTransChInfo,
                dl-AddReconfTransChInfoList     DL-AddReconfTransChInfoList,
                ul-DPCH-Info                    UL-DPCH-Info-r4,
                modeSpecificInfo                CHOICE {
                    fdd                             SEQUENCE {
                        dl-PDSCH-Information            DL-PDSCH-Information OPTIONAL,
                        cpch-SetInfo                    CPCH-SetInfo        OPTIONAL
                    },
                    tdd                             NULL
                },
                dl-CommonInformation            DL-CommonInformation-r4,
                dl-InformationPerRL-List        DL-InformationPerRL-List-r4,
                frequencyInfo                   FrequencyInfo
            },
            preconfiguration                SEQUENCE {
-- All IEs that include an FDD/TDD choice are split in two IEs for this message,
-- one for the FDD only elements and one for the TDD only elements, so that one
-- FDD/TDD choice in this level is sufficient.
                preConfigMode                   CHOICE {
                    predefinedConfigIdentity        PredefinedConfigIdentity,
                    defaultConfig                   SEQUENCE {
                        defaultConfigMode               DefaultConfigMode,
                        defaultConfigIdentity           DefaultConfigIdentity-r5
                    }
                },
                rab-Info                        RAB-Info-Post       OPTIONAL,
                modeSpecificInfo                CHOICE {
                    fdd                             SEQUENCE {
                        ul-DPCH-Info                    UL-DPCH-InfoPostFDD,
                        dl-CommonInformationPost        DL-CommonInformationPost,
                        dl-InformationPerRL-List        DL-InformationPerRL-ListPostFDD,
                        frequencyInfo                   FrequencyInfoFDD
                    },
                    tdd                             CHOICE {
                        tdd384                          SEQUENCE {
                            ul-DPCH-Info                    UL-DPCH-InfoPostTDD,
                            dl-InformationPerRL             DL-InformationPerRL-PostTDD,
                            frequencyInfo                   FrequencyInfoTDD,
                            primaryCCPCH-TX-Power           PrimaryCCPCH-TX-Power
                        },
                        tdd128                          SEQUENCE {
                            ul-DPCH-Info                    UL-DPCH-InfoPostTDD-LCR-r4,
                            dl-InformationPerRL             DL-InformationPerRL-PostTDD-LCR-r4,
                            frequencyInfo                   FrequencyInfoTDD,
                            primaryCCPCH-TX-Power           PrimaryCCPCH-TX-Power
                        }
                    }
                }
            }
        },
    -- Physical channel IEs
        maxAllowedUL-TX-Power           MaxAllowedUL-TX-Power
```

}

========== change ends here ==========

========== change starts here ==========

```
RadioBearerReconfiguration-r5-IEs ::= SEQUENCE {
    -- User equipment IEs
        integrityProtectionModeInfo     IntegrityProtectionModeInfo         OPTIONAL,
        cipheringModeInfo               CipheringModeInfo                   OPTIONAL,
        activationTime                  ActivationTime                      OPTIONAL,
        new-U-RNTI                      U-RNTI                              OPTIONAL,
        new-C-RNTI                      C-RNTI                              OPTIONAL,
        new-DSCH-RNTI                   DSCH-RNTI                           OPTIONAL,
        new-H-RNTI                      H-RNTI                              OPTIONAL,
        rrc-StateIndicator              RRC-StateIndicator,
        utran-DRX-CycleLengthCoeff      UTRAN-DRX-CycleLengthCoefficient    OPTIONAL,
    -- Core network IEs
        cn-InformationInfo              CN-InformationInfo                  OPTIONAL,
    -- UTRAN mobility IEs
        ura-Identity                    URA-Identity                        OPTIONAL,
    -- Specification mode information
        specificationMode               CHOICE {
            complete                        SEQUENCE {
            -- Radio bearer IEs
                rab-InformationReconfigList     RAB-InformationReconfigList         OPTIONAL,
                rb-InformationReconfigList      RB-InformationReconfigList-r5       OPTIONAL,
                rb-InformationAffectedList      RB-InformationAffectedList-r5       OPTIONAL,
                rb-PDCPContextRelocationList    RB-PDCPContextRelocationList        OPTIONAL,
            -- Transport channel IEs
                ul-CommonTransChInfo            UL-CommonTransChInfo-r4             OPTIONAL,
                ul-deletedTransChInfoList       UL-DeletedTransChInfoList           OPTIONAL,
                ul-AddReconfTransChInfoList     UL-AddReconfTransChInfoList         OPTIONAL,
                modeSpecificTransChInfo         CHOICE {
                    fdd                             SEQUENCE {
                        cpch-SetID                      CPCH-SetID                  OPTIONAL,
                        addReconfTransChDRAC-Info       DRAC-StaticInformationList  OPTIONAL
                    },
                    tdd                             NULL
                }                                                                   OPTIONAL,
                dl-CommonTransChInfo            DL-CommonTransChInfo-r4             OPTIONAL,
                dl-DeletedTransChInfoList       DL-DeletedTransChInfoList-r5            OPTIONAL,
                dl-AddReconfTransChInfoList     DL-AddReconfTransChInfoList-r5      OPTIONAL
            },
            preconfiguration                SEQUENCE {
            -- All IEs that include an FDD/TDD choice are split in two IEs for this message,
            -- one for the FDD only elements and one for the TDD only elements, so that one
            -- FDD/TDD choice in this level is sufficient.
                preConfigMode                   CHOICE {
                    predefinedConfigIdentity        PredefinedConfigIdentity,
                    defaultConfig                   SEQUENCE {
                        defaultConfigMode               DefaultConfigMode,
                        defaultConfigIdentity           DefaultConfigIdentity-r5~~4~~
                    }
                }
            }
        },
    -- Physical channel IEs
        frequencyInfo                   FrequencyInfo                       OPTIONAL,
        maxAllowedUL-TX-Power           MaxAllowedUL-TX-Power               OPTIONAL,
        ul-ChannelRequirement           UL-ChannelRequirement-r5            OPTIONAL,
        modeSpecificPhysChInfo          CHOICE {
            fdd                             SEQUENCE {
                dl-PDSCH-Information            DL-PDSCH-Information        OPTIONAL
            },
            tdd                             NULL
        },
        dl-HSPDSCH-Information          DL-HSPDSCH-Information              OPTIONAL,
        dl-CommonInformation            DL-CommonInformation-r4             OPTIONAL,
        dl-InformationPerRL-List        DL-InformationPerRL-List-r5     OPTIONAL
}
```

========== change ends here ==========

========== change starts here ==========

```
RRCConnectionSetup-r5-IEs ::= SEQUENCE {
    -- TABULAR: Integrity protection shall not be performed on this message.
        activationTime                  ActivationTime                      OPTIONAL,
        new-U-RNTI                      U-RNTI,
        new-c-RNTI                      C-RNTI                              OPTIONAL,
        rrc-StateIndicator              RRC-StateIndicator,
        utran-DRX-CycleLengthCoeff      UTRAN-DRX-CycleLengthCoefficient,
        -- TABULAR: If capabilityUpdateRequirements is not present, the default value
        -- defined in 10.3.3.2 shall be used.
        capabilityUpdateRequirement     CapabilityUpdateRequirement-r4      OPTIONAL,
    -- Specification mode information
        specificationMode               CHOICE {
            complete                        SEQUENCE {
                -- Radio bearer IEs
                srb-InformationSetupList        SRB-InformationSetupList2,
                -- Transport channel IEs
                ul-CommonTransChInfo            UL-CommonTransChInfo                OPTIONAL,
                ul-AddReconfTransChInfoList     UL-AddReconfTransChInfoList         OPTIONAL,
                dl-CommonTransChInfo            DL-CommonTransChInfo-r4             OPTIONAL,
                dl-AddReconfTransChInfoList     DL-AddReconfTransChInfoList         OPTIONAL
            },
            preconfiguration                SEQUENCE {
                -- All IEs that include an FDD/TDD choice are split in two IEs for this message,
                -- one for the FDD only elements and one for the TDD only elements, so that one
                -- FDD/TDD choice in this level is sufficient.
                preConfigMode                   CHOICE {
                    predefinedConfigIdentity        PredefinedConfigIdentity,
                    defaultConfig                   SEQUENCE {
                        defaultConfigMode               DefaultConfigMode,
                        defaultConfigIdentity           DefaultConfigIdentity-r54
                    }
                }
            }
        },
    -- Physical channel IEs
        frequencyInfo                   FrequencyInfo                       OPTIONAL,
        maxAllowedUL-TX-Power           MaxAllowedUL-TX-Power               OPTIONAL,
        ul-ChannelRequirement           UL-ChannelRequirement-r4            OPTIONAL,
        dl-CommonInformation            DL-CommonInformation-r4             OPTIONAL,
        dl-InformationPerRL-List        DL-InformationPerRL-List-r4         OPTIONAL
}
```

========== change ends here ==========

================ change starts here ================

```
DefaultConfigIdentity ::=           INTEGER (0..10)

DefaultConfigIdentity-r4 ::=        INTEGER (0..12)

DefaultConfigIdentity-r5 ::=        INTEGER (0..13)
```

================ change ends here ================