# TSG-RAN Meeting #22 Maui, USA, 09-12 December 2003

Title: 25.321 CRs to Rel-5

Source: TSG-RAN WG2

Agenda item: 7.3.5

Spec	CR	Rev	Phase	Subject	Cat	Version-Current	Version-New	Doc-2nd-Level	Workitem
25.321	179	-	Rel-5	Corrections Relating to HSDPA TB Sizes for 1.28Mcps TDD	F	5.6.0	5.7.0	R2-032619	HSDPA-L23
25.321	180	-	Rel-5	HSDPA Transport block size table for 3.84Mcps TDD	F	5.6.0	5.7.0	R2-032620	HSDPA-L23
25.321	181	-	Rel-5	HSDPA TB size table	F	5.6.0	5.7.0	R2-032621	HSDPA-L23
25.321	182	-	Rel-5	Unwarranted HARQ re-transmissions	F	5.6.0	5.7.0	R2-032622	HSDPA-L23
25.321	183	-	Rel-5	MAC-hs Re-ordering Protocol Flushing correction	F	5.6.0	5.7.0	R2-032623	HSDPA-L23
25.321	184	-	Rel-5	Correction to window based stall avoidance mechanism	F	5.6.0	5.7.0	R2-032624	HSDPA-L23

# 3GPP TSG-RAN WG2 Meeting #39 San-Diego, USA, 17<sup>th</sup> – 21<sup>st</sup> November 2003

# R2-032619

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Other comments: %

- 1) Fill out the above form. The symbols above marked **#** contain pop-up help information about the field that they are closest to.
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under <u>ftp://ftp.3gpp.org/specs/</u> For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.
- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

## 9.2.3.3 Transport block size for 1.28 Mcps TDD

The mapping of transport block size, in bits, to TFRI value is dependent upon the UE's HS-DSCH capability class.

If k is the signalled TFRI value then the corresponding HS-DSCH transport block size  $L_k$  is given by: If k = 1..62

$$L_k = \lfloor L_{\min} p^{k-1} \rfloor$$

where

 $p = \frac{1340}{1269}$  for the 1.4 Mbps user classif the HS-DSCH physical layer category is between 1 and 6 inclusively,  $p = \frac{1755}{1652}$  for the 2.0 Mbps user class if the HS-DSCH physical layer category is between 7 and 12 inclusively,  $p = \frac{2345}{1652}$  for the 2.8 Mbps user class if the HS-DSCH physical layer category is between 12 and 12

$$p = \frac{2545}{2196}$$
 for the 2.8 Mbps user class if the HS-DSCH physical layer category is between 13 and 15 inclusively,

and

$$L_{\min} = 240$$

If k = 63 then,

 $L_k = 7016$  for the 1.4 Mbps user class if the HS-DSCH physical layer category is between 1 and 6 inclusively,

10204 for the 2.0 Mbps user class if the HS-DSCH physical layer category is between 7 and 12 inclusively,

14056 for the 2.8 Mbps user class if the HS-DSCH physical layer category is between 13 and 15 inclusively.

If k=0,  $L_k$  indicates NULL and shall not be used to signal a transport block size in the TFRI.

Transport block sizes calculated by this formulae shall equal the values indicated in the following tables: -

			category [	<u>1,6]</u>			
<u>TB index (k)</u>	<u>TB size</u> [bits]						
<u>0</u>	<u>NULL</u>	<u>16</u>	<u>543</u>	<u>32</u>	<u>1297</u>	<u>48</u>	<u>3100</u>
<u>1</u>	<u>240</u>	<u>17</u>	<u>573</u>	<u>33</u>	<u>1370</u>	<u>49</u>	<u>3274</u>
<u>2</u>	<u>253</u>	<u>18</u>	<u>605</u>	<u>34</u>	<u>1446</u>	<u>50</u>	<u>3457</u>
<u>3</u>	<u>267</u>	<u>19</u>	<u>639</u>	<u>35</u>	<u>1527</u>	<u>51</u>	<u>3650</u>
<u>4</u>	<u>282</u>	<u>20</u>	<u>675</u>	<u>36</u>	<u>1613</u>	<u>52</u>	<u>3854</u>
<u>5</u>	<u>298</u>	<u>21</u>	<u>712</u>	<u>37</u>	<u>1703</u>	<u>53</u>	<u>4070</u>
<u>6</u>	<u>315</u>	<u>22</u>	<u>752</u>	<u>38</u>	<u>1798</u>	<u>54</u>	<u>4298</u>
<u>7</u>	<u>332</u>	<u>23</u>	<u>794</u>	<u>39</u>	<u>1899</u>	<u>55</u>	<u>4538</u>
<u>8</u>	<u>351</u>	<u>24</u>	<u>839</u>	<u>40</u>	<u>2005</u>	<u>56</u>	<u>4792</u>
<u>9</u>	<u>370</u>	<u>25</u>	<u>886</u>	<u>41</u>	<u>2118</u>	<u>57</u>	<u>5060</u>
<u>10</u>	<u>391</u>	<u>26</u>	<u>936</u>	<u>42</u>	<u>2236</u>	<u>58</u>	<u>5344</u>
<u>11</u>	<u>413</u>	<u>27</u>	<u>988</u>	<u>43</u>	<u>2361</u>	<u>59</u>	<u>5643</u>
<u>12</u>	<u>436</u>	<u>28</u>	<u>1043</u>	<u>44</u>	<u>2493</u>	<u>60</u>	<u>5958</u>

Table 9.2.3.3.1: HSDPA Transport Block Sizes for 1.28 Mcps TDD, for HS-DSCH physical layer

<u>13</u>	<u>461</u>	<u>29</u>	<u>1102</u>	<u>45</u>	<u>2633</u>	<u>61</u>	<u>6292</u>
<u>14</u>	<u>487</u>	<u>30</u>	<u>1163</u>	<u>46</u>	<u>2780</u>	<u>62</u>	<u>6644</u>
<u>15</u>	<u>514</u>	<u>31</u>	<u>1228</u>	<u>47</u>	<u>2936</u>	<u>63</u>	<u>7016</u>

# Table 9.2.3.3.2: HSDPA Transport Block Sizes for 1.28 Mcps TDD, for HS-DSCH physical layer category [7,12]

<u>TB index (k)</u>	<u>TB size</u> [bits]	TB index (k)	TB size [bits]	TB index (k)	<u>TB size</u> [bits]	TB index (k)	<u>TB size</u> [bits]
<u>0</u>	NULL	<u>16</u>	<u>594</u>	<u>32</u>	1564	<u>48</u>	<u>4118</u>
<u>1</u>	<u>240</u>	<u>17</u>	<u>631</u>	<u>33</u>	<u>1662</u>	<u>49</u>	<u>4375</u>
2	<u>254</u>	<u>18</u>	<u>671</u>	<u>34</u>	<u>1766</u>	<u>50</u>	<u>4648</u>
<u>3</u>	<u>270</u>	<u>19</u>	<u>712</u>	<u>35</u>	<u>1876</u>	<u>51</u>	<u>4938</u>
<u>4</u>	<u>287</u>	<u>20</u>	<u>757</u>	<u>36</u>	<u>1993</u>	<u>52</u>	<u>5246</u>
<u>5</u>	<u>305</u>	<u>21</u>	<u>804</u>	<u>37</u>	<u>2117</u>	<u>53</u>	<u>5573</u>
<u>6</u>	<u>324</u>	<u>22</u>	<u>854</u>	<u>38</u>	<u>2249</u>	<u>54</u>	<u>5920</u>
<u>7</u>	<u>344</u>	<u>23</u>	<u>908</u>	<u>39</u>	<u>2389</u>	<u>55</u>	<u>6289</u>
<u>8</u>	<u>366</u>	<u>24</u>	<u>964</u>	<u>40</u>	<u>2538</u>	<u>56</u>	<u>6681</u>
<u>9</u>	<u>389</u>	<u>25</u>	<u>1024</u>	<u>41</u>	<u>2697</u>	<u>57</u>	<u>7098</u>
<u>10</u>	<u>413</u>	<u>26</u>	<u>1088</u>	<u>42</u>	<u>2865</u>	<u>58</u>	<u>7541</u>
<u>11</u>	<u>439</u>	<u>27</u>	<u>1156</u>	<u>43</u>	<u>3043</u>	<u>59</u>	<u>8011</u>
<u>12</u>	<u>466</u>	<u>28</u>	<u>1228</u>	<u>44</u>	<u>3233</u>	<u>60</u>	<u>8510</u>
<u>13</u>	<u>495</u>	<u>29</u>	<u>1305</u>	<u>45</u>	<u>3435</u>	<u>61</u>	<u>9041</u>
<u>14</u>	<u>526</u>	<u>30</u>	<u>1386</u>	<u>46</u>	<u>3649</u>	<u>62</u>	<u>9605</u>
<u>15</u>	<u>559</u>	<u>31</u>	<u>1473</u>	<u>47</u>	<u>3877</u>	<u>63</u>	<u>10204</u>

# Table 9.2.3.3.3 : HSDPA Transport Block Sizes for 1.28 Mcps TDD, for HS-DSCH physical layer category [13,15]

<u>TB index (k)</u>	TB size	<u>TB index (k)</u>	TB size	TB index (k)	TB size	TB index (k)	TB size
	[bits]		[bits]		[bits]		[bits]
<u>0</u>	<u>NULL</u>	<u>16</u>	<u>642</u>	<u>32</u>	<u>1836</u>	<u>48</u>	<u>5250</u>
<u>1</u>	<u>240</u>	<u>17</u>	<u>686</u>	<u>33</u>	<u>1961</u>	<u>49</u>	<u>5606</u>
<u>2</u>	<u>256</u>	<u>18</u>	<u>732</u>	<u>34</u>	<u>2094</u>	<u>50</u>	<u>5987</u>
<u>3</u>	<u>273</u>	<u>19</u>	<u>782</u>	<u>35</u>	<u>2236</u>	<u>51</u>	<u>6393</u>
<u>4</u>	<u>292</u>	<u>20</u>	<u>835</u>	<u>36</u>	<u>2388</u>	<u>52</u>	<u>6827</u>
<u>5</u>	<u>312</u>	<u>21</u>	<u>892</u>	<u>37</u>	<u>2550</u>	<u>53</u>	<u>7290</u>
<u>6</u>	<u>333</u>	<u>22</u>	<u>952</u>	<u>38</u>	<u>2723</u>	<u>54</u>	<u>7785</u>
<u>7</u>	<u>355</u>	<u>23</u>	<u>1017</u>	<u>39</u>	<u>2908</u>	<u>55</u>	<u>8313</u>
<u>8</u>	<u>380</u>	<u>24</u>	<u>1086</u>	<u>40</u>	<u>3105</u>	<u>56</u>	<u>8877</u>
<u>9</u>	<u>405</u>	<u>25</u>	<u>1160</u>	<u>41</u>	<u>3316</u>	<u>57</u>	<u>9479</u>
<u>10</u>	<u>433</u>	<u>26</u>	<u>1238</u>	<u>42</u>	<u>3541</u>	<u>58</u>	<u>10123</u>
<u>11</u>	<u>462</u>	<u>27</u>	<u>1322</u>	<u>43</u>	<u>3781</u>	<u>59</u>	<u>10809</u>
<u>12</u>	<u>494</u>	<u>28</u>	<u>1412</u>	<u>44</u>	<u>4037</u>	<u>60</u>	<u>11543</u>
<u>13</u>	<u>527</u>	<u>29</u>	<u>1508</u>	<u>45</u>	<u>4311</u>	<u>61</u>	<u>12326</u>
<u>14</u>	<u>563</u>	<u>30</u>	<u>1610</u>	<u>46</u>	<u>4604</u>	<u>62</u>	<u>13162</u>
<u>15</u>	<u>601</u>	<u>31</u>	<u>1719</u>	<u>47</u>	<u>4916</u>	<u>63</u>	<u>14056</u>

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Title:	ж	HSDPA	Franspo	ort block size t	able for 3	3.84M	lcps	TDD			
Source:	ж	RAN WG	2								
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Reason for change: %	For consistency with FDD and for the same reasons that tabular listing of HSDPA transport block sizes were introduced for FDD, tabular representation of block sizes are added for 3.84 Mcps TDD.
Summary of change: #	In section 9.2.3.2 tables of HSDPA transport block sizes are added.
Consequences if <b>#</b>	Reference block sizes will not be available to enable confirmation of the correct
not approved:	operation of the HSDPA transport block formulae.
Clauses affected: #	9.2.3.2
	YN

		Υ	Ν			
Other specs	ж		Χ	Other core specifications	B	
affected:			Χ	Test specifications		
			Χ	O&M Specifications		
Other comments:	ж					

- 1) Fill out the above form. The symbols above marked **#** contain pop-up help information about the field that they are closest to.
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under <u>ftp://ftp.3gpp.org/specs/</u> For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.

3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

# 9.2.3.2 Transport block size for 3.84 Mcps TDD

Let k be the signalled TFRI value, then the corresponding HS-DSCH transport block size  $L_k$  is given by :

If *k*=1..510

$$L_{k} = \lfloor L_{\min} p^{k} \rfloor$$
$$p = \frac{8313}{8192}$$
$$L_{\min} = 57$$

If k = 511

 $L_k = 102000$ 

If $k=0$ , $L_k$ indicates NULL	and chall not be used	to signal a transport blog	ok cize in the TERI
If $k=0, L_k$ multicates NULL	and shan not be used	to signal a transport blow	K SIZC III UIC TTIKI.

Transport block sizes calculated by this formula shall equal the values indicated in Table 9.2.3.2.1

|--|

TB index	TB size	TB index	TB size	TB index	TB size	TB index	TB size
<u>(k)</u>	[bits]	<u>(k)</u>	[bits]	<u>(k)</u>	[bits]	<u>(k)</u>	[bits]
<u>0</u>	NULL	<u>128</u>	<u>372</u>	<u>256</u>	<u>2432</u>	<u>384</u>	<u>15890</u>
<u>1</u>	<u>57</u>	<u>129</u>	<u>377</u>	<u>257</u>	<u>2468</u>	<u>385</u>	<u>16124</u>
<u>2</u>	<u>58</u>	<u>130</u>	<u>383</u>	<u>258</u>	<u>2504</u>	<u>386</u>	<u>16362</u>
<u>3</u>	<u>59</u>	<u>131</u>	<u>389</u>	<u>259</u>	<u>2541</u>	<u>387</u>	<u>16604</u>
<u>4</u>	<u>60</u>	<u>132</u>	<u>394</u>	<u>260</u>	<u>2579</u>	<u>388</u>	<u>16849</u>
<u>5</u>	<u>61</u>	<u>133</u>	<u>400</u>	<u>261</u>	<u>2617</u>	<u>389</u>	<u>17098</u>
<u>6</u>	<u>62</u>	<u>134</u>	<u>406</u>	<u>262</u>	<u>2656</u>	<u>390</u>	<u>17351</u>
<u>7</u>	<u>63</u>	<u>135</u>	<u>412</u>	<u>263</u>	<u>2695</u>	<u>391</u>	<u>17607</u>
<u>8</u>	<u>64</u>	<u>136</u>	<u>418</u>	<u>264</u>	<u>2735</u>	<u>392</u>	<u>17867</u>
<u>9</u>	<u>65</u>	<u>137</u>	<u>424</u>	<u>265</u>	<u>2775</u>	<u>393</u>	<u>18131</u>
<u>10</u>	<u>66</u>	<u>138</u>	<u>431</u>	<u>266</u>	<u>2816</u>	<u>394</u>	<u>18399</u>
<u>11</u>	<u>66</u>	<u>139</u>	<u>437</u>	<u>267</u>	<u>2858</u>	<u>395</u>	<u>18671</u>
<u>12</u>	<u>67</u>	<u>140</u>	<u>443</u>	<u>268</u>	<u>2900</u>	<u>396</u>	<u>18946</u>
<u>13</u>	<u>68</u>	<u>141</u>	<u>450</u>	<u>269</u>	<u>2943</u>	<u>397</u>	<u>19226</u>
<u>14</u>	<u>69</u>	<u>142</u>	<u>457</u>	<u>270</u>	<u>2986</u>	<u>398</u>	<u>19510</u>
<u>15</u>	<u>71</u>	<u>143</u>	<u>463</u>	<u>271</u>	<u>3030</u>	<u>399</u>	<u>19798</u>
<u>16</u>	<u>72</u>	<u>144</u>	<u>470</u>	<u>272</u>	<u>3075</u>	<u>400</u>	<u>20091</u>
<u>17</u>	<u>73</u>	<u>145</u>	<u>477</u>	<u>273</u>	<u>3121</u>	<u>401</u>	<u>20388</u>
<u>18</u>	<u>74</u>	<u>146</u>	<u>484</u>	<u>274</u>	<u>3167</u>	<u>402</u>	<u>20689</u>
<u>19</u>	<u>75</u>	<u>147</u>	<u>491</u>	<u>275</u>	<u>3213</u>	<u>403</u>	<u>20994</u>
<u>20</u>	<u>76</u>	<u>148</u>	<u>499</u>	<u>276</u>	<u>3261</u>	<u>404</u>	<u>21304</u>
<u>21</u>	<u>77</u>	<u>149</u>	<u>506</u>	<u>277</u>	<u>3309</u>	<u>405</u>	<u>21619</u>
<u>22</u>	<u>78</u>	<u>150</u>	<u>514</u>	<u>278</u>	<u>3358</u>	<u>406</u>	<u>21938</u>
<u>23</u>	<u>79</u>	<u>151</u>	<u>521</u>	<u>279</u>	<u>3408</u>	<u>407</u>	<u>22263</u>
<u>24</u>	<u>81</u>	<u>152</u>	<u>529</u>	<u>280</u>	<u>3458</u>	<u>408</u>	<u>22591</u>
<u>25</u>	<u>82</u>	<u>153</u>	<u>537</u>	<u>281</u>	<u>3509</u>	<u>409</u>	<u>22925</u>
<u>26</u>	<u>83</u>	<u>154</u>	<u>545</u>	<u>282</u>	<u>3561</u>	<u>410</u>	<u>23264</u>
<u>27</u>	<u>84</u>	<u>155</u>	<u>553</u>	<u>283</u>	<u>3613</u>	<u>411</u>	<u>23607</u>

28	<u>85</u>	<u>156</u>	<u>561</u>	284	3667	412	23956
<u>29</u>	87	157	569	285	<u>3721</u>	413	24310
30	88	158	578	286	3776	414	24669
<u>31</u>	<u>89</u>	<u>159</u>	<u>586</u>	287	<u>3832</u>	415	25033
<u>32</u>	<u>91</u>	<u>160</u>	<u>595</u>	288	<u>3888</u>	416	25403
33	<u>92</u>	<u>160</u> 161	<u>604</u>	289	<u>3946</u>	<u>417</u>	25778
<u>34</u>	<u>93</u>	<u>161</u> 162	<u>613</u>	<u>200</u>	<u>4004</u>	<u>418</u>	<u>26159</u>
<u>35</u>	<u>95</u>	<u>162</u>	<u>622</u>	<u>200</u> 291	4063	<u>419</u>	<u>26545</u>
<u>36</u>	<u>96</u>	<u>165</u> 164	<u>631</u>	292	4123	420	26938
<u>37</u>	<u>98</u>	<u>164</u> <u>165</u>	<u>640</u>	<u>293</u>	4184	421	27335
<u>38</u>	<u>99</u>	<u>166</u>	<u>650</u>	<u>200</u> 294	<u>4246</u>	422	27739
<u>39</u>	<u> </u>	<u>160</u> 167	<u>659</u>	<u>294</u> 295	4309	423	28149
<u>40</u>	<u>100</u> 102	<u>167</u> 168	<u>669</u>	<u>296</u>	4372	424	28565
<u>40</u> 41	<u>102</u> 103	<u>169</u>	<u>679</u>	<u>290</u> 297	4437	425	<u>28987</u>
41 42	<u>105</u> 105	<u>109</u> 170	<u>689</u>			425	
<u>42</u> <u>43</u>	<u>105</u> 107	<u>170</u> 171	<u>699</u>	<u>298</u> 299	<u>4502</u> 4569	<u>420</u> 427	<u>29415</u> <u>29849</u>
<u>44</u> 45	<u>108</u>	<u>172</u>	<u>709</u>	<u>300</u>	<u>4636</u>	<u>428</u>	<u>30290</u>
	<u>110</u>	<u>173</u>	720 720	<u>301</u>	<u>4705</u>	<u>429</u>	<u>30738</u>
<u>46</u>	<u>111</u>	<u>174</u>	<u>730</u>	<u>302</u>	<u>4774</u>	<u>430</u>	<u>31192</u>
<u>47</u>	<u>113</u>	<u>175</u>	<u>741</u>	<u>303</u>	<u>4845</u>	<u>431</u>	<u>31652</u>
<u>48</u>	<u>115</u>	<u>176</u>	<u>752</u>	<u>304</u>	<u>4916</u>	<u>432</u>	<u>32120</u>
<u>49</u>	<u>116</u>	<u>177</u>	<u>763</u>	<u>305</u>	<u>4989</u>	<u>433</u>	<u>32594</u>
<u>50</u>	<u>118</u>	<u>178</u>	<u>775</u>	<u>306</u>	<u>5063</u>	<u>434</u>	<u>33076</u>
<u>51</u>	<u>120</u>	<u>179</u>	<u>786</u>	<u>307</u>	<u>5138</u>	<u>435</u>	<u>33564</u>
<u>52</u>	<u>122</u>	<u>180</u>	<u>798</u>	<u>308</u>	<u>5213</u>	<u>436</u>	<u>34060</u>
<u>53</u> 54	<u>123</u> 125	<u>181</u>	<u>809</u> 821	<u>309</u>	<u>5290</u>	<u>437</u>	<u>34563</u>
		<u>182</u>		<u>310</u>	<u>5369</u>	<u>438</u>	<u>35074</u>
<u>55</u>	<u>127</u>	<u>183</u>	<u>834</u>	<u>311</u>	<u>5448</u>	<u>439</u>	<u>35592</u>
<u>56</u>	<u>129</u>	<u>184</u>	<u>846</u>	<u>312</u>	<u>5528</u>	<u>440</u>	<u>36117</u>
<u>57</u>	<u>131</u>	<u>185</u>	<u>858</u>	<u>313</u>	<u>5610</u>	<u>441</u>	<u>36651</u>
<u>58</u>	<u>133</u>	<u>186</u>	<u>871</u>	<u>314</u>	<u>5693</u>	<u>442</u>	<u>37192</u>
<u>59</u>	<u>135</u>	<u>187</u>	<u>884</u>	<u>315</u>	<u>5777</u>	<u>443</u>	<u>37742</u>
<u>60</u>	<u>137</u>	<u>188</u>	<u>897</u>	<u>316</u>	<u>5862</u>	<u>444</u>	<u>38299</u>
<u>61</u>	<u>139</u>	<u>189</u>	<u>910</u>	<u>317</u>	<u>5949</u>	<u>445</u>	<u>38865</u>
<u>62</u>	<u>141</u>	<u>190</u>	<u>924</u>	<u>318</u>	<u>6037</u>	<u>446</u>	<u>39439</u>
<u>63</u>	<u>143</u>	<u>191</u>	<u>937</u>	<u>319</u>	<u>6126</u>	<u>447</u>	<u>40021</u>
<u>64</u>	<u>145</u>	<u>192</u>	<u>951</u>	<u>320</u>	<u>6217</u>	<u>448</u>	<u>40613</u>
<u>65</u>	<u>147</u>	<u>193</u>	<u>965</u>	<u>321</u>	<u>6308</u>	<u>449</u>	<u>41212</u>
<u>66</u>	<u>150</u>	<u>194</u>	<u>980</u>	<u>322</u>	<u>6402</u>	<u>450</u>	<u>41821</u>
<u>67</u>	<u>152</u>	<u>195</u>	<u>994</u>	<u>323</u>	<u>6496</u>	<u>451</u>	<u>42439</u>
<u>68</u>	<u>154</u>	<u>196</u>	<u>1009</u>	<u>324</u>	<u>6592</u>	<u>452</u>	<u>43066</u>
<u>69</u> 70	<u>156</u>	<u>197</u>	<u>1024</u>	<u>325</u>	<u>6689</u>	<u>453</u>	<u>43702</u>
<u>70</u>	<u>159</u>	<u>198</u>	<u>1039</u>	<u>326</u>	<u>6788</u>	<u>454</u>	<u>44347</u>
<u>71</u>	<u>161</u>	<u>199</u>	<u>1054</u>	<u>327</u>	<u>6889</u>	<u>455</u>	<u>45002</u>
<u>72</u>	<u>163</u>	<u>200</u>	<u>1070</u>	<u>328</u>	<u>6990</u>	<u>456</u>	<u>45667</u>
<u>73</u>	<u>166</u>	<u>201</u>	<u>1085</u>	<u>329</u>	<u>7094</u>	<u>457</u>	<u>46342</u>
<u>74</u>	<u>168</u>	<u>202</u>	<u>1101</u>	<u>330</u>	<u>7198</u>	<u>458</u>	47026
<u>75</u>	<u>171</u>	<u>203</u>	<u>1118</u>	<u>331</u>	<u>7305</u>	<u>459</u>	<u>47721</u>
<u>76</u>	<u>173</u>	<u>204</u>	<u>1134</u>	<u>332</u>	<u>7413</u>	<u>460</u>	<u>48426</u>
<u>77</u>	<u>176</u>	<u>205</u>	<u>1151</u>	<u>333</u>	<u>7522</u>	<u>461</u>	<u>49141</u>

<u>78</u>	<u>178</u>	<u>206</u>	<u>1168</u>	<u>334</u>	<u>7633</u>	<u>462</u>	<u>49867</u>
<u>79</u>	<u>181</u>	<u>207</u>	<u>1185</u>	<u>335</u>	<u>7746</u>	<u>463</u>	<u>50603</u>
<u>80</u>	<u>184</u>	<u>208</u>	<u>1203</u>	<u>336</u>	<u>7860</u>	<u>464</u>	<u>51351</u>
<u>81</u>	<u>186</u>	<u>209</u>	<u>1221</u>	<u>337</u>	<u>7976</u>	<u>465</u>	<u>52109</u>
<u>82</u>	<u>189</u>	<u>210</u>	<u>1239</u>	<u>338</u>	<u>8094</u>	<u>466</u>	<u>52879</u>
<u>83</u>	<u>192</u>	<u>211</u>	<u>1257</u>	<u>339</u>	<u>8214</u>	<u>467</u>	<u>53660</u>
<u>84</u>	<u>195</u>	<u>212</u>	<u>1276</u>	<u>340</u>	<u>8335</u>	<u>468</u>	<u>54453</u>
<u>85</u>	<u>198</u>	<u>213</u>	<u>1294</u>	<u>341</u>	<u>8458</u>	<u>469</u>	<u>55257</u>
<u>86</u>	<u>201</u>	<u>214</u>	<u>1313</u>	<u>342</u>	<u>8583</u>	<u>470</u>	<u>56073</u>
<u>87</u>	<u>204</u>	<u>215</u>	<u>1333</u>	<u>343</u>	<u>8710</u>	<u>471</u>	<u>56901</u>
<u>88</u>	<u>207</u>	<u>216</u>	<u>1353</u>	<u>344</u>	<u>8839</u>	<u>472</u>	<u>57742</u>
<u>89</u>	<u>210</u>	<u>217</u>	<u>1373</u>	<u>345</u>	<u>8969</u>	<u>473</u>	<u>58595</u>
<u>90</u>	<u>213</u>	<u>218</u>	<u>1393</u>	<u>346</u>	<u>9102</u>	<u>474</u>	<u>59460</u>
<u>91</u>	<u>216</u>	<u>219</u>	<u>1413</u>	<u>347</u>	<u>9236</u>	<u>475</u>	<u>60338</u>
<u>92</u>	<u>219</u>	<u>220</u>	<u>1434</u>	<u>348</u>	<u>9373</u>	<u>476</u>	<u>61230</u>
<u>93</u>	<u>222</u>	<u>221</u>	<u>1456</u>	<u>349</u>	<u>9511</u>	<u>477</u>	<u>62134</u>
<u>94</u>	<u>226</u>	<u>222</u>	<u>1477</u>	<u>350</u>	<u>9652</u>	<u>478</u>	<u>63052</u>
<u>95</u>	<u>229</u>	<u>223</u>	<u>1499</u>	<u>351</u>	<u>9794</u>	<u>479</u>	<u>63983</u>
<u>96</u>	232	224	1521	352	<u>9939</u>	480	64928
<u>97</u>	<u>236</u>	225	<u>1543</u>	<u>353</u>	10086	<u>481</u>	65887
<u>98</u>	<u>239</u>	226	<u>1566</u>	<u>354</u>	10235	<u>482</u>	66860
99	243	227	1589	355	10386	483	67848
100	246	228	1613	356	10539	484	68850
101	250	229	1637	357	10695	485	69867
102	254	230	1661	358	10853	486	70899
103	258	231	1685	359	11013	487	71946
104	261	232	1710	360	11176	488	73009
105	265	233	1736	361	11341	489	74087
106	269	234	1761	362	11508	490	75182
107	273	235	1787	363	11678	491	76292
108	277	236	1814	364	11851	492	77419
109	281	237	1840	365	12026	493	78563
110	285	238	1868	366	12204	494	79723
111	<u>290</u>	239	1895	367	12384	495	80901
112	<u>200</u> 294	240	1923	368	12567	<u>496</u>	82095
113	2 <u>98</u>	<u>240</u> 241	<u>1920</u> 1952	369	12752	<u>497</u>	83308
114	<u>200</u> 303	242	<u>1981</u>	<u>370</u>	12941	498	84539
115	<u>307</u>	243	<u>2010</u>	<u>371</u>	13132	<u>499</u>	<u>85787</u>
116	<u>312</u>	244	2039	372	13326	<u>+35</u> 500	87054
117	<u>316</u>	<u>244</u> <u>245</u>	2039	373	13523	<u>500</u> 501	88340
118	<u>321</u>	245	2100	374	13722	<u>501</u> 502	<u>89645</u>
	326						
<u>119</u>		<u>247</u> 248	<u>2131</u> 2163	<u>375</u> 376	<u>13925</u>	<u>503</u>	<u>90969</u>
<u>120</u>	<u>331</u>	<u>248</u>	<u>2163</u>	<u>376</u>	<u>14131</u>	<u>504</u>	<u>92313</u>
<u>121</u>	<u>336</u>	<u>249</u> 250	<u>2195</u>	<u>377</u>	<u>14340</u>	<u>505</u>	<u>93676</u>
<u>122</u>	<u>340</u>	<u>250</u>	<u>2227</u>	<u>378</u>	<u>14551</u>	<u>506</u>	<u>95060</u>
<u>123</u>	<u>346</u>	<u>251</u>	<u>2260</u>	<u>379</u>	<u>14766</u>	<u>507</u>	<u>96464</u>
<u>124</u>	<u>351</u>	<u>252</u>	<u>2293</u>	<u>380</u>	<u>14984</u>	<u>508</u>	<u>97889</u>
<u>125</u>	<u>356</u>	<u>253</u>	<u>2327</u>	<u>381</u>	<u>15206</u>	<u>509</u>	<u>99335</u>
<u>126</u>	<u>361</u>	<u>254</u>	<u>2362</u>	<u>382</u>	<u>15430</u>	<u>510</u>	<u>10080</u>
<u>127</u>	<u>366</u>	<u>255</u>	<u>2397</u>	<u>383</u>	<u>15658</u>	<u>511</u>	<u>10200</u>

CHANGE REQUEST									CR-Form-v7		
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Proposed chang	ie a	offects: (	JICC a	pps <b>#</b>	ME	<mark>X</mark> Ra	adio	Access Netwo	rk 🗙	Core Ne	etwork
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Reason for change: # The formula based scheme is open to finite accuracy and rounding errors. Note also that part Summary of change: # Made the formula informative and introduced the normative table in an Annex. Consequences if # Errors in the computation may result in costly hardware changes for not approved: manufacturers. Clauses affected: **%** 9.2.3.1, Annex A Ν Other specs ж X Other core specifications ж affected: X Test specifications X O&M Specifications **%** The formula after the first "else" in 9.2.3.1: Other comments:  $L(i,k_i) = \lfloor L_{\min} p^{k_{0,i}+k_i} \rfloor$ p = 2085/2048  $L_{\rm min} = 296$  $k_{0,i}^{\text{man}} = \text{from Table 9.2.3.1}$  $k_i = 0,...,63$ Was erroneously deleted in the version 5.6.0. It is now proposed to change it into:  $L(k_t) = L_{\min} p^{k_t}$ p = 2085 / 2048 $L_{\rm min} = 296$ 

(the previous formula has been "added and deleted" with change bars, so that people can see what happened).

#### How to create CRs using this form:

- 1) Fill out the above form. The symbols above marked **#** contain pop-up help information about the field that they are closest to.
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under <u>ftp://ftp.3gpp.org/specs/</u> For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.
- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

# 9.2.3.1 Transport block size for FDD

For all transmissions of a transport block, the transport block size is derived from the TFRI value as specified below, except only in those cases of retransmissions where the Node-B selects a combination for which no mapping exists between the original transport block size and the selected combination of channelisation Code set and modulation type. In such cases, the transport block size index value signalled to the UE shall be set to 111111, i.e.,  $k_i$ =63.

Let  $k_i$  be the TFRI signalled on the HS-SCCH value and let  $k_{0,i}$  be the value in the table 9.2.3.1 corresponding to the modulation and the number of codes signalled on the HS-SCCH. Let  $k_t$  be the sum of the two values:  $k_t = k_i + k_{0,i}$ . The transport block size  $L(k_i)$  can be obtained by accessing the position  $k_t$  in the table in Annex A (normative) or by using the formula below (informative):

For each combination of channelization code set and modulation scheme i = 0..31, a set of  $k_i = 0..62$  transport block sizes  $L(i, k_i)$  is given by:

If <u>k<sub>t</sub> i = 0 and k<sub>r</sub> < 4039</u>

$$\frac{L(i,k_i) = 137 + 12k_i}{k_i = 0,...,38} \quad L(k_t) = 125 + 12 \cdot k_t$$

else

 $\begin{array}{l} L(i,k_i) = \left[ L_{\min} p^{k_{0,i}+k_i} \right] \\ p = 2085/2048 \\ \hline L_{\min} = 296 \\ k_{0,i} = \text{from Table 9.2.3.1} \\ k_i = 0, \dots, 63 \end{array} \begin{array}{l} L(k_t) = \left[ L_{\min} p^{k_t} \right] \\ p = 2085/2048 \\ \hline L_{\min} = 296 \end{array}$ 

end

The 'if' statement above is true only for a single channelization code using QPSK modulation. The index  $k_i$  of the transport block size  $L(i, k_i)$  corresponds to the 6 bit transport block size index signalled on the HS-SCCH. The index *i* corresponds to the combination of channelization code set and modulation scheme as defined in Table 9.2.3.1.

Combination <i>i</i>	Modulation scheme	Number of channelization codes	<i>k</i> <sub>0,<i>i</i></sub>
0	QPSK	1	1
1		2	40
2		3	63
3		4	79
4		5	92
5		6	102
6		7	111
7		8	118
8		9	125
9		10	131
10		11	136
11		12	141
12		13	145
13		14	150
14	]	15	153
15	16QAM	1	40
16	]	2	79
17		3	102

Table 9.2.3.1: Values of *k*<sub>0,i</sub> for different numbers of channelization codes and modulation schemes

18	4	118
19	5	131
20	6	141
21	7	150
22	8	157
23	9	164
24	10	169
25	11	175
26	12	180
27	13	184
28	14	188
29	15	192

# <u>Annex A (normative):</u> <u>HS-DSCH Transport Block Size Table for FDD</u>

The following table provides the mapping between  $k_t$  (as per the definition in section 9.2.3.1) and the HS-DSCH Transport Block Size (L( $k_t$ )):

Index	TB Size	Index	TB Size	Index	TB Size
<u>1</u>	<u>137</u>	<u>86</u>	<u>1380</u>	<u>171</u>	<u>6324</u>
<u>2</u>	<u>149</u>	<u>87</u>	<u>1405</u>	<u>172</u>	<u>6438</u>
1 2 3 4 5 6 7 8 9	<u>161</u>	<u>88</u>	<u>1430</u>	<u>173</u>	<u>6554</u>
<u>4</u>	<u>173</u>	<u>89</u>	<u>1456</u>	<u>174</u>	<u>6673</u>
<u>5</u>	<u>185</u>	<u>90</u>	<u>1483</u>	<u>175</u>	<u>6793</u>
<u>6</u>	<u>197</u>	<u>91</u>	<u>1509</u>	<u>176</u>	<u>6916</u>
<u>7</u>	<u>209</u>	<u>92</u>	<u>1537</u>	<u>177</u>	<u>7041</u>
<u>8</u>	<u>221</u>	<u>93</u>	<u>1564</u>	<u>178</u>	<u>7168</u>
	<u>233</u>	<u>94</u>	<u>1593</u>	<u>179</u>	<u>7298</u>
<u>10</u>	<u>245</u>	<u>95</u>	<u>1621</u>	<u>180</u>	<u>7430</u>
<u>11</u>	<u>257</u>	<u>96</u>	<u>1651</u>	<u>181</u>	<u>7564</u>
<u>12</u>	<u>269</u>	<u>97</u>	<u>1681</u>	<u>182</u>	<u>7700</u>
<u>13</u>	<u>281</u>	<u>98</u>	<u>1711</u>	<u>183</u>	<u>7840</u>
<u>14</u>	<u>293</u>	<u>99</u>	<u>1742</u>	<u>184</u>	<u>7981</u>
<u>15</u>	<u>305</u>	<u>100</u>	<u>1773</u>	<u>185</u>	<u>8125</u>
<u>16</u>	<u>317</u>	<u>101</u>	<u>1805</u>	<u>186</u>	<u>8272</u>
<u>17</u>	<u>329</u>	<u>102</u>	<u>1838</u>	<u>187</u>	<u>8422</u>
<u>18</u>	<u>341</u>	<u>103</u>	<u>1871</u>	<u>188</u>	<u>8574</u>
<u>19</u>	<u>353</u>	<u>104</u>	<u>1905</u>	<u>189</u>	<u>8729</u>
<u>20</u>	<u>365</u>	<u>105</u>	<u>1939</u>	<u>190</u>	<u>8886</u>
<u>21</u>	<u>377</u>	<u>106</u>	<u>1974</u>	<u>191</u>	<u>9047</u>
<u>22</u>	<u>389</u>	<u>107</u>	<u>2010</u>	<u>192</u>	<u>9210</u>
<u>23</u>	<u>401</u>	<u>108</u>	<u>2046</u>	<u>193</u>	<u>9377</u>
<u>24</u>	<u>413</u>	<u>109</u>	<u>2083</u>	<u>194</u>	<u>9546</u>
<u>25</u>	<u>425</u>	<u>110</u>	<u>2121</u>	<u>195</u>	<u>9719</u>
<u>26</u>	<u>437</u>	<u>111</u>	<u>2159</u>	<u>196</u>	<u>9894</u>
<u>27</u>	<u>449</u>	<u>112</u>	<u>2198</u>	<u>197</u>	<u>10073</u>
<u>28</u>	<u>461</u>	<u>113</u>	<u>2238</u>	<u>198</u>	<u>10255</u>
<u>29</u>	<u>473</u>	<u>114</u>	<u>2279</u>	<u>199</u>	<u>10440</u>
<u>30</u>	<u>485</u>	<u>115</u>	<u>2320</u>	<u>200</u>	<u>10629</u>

<u>31</u>	<u>497</u>	<u>116</u>	<u>2362</u>	<u>201</u>	<u>10821</u>
32	509	117	2404	202	11017
33	521	118	2448	203	11216
<u>34</u>	<u>533</u>	<u>119</u>	2492	204	11418
<u>35</u>	<u>545</u>	<u>120</u>	<u>2537</u>	<u>205</u>	<u>11625</u>
<u>36</u>	<u>557</u>	<u>121</u>	<u>2583</u>	<u>206</u>	<u>11835</u>
<u>37</u>	<u>569</u>	<u>122</u>	<u>2630</u>	<u>207</u>	<u>12048</u>
<u>38</u>	<u>581</u>	<u>123</u>	<u>2677</u>	<u>208</u>	12266
39	593	124	2726	209	12488
40	<u>605</u>	125	2775	210	12713
<u>41</u>	<u>616</u>	<u>126</u>	<u>2825</u>	<u>211</u>	<u>12943</u>
<u>42</u>	<u>627</u>	<u>127</u>	<u>2876</u>	<u>212</u>	<u>13177</u>
<u>43</u>	<u>639</u>	<u>128</u>	<u>2928</u>	<u>213</u>	<u>13415</u>
<u>44</u>	<u>650</u>	<u>129</u>	<u>2981</u>	<u>214</u>	<u>13657</u>
<u>45</u>	<u>662</u>	<u>130</u>	<u>3035</u>	<u>215</u>	13904
46	674	131	3090	216	14155
47	<u>686</u>	132	<u>3145</u>	217	14411
<u>48</u>	<u>699</u>	<u>133</u>	<u>3202</u>	<u>218</u>	<u>14671</u>
<u>49</u>	<u>711</u>	<u>134</u>	<u>3260</u>	<u>219</u>	<u>14936</u>
<u>50</u>	<u>724</u>	<u>135</u>	<u>3319</u>	<u>220</u>	<u>15206</u>
<u>51</u>	737	136	<u>3379</u>	221	<u>15481</u>
<u>52</u>	751	137	3440	222	15761
<u>53</u>	764	138	3502	223	16045
<u>54</u>	<u>778</u>	<u>139</u>	<u>3565</u>	<u>224</u>	<u>16335</u>
<u>55</u>	<u>792</u>	<u>140</u>	<u>3630</u>	<u>225</u>	<u>16630</u>
<u>56</u>	<u>806</u>	<u>141</u>	<u>3695</u>	<u>226</u>	<u>16931</u>
<u>57</u>	<u>821</u>	<u>142</u>	<u>3762</u>	<u>227</u>	<u>17237</u>
<u>58</u>	836	143	<u>3830</u>	228	<u>17548</u>
<u>59</u>	851	144	3899	229	17865
60	866	145	3970	230	18188
<u>61</u>	882	<u>146</u>	4042	<u>231</u>	18517
<u>62</u>	<u>898</u>	<u>147</u>	<u>4115</u>	<u>232</u>	<u>18851</u>
<u>63</u>	<u>914</u>	<u>148</u>	<u>4189</u>	<u>233</u>	<u>19192</u>
<u>64</u>	<u>931</u>	<u>149</u>	<u>4265</u>	<u>234</u>	<u>19538</u>
<u>65</u>	<u>947</u>	<u>150</u>	<u>4342</u>	<u>235</u>	<u>19891</u>
<u>66</u>	<u>964</u>	<u>151</u>	4420	236	20251
67	982	152	4500	237	20617
68	1000	153	4581	238	20989
	1018	<u>154</u>	4664		21368
<u>69</u>				<u>239</u>	
<u>70</u>	<u>1036</u>	<u>155</u>	<u>4748</u>	<u>240</u>	<u>21754</u>
<u>71</u>	<u>1055</u>	<u>156</u>	<u>4834</u>	<u>241</u>	<u>22147</u>
<u>72</u>	<u>1074</u>	<u>157</u>	<u>4921</u>	<u>242</u>	<u>22548</u>
<u>73</u>	1093	<u>158</u>	5010	<u>243</u>	<u>22955</u>
74	1113	159	5101	244	23370
75	1133	160	5193	245	23792
<u>76</u>	<u>1154</u>	<u>160</u>	<u>5287</u>	246	24222
77	<u>1175</u>	<u>162</u>	<u>5382</u>	<u>247</u>	<u>24659</u>
<u>78</u>	<u>1196</u>	<u>163</u>	<u>5480</u>	<u>248</u>	<u>25105</u>
<u>79</u>	<u>1217</u>	<u>164</u>	<u>5579</u>	<u>249</u>	<u>25558</u>
<u>80</u>	<u>1239</u>	<u>165</u>	<u>5680</u>	<u>250</u>	<u>26020</u>
81	1262	166	5782	251	26490
82	1285	167	5887	252	26969
83	1308	<u>167</u>	<u>5993</u>	253	27456
<u>84</u>	<u>1331</u>	<u>169</u>	<u>6101</u>	<u>254</u>	<u>27952</u>
<u>85</u>	<u>1356</u>	<u>170</u>	<u>6211</u>		

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						_						
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		be found in	3GPP	<u>R 21.900</u> .					Rel-5	· ·	ease 5)	
									Rel-6	(Rel	ease 6)	

Reason for change: %					
	de-coding is completed.				
	It is not stated what the HARQ process will do if a MAC-hs PDU is intended for a				
	HARQ process which does not exist				
Summary of change: #	We specify that the UE will discard data meant for a MAC-hs process that does				
cannary or onanger or	not exist.				
	We specify that if a transmission is received within 5TTI of the previous one, then				
	the UE should discard it.				
0	. 1177 Second a second of the second				
-	UE implementations will have to handle cases which are not useful and will				
not approved:	therefore be made more complex without reason.				
Clauses affected: %	11.6.2.1, 11.6.2.2				
	11.0.2.1, 11.0.2.2				
	YN				
Other specs %	Other core specifications %				
affected:	Test specifications				
	O&M Specifications				
Other comments: #					

- 1) Fill out the above form. The symbols above marked # contain pop-up help information about the field that they are closest to.
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under <u>ftp://ftp.3gpp.org/specs/</u> For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.
- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

#### 11.6.2.1 HARQ Entity

-----There is one HARQ entity at the UE which processes the HARQ process identifiers in-received on the HS-SCCH transmissions associated with MAC-hs PDUs received on the HS-DSCH. [NOTE TO EDITOR: CHANGE IN FORMAT]

A number of parallel HARQ processes are used in the UE to support the HARQ entity. The number of HARQ processes is configured by upper layers:

- Each received MAC-hs PDU shall be allocated to the HARQ process indicated by the HARQ process identifier of the MAC-hs PDU.

<u>If there is no HARQ process corresponding to the indicated HARQ process identifier of the MAC hs PDU, the</u> <u>PDU shall be discarded.</u>

### 11.6.2.2 HARQ process

A number of parallel HARQ processes are used in the UE to support the HARQ protocol. The number of HARQ processes is configured by upper layers.

The HARQ process processes the New Data Indicator indicated by lower layers for each received MAC-hs PDU.

#### The UE may:

- for FDD, if the MAC-hs PDU is received within 5 sub-frames from the reception of the previous MAC-hs PDU intended for this HARQ process; or
- for TDD, if the MAC-hs PDU is received before generation of feedback resulting from reception of a previous MAC-hs PDU for the same H-ARQ process:
  - discard the MAC-hs PDU.

The UE shall:

- if the New Data Indicator has been incremented compared to the value in the previous received transmission in this HARQ process or this is the first received transmission in the HARQ process:
  - replace the data currently in the soft buffer for this HARQ process with the received data.
  - if the Transport Block Size index value is equal to 111111 (FDD only):
    - generate a positive acknowledgement (ACK) of the data in this HARQ process;
    - discard the received data;
    - assume that the data has been successfully decoded.
- if the New Data Indicator is identical to the value used in the previous received transmission in the HARQ process:
  - if the Transport Block Size index value is equal to 111111 (FDD only):
    - assume that the transport block size is identical to the last valid transport block size signalled for this HARQ process.
  - if the data has not yet been successfully decoded:
    - combine the received data with the data currently in the soft buffer for this HARQ process.
- if the data in the soft buffer has been successfully decoded and no error was detected:
  - deliver the decoded MAC-hs PDU to the reordering entity;
  - generate a positive acknowledgement (ACK) of the data in this HARQ process.
- else:

- generate a negative acknowledgement (NAK) of the data in this HARQ process;
- schedule the generated positive or negative acknowledgement for transmission and the time of transmission relative to the reception of data in a HARQ process is configured by upper layer.

The HARQ process processes the Queue ID in the received MAC-hs PDUs. The UE shall:

- arrange the received MAC-hs PDUs in queues based on the Queue ID.

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Proposed change affects: UICC apps ME X Radio Access Network Core Network											
Title:	ж	MAC-hs F	Re-orderi	ng Protocol	Flushing	corre	ctior	1			
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Work item code	:Ж	HSDPA-L	.23					Date: ೫	Sep	otember 2	003
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Reason for change: %	During RAN2#37, CR174R1 (R2-032038) was approved. This CR introduced the possibility for the UE to flush part of its re-ordering buffer in case of memory shortage. The introduced description was however incomplete.
Summary of change: #	The description of the flushing is completed.
Consequences if % not approved:	The description of the flushing remains incomplete.

Clauses affected:	<b>%</b> 11.6.2.3.2 <b>Y N</b>	
Other specs affected:	#     X       Other core specifications     #       X     Test specifications       X     O&M Specifications	
Other comments:	ж	

- 1) Fill out the above form. The symbols above marked **#** contain pop-up help information about the field that they are closest to.
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3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

#### 11.6.2.3.2 Reordering functionality

If no timer T1 is active:

- the timer T1 shall be started when a MAC-hs PDU with TSN > next\_expected\_TSN is correctly received.
- T1\_TSN shall be set to the TSN of this MAC-hs PDU.

If a timer T1 is already active:

- no additional timer shall be started, i.e. only one timer T1 may be active at a given time.

The timer T1 shall be stopped if:

- the MAC-hs PDU with TSN = T1\_TSN can be delivered to the disassembly entity before the timer expires.

When the timer T1expires and T1\_TSN > next\_expected\_TSN:

- all correctly received MAC-hs PDUs with TSN > next\_expected\_TSN up to and including T1\_TSN-1 shall be delivered to the disassembly entity;
- all correctly received MAC-hs PDUs up to the next not received MAC-hs PDU shall be delivered to the disassembly entity.
- next\_expected\_TSN shall be set to the TSN of the next not received MAC-hs PDU.

When the timer T1 is stopped or expires, and there still exist some received MAC-hs PDUs that can not be delivered to higher layer:

- timer T1 is started
- set T1\_TSN to the highest TSN among those of the MAC-hs PDUs that can not be delivered.

#### **Transmitter operation:**

After the transmitter has transmitted a MAC-hs PDU with TSN=SN, any MAC-hs PDU with TSN  $\leq$  SN – TRANSMIT\_WINDOW\_SIZE should not be retransmitted to avoid sequence number ambiguity in the receiver.

#### **Receiver operation:**

When a MAC-hs PDU with TSN = SN is received:

- If SN is within the receiver window:
  - if SN < next\_expected\_TSN, or this MAC-hs PDU has previously been received:
    - the MAC-hs PDU shall be discarded.
    - else:
  - the MAC-hs PDU is placed in the reordering buffer at the place indicated by the TSN.
- If SN is outside the receiver window:
  - the received MAC-hs PDU shall be placed above the highest received TSN in the reordering buffer, at the position indicated by SN;
  - RcvWindow\_UpperEdge shall be set to SN thus advancing the receiver window;
  - any MAC-hs PDUs with TSN ≤ RcvWindow\_UpperEdge RECEIVE\_WINDOW\_SIZE, i.e. outside the receiver window after its position is updated, shall be removed from the reordering buffer and be delivered to the disassembly entity;

- next\_expected\_TSN shall be set to RcvWindow\_UpperEdge RECEIVE\_WINDOW\_SIZE + 1;
- All received MAC-hs PDUs with consecutive TSNs from next\_expected\_TSN (included) up to the first not received MAC-hs PDU are delivered to the disassembly entity.
- next\_expected\_TSN shall be advanced to the TSN of this first not received MAC-hs PDU.

In case a UE has insufficient memory to process a received MAC-hs PDU, it shall perform the following set of operations:

- select TSN\_flush such that: next\_expected\_TSN < TSN\_flush  $\leq$  RcvWindow\_UpperEdge + 1;
- deliver all correctly received MAC-hs PDUs with TSN < <u>next\_expected\_TSN\_flush</u> to the disassembly entity;
- if the MAC-hs PDU with TSN=TSN\_flush has previously been received:
  - deliver all received MAC-hs PDUs with consecutive TSNs from TSN\_flush (included) up to the first not received MAC-hs PDU to the disassembly entity;
  - advance next\_expected\_TSN to the TSN of this first not received MAC-hs PDU.

- else

\_\_\_\_\_\_set next\_expected\_TSN to TSN\_flush. [comment: changed indentation]

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For <b>HELP</b> on using this form, see bottom of this page or look at the pop-up text over the <b>#</b> symbols.								symbols.		
Proposed change affects:       UICC apps%       ME X Radio Access Network       Core Network										
Title:	ж	Correctio	n to wir	ndow based s	tall avoid	ance	mech	nanism		
Source:	ж	RAN WG	2							
Work item code:	ж	HSDPA-L	.23					Date: Ж	Nov 2003	
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Reason for change: Ж	Current receiver operation with window based stall avoidance mechanism is incomplete. Protocol errors may occur if next_expected_TSN is unconditionally set to the lower edge of the receiver window after the receiver window is updated.				
Summary of change: #	<ol> <li>A new condition is added so that the next_expected_TSN is set to the lower edge of the receiver window only when the next_expected_TSN goes below the receiver window.</li> <li>A clarifying condition is added so that the delivery of the MAC-hs PDUs whose TSNs are larger than or equal to the next_expected_TSN is activated when the MAC-hs PDU with TSN = next_expecte_TSN is already stored in the reordering buffer.</li> </ol>				
	Isolated Impact Analysis				
	The change has an isolated impact since only the UE behavior of the window based stall avoidance mechanism is affected. The change can be seen as a correction where the specification was incomplete.				
Consequences if % not approved:	<ol> <li>Correctly and sequentially received MAC-hs PDU may be stored in the reordering buffer not being delivered to disassembly entity. It can increase transmission delay.</li> <li>UE can't figure out whether the received MAC-hs PDU has valid TSN. It can cause protocol errors.</li> </ol>				
Clauses affected: %	11.6.2.3				

Clauses affected: % 11.6.2.3

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Other specs affected:	XOther core specificationsXTest specificationsXO&M Specifications	¥
Other comments:	¥	

- 1) Fill out the above form. The symbols above marked **#** contain pop-up help information about the field that they are closest to.
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- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

## 11.6.2.3 Reordering entity

### 11.6.2.3.1 Definitions

In the functions described in this section the following definitions apply:

#### Parameters

- Transmitter window size (TRANSMIT\_WINDOW\_SIZE) TRANSMIT\_WINDOW\_SIZE is the size of the transmitter window according to the definition below. This is a parameter in the Node B and the value of the parameter is configured by higher layers.
- Receiver window size (RECEIVE\_WINDOW\_SIZE) RECEIVE\_WINDOW\_SIZE is the size of the receiver window according to the definition below. This is a parameter in the UE and the value of the parameter is configured by higher layers.

#### State variables

- next\_expected\_TSN:

The next\_expected\_TSN is the Transmission sequence number (TSN) following the TSN of the last in-sequence MAC-hs PDU received. It shall be updated upon the <u>delivery</u>receipt to the disassembly entity of the MAC-hs PDU with TSN equal to next\_expected\_TSN. The initial value of next\_expected\_TSN =0.

- RcvWindow\_UpperEdge:

The RcvWindow\_UpperEdge represents the TSN, which is at the upper edge of the receiver window. After the first MAC-hs PDU has been received successfully, it also corresponds to the MAC-hs PDU with the highest TSN of all received MAC-hs PDUs. The initial RcvWindow\_UpperEdge equals 63. RcvWindow\_UpperEdge is updated based on the reception of new payloadsMAC-hs PDU according to the procedure given below.

T1\_TSN:

The TSN of the latest MAC-hs PDU that cannot be delivered to the disassembly entity, when the timer T1 is started.

#### Timers

- Re-ordering release timer (T1):

The Re-ordering release timer T1 controls the stall avoidance in the UE reordering buffer as described below. The value of T1 is configured by upper layers.

#### Other definitions

- Receiver window:

The receiver window defines TSNs of those MAC-hs PDUs that can be received in the receiver without causing an advancement of the receiver window according to the procedure below. The size of the receiver window equals RECEIVE\_WINDOW\_SIZE and spans TSNs going from RcvWindow\_UpperEdge – RECEIVE\_WINDOW\_SIZE + 1 to RcvWindow\_UpperEdge included.

#### 11.6.2.3.2 Reordering functionality

If no timer T1 is active:

- the timer T1 shall be started when a MAC-hs PDU with TSN > next\_expected\_TSN is correctly received.
- T1\_TSN shall be set to the TSN of this MAC-hs PDU.

If a timer T1 is already active:

- no additional timer shall be started, i.e. only one timer T1 may be active at a given time.

The timer T1 shall be stopped if:

- the MAC-hs PDU with TSN = T1\_TSN can be delivered to the disassembly entity before the timer expires.

When the timer T1expires and T1\_TSN > next\_expected\_TSN:

- all correctly received MAC-hs PDUs with TSN > next\_expected\_TSN up to and including T1\_TSN-1 shall be delivered to the disassembly entity;
- all correctly received MAC-hs PDUs up to the next not received MAC-hs PDU shall be delivered to the disassembly entity.
- next\_expected\_TSN shall be set to the TSN of the next not received MAC-hs PDU.

When the timer T1 is stopped or expires, and there still exist some received MAC-hs PDUs that can not be delivered to higher layer:

- timer T1 is started
- set T1\_TSN to the highest TSN among those of the MAC-hs PDUs that can not be delivered.

#### **Transmitter operation:**

After the transmitter has transmitted a MAC-hs PDU with TSN=SN, any MAC-hs PDU with TSN  $\leq$  SN – TRANSMIT\_WINDOW\_SIZE should not be retransmitted to avoid sequence number ambiguity in the receiver.

#### **Receiver operation:**

When a MAC-hs PDU with TSN = SN is received:

- **<u>Lif</u>** SN is within the receiver window:
  - if SN < next\_expected\_TSN, -or this MAC-hs PDU has previously been received:
    - the MAC-hs PDU shall be discarded;-
  - else: [Indentation changed to B2]
    - the MAC-hs PDU isshall be placed in the reordering buffer at the place indicated by the TSN;
       [Indentation changed to B3]
- **L**if SN is outside the receiver window:
  - the received MAC-hs PDU shall be placed above the highest received TSN in the reordering buffer, at the position indicated by SN;
  - RcvWindow\_UpperEdge shall be set to SN thus advancing the receiver window;
  - any MAC-hs PDUs with TSN ≤ RcvWindow\_UpperEdge RECEIVE\_WINDOW\_SIZE, i.e. outside the
    receiver window after its position is updated, shall be removed from the reordering buffer and be delivered to
    the disassembly entity;
  - if next\_expected\_TSN is below the updated receiver window:
    - next\_expected\_TSN shall be set to RcvWindow\_UpperEdge RECEIVE\_WINDOW\_SIZE + 1;
       [Indentation changed to B3]
- if the MAC-hs PDU with TSN = next expected TSN is stored in the reordering buffer:
  - Aall received MAC-hs PDUs with consecutive TSNs from next\_expected\_TSN (included) up to the first not received MAC-hs PDU areshall be delivered to the disassembly entity:- Indentation changed to B2]
  - next\_expected\_TSN shall be advanced -to the TSN of this first not received MAC-hs PDU. [Indentation changed to B2]

In case a UE has insufficient memory to process a received MAC-hs PDU, it shall perform the following set of operations:

- select TSN\_flush such that: next\_expected\_TSN < TSN\_flush  $\leq$  RcvWindow\_UpperEdge + 1;
- deliver all correctly received MAC-hs PDUs with TSN < next\_expected\_TSN to the disassembly entity;
- set next\_expected\_TSN to TSN\_flush.