TSG RAN Meeting #20 Hämeenlinna, Finland, 3 - 6 June, 2003

RP-030207

Title CRs (R'99 and Rel-4/Rel-5/Rel-6 Category A) to TS 25.101

Source TSG RAN WG4

Agenda Item 7.4.3

RAN4 Tdoc	Spec	CR	R	Cat	Rel	Curr Ver	Title	Work Item
R4-020623	25.101	235	1	F	R99	3.13.0	Problems with "Out of sync" in Initial convergence test	TEI
R4-020624	25.101	236	1	Α	Rel-4	4.7.0	Problems with "Out of sync" in Initial convergence test	TEI
R4-020625	25.101	237	1	Α	Rel-5	5.6.0	Problems with "Out of sync" in Initial convergence test	TEI
R4-020626	25.101	238	1	Α	Rel-6	6.0.0	Problems with "Out of sync" in Initial convergence test	TEI
R4-020580	25.101	240	1	F	R99	3.13.0	Correction of SSDT performance requirements	TEI
R4-020581	25.101	241	1	Α	Rel-4	4.7.0	Correction of SSDT performance requirements	TEI
R4-020582	25.101	242	1	Α	Rel-5	5.6.0	Correction of SSDT performance requirements	TEI
R4-020583	25.101	243	1	Α	Rel-6	6.0.0	Correction of SSDT performance requirements	TEI

3GPP TSG RAN WG4 (Radio) Meeting #27

R4-030623

Paris, France 19 - 23 May, 2003

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How to create CRs using this form:

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8.8.2 Power control in the downlink, initial convergence

This requirement verifies that DL power control works properly during the first seconds after DPCH connection is established

8.8.2.1 Minimum requirements

For the parameters specified in Table 8.31 the downlink DPCH_Ec/Ior power ratio measured values, which are averaged over 50 ms, shall be within the range specified in Table 8.32 more than 90% of the time. T1 equals to 500 ms and it starts 10 ms after the DPDCH connection is initiated. T2 equals to 500 ms and it starts when T1 has expired. Power control is ON during the test.

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Parameter Unit Test 1 Test 2 Test 3 Test 4 Target quality value **BLFR** 0.01 0.01 0.1 0.1 on DTCH Initial DPCH_Ec/lor dB -5.9 -25.9-18 -3 -22.1-18 Information Data 12.2 kbps 12.2 64 64 Rate \hat{I}_{or}/I_{oc} dΒ -1 dBm/3.84 I_{oc} -60 MHz Propagation condition Static Maximum DL Power dB Minimum DL Power dB -18 **DL Power Control** dΒ 1 step size, Δ_{TPC} Limited Power "Not used" Increase

Table 8.31: Test parameters for downlink power control

Table 8.32: Requirements in downlink power control

Parameter	Unit	Test 1 and Test 2	Test 3 and Test 4
$\frac{DPCH \ _E_c}{I_{or}}$ during T1	dB	-18.9 ≤ DPCH_Ec/lor ≤ -11.9	-15.1 ≤ DPCH_Ec/lor ≤ -8.1
$\frac{DPCH \ _E_c}{I_{or}}$ during T2	dB	-18.9 ≤ DPCH_Ec/lor ≤ -14.9	-15.1 ≤ DPCH_Ec/lor ≤ -11.1

3GPP TSG RAN WG4 (Radio) Meeting #27

R4-030624

Paris, France 19 - 23 May, 2003

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8.8.2 Power control in the downlink, initial convergence

This requirement verifies that DL power control works properly during the first seconds after DPCH connection is established.

8.8.2.1 Minimum requirements

For the parameters specified in Table 8.31 the downlink DPCH_Ec/Ior power ratio measured values, which are averaged over 50 ms, shall be within the range specified in Table 8.32 more than 90% of the time. T1 equals to 500 ms and it starts 10 ms after the DPDCH connection is initiated. T2 equals to 500 ms and it starts when T1 has expired. Power control is ON during the test.

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3GPP TSG RAN WG4 (Radio) Meeting #27

R4-030625

Paris, France 19 - 23 May, 2003

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Limited Power

Increase

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Parameter Unit Test 1 Test 2 Test 3 Test 4 Target quality value **BLER** 0.01 0.01 0.1 0.1 on DTCH Initial DPCH_Ec/lor DB -25.9-18 -3 -22.1-18 -5.9 Information Data **Kbps** 12.2 12.2 64 64 Rate \hat{I}_{or}/I_{oc} DB dBm/3.84 -60 I_{oc} MHz Propagation condition Static Maximum_DL_Power DB Minimum_DL_Power DB -18 **DL Power Control** DB 1 step size, Δ_{TPC}

Table 8.31: Test parameters for downlink power control

Table 8.32: Requirements in downlink power control

"Not used"

Parameter	Unit	Test 1 and Test 2	Test 3 and Test 4
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3GPP TSG RAN WG4 (Radio) Meeting #27

R4-030626

Paris, France 19 - 23 May, 2003

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3GPP TSG RAN WG4 (Radio) Meeting #27 030580

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Paris, France 19 - 23 May, 2003

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8.6.3 Demodulation of DCH in Site Selection Diversity Transmission Power Control mode

The bit error characteristics of UE receiver is determined in Site Selection Diversity Transmission power control (SSDT) mode. Two Node B emulators are required for this performance test. The delay profiles of signals received from different Node Bs are assumed to be the same but time shifted by 10 chip periods (2604 ns).

8.6.3.1 Minimum requirements

The downlink physical channels and their relative power to Ior are the same as those specified in clause C.3.2 irrespective of Node Bs and the test cases. DPCH_Ec/Ior value applies whenever DPDCH in the cell is transmitted. In Test 1 and Test 3, the received powers at UE from two Node Bs are the same, while 3dB offset is given to one that comes from one of Node Bs for Test 2 and Test 4 as specified in Table 8.23.

For the parameters specified in Table 8.23 the average downlink $\underline{DPCH_{-}E_{c}}$ power ratio shall be below the specified value for the BLER shown in Table 8.24.

Table 8.23: DCH parameters in multi-path propagation conditions during SSDT mode (Propagation condition: Case 1)

Parameter	Unit	Test 1	Test 2	Test 3	Test 4				
Phase reference			P-	CPICH					
\hat{I}_{or1}/I_{oc}	dB	0	-3	0	0				
\hat{I}_{or2}/I_{oc}	dB	0	0	0	-3				
I_{oc}	dBm/3.84 MHz			-60					
Information Data Rate	kbps	12.2	12.2	12.2	12.2				
Cell ID code word error ratio in uplink	%	1	1	1	1				
Number of FBI bits assigned to "S" Field		1	1	2	2				
Code word Set		Long	Long	Short	Short				
UL DPCCH slot Format		1	#2	#5					

NOTE: The code word errors are introduced independently in both uplink channels.

Table 8.24: DCH requirements in multi-path propagation conditions during SSDT Mode

Test Number	$\frac{DPCH_{-}E_{c}}{I_{or}}$	BLER
1	- <u>6.07.5</u> dB	10 ⁻²
2	- <u>5.06.5</u> dB	10 ⁻²
3	-10.5 dB	10 ⁻²
4	-9.2 dB	10 ⁻²

3GPP TSG RAN WG4 (Radio) Meeting #27 030581

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	X O&M Specifications
Other comments:	lpha
	Equivalent CRs in other Releases: CR240r1 cat. F to 25.101 v3.13.0, CR242r1 cat. A to 25.101 v5.6.0. CR243r1 cat. A to 25.101 v6.0.0

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I_{oc}	dBm/3.84 MHz -60							
Information Data Rate	kbps	12.2	12.2	12.2	12.2			
Cell ID code word error ratio in uplink	%	1	1	1	1			
Number of FBI bits assigned to "S" Field		1	1	2	2			
Code word Set		Long	Long	Short	Short			
UL DPCCH slot Format		1	#2 #5					

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3GPP TSG RAN WG4 (Radio) Meeting #27 030582

R4-

Paris, France 19 - 23 May, 2003

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3GPP TSG RAN WG4 (Radio) Meeting #27 030583

R4-

Paris, France 19 - 23 May, 2003

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