TSG RAN Meeting #15 RP-020036

Cheju, Korea, 5 - 8 March 2002

Title: CRs (Rel-5) for WI "Node B Synchronisation for 1.28 Mcps TDD"

Source: TSG RAN WG4

Agenda Item: 9.3.2

RAN4 Tdoc	Spec	CR	Rev	Phase	Title	Cat	Curr Ver	New Ver
R4-020155	25.123	175		Rel-5	NodeB Synchronisation Measurements performance requirements for 1.28Mcps TDD	В	4.4.0	5.0.0

3GPP TSG RAN WG4 Meeting #21

R4-020155

Sophia Antipolis, France 28th January - 1st February 2002

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*	25.	123	CR	175	¥	rev	-	ж	Current ve	ersion:	4.4.0) #
For <u>HELP</u> on t	using th	is forn	n, see b	ottom of ti	his pag	e or l	ook a	t the	pop-up te	ext ove	er the # s	ymbols.
Proposed change	affects	s: #	(U)SIN	M N	/E/UE		Radio	o Acc	cess Netw	ork X	Core N	Network
Title: ਮ	Nod	eB Syr	nchronis	ation Mea	asurem	ent pe	erforn	nanc	e requirer	nents_	for 1.28M	cps TDD
Source: #	RAN	WG4										
Work item code: ₩	RAN	limp-N	BSLCR						Date:	 ≇ 1/	/2/2002	
Category:	В								Release:	₩ R	el-5	
	F A E C Detail	(esse (corre (Addi (Fund (Edito	ntial corresponds ition of fectional m prial mod	to a correct lature), lification of the abo	tion in a of featui	re)		ease,	2	(GS (Re (Re (Re (Re	following re SM Phase 2 Please 1996 Please 1997 Please 1998 Please 1998 Please 4) Please 5)	2) 5) 7) 3)
Reason for chang	e: #			es, mappir on Burst T				requ	irements	for 1.2	28Mcps TI	DD NodeB
Summary of chan	ge: #			requirem introduce		Node	B syr	nchro	onisation b	ourst ti	ming and	SIR
Consequences if not approved:	ж	Unfini	shed/ind	complete i	require	ments	S.					
Clauses affected:	ж	9.2.1.	11B									
Other specs affected:	¥	Tes	st specif	specificat ications ifications	tions	¥						
Other comments:	¥			of this CR		-		on the	e finalisati	on of t	the 1.28M	cps TDD

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at: http://www.3gpp.org/3G Specs/CRs.htm. Below is a brief summary:

- 1) Fill out the above form. The symbols above marked \$\mathbb{X}\$ contain pop-up help information about the field that they are closest to.
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under ftp://www.3gpp.org/specs/ For the latest version, look for the directory name with the latest date e.g. 2000-09 contains the specifications resulting from the September 2000 TSG meetings.

3)	With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.	

9.2.1.11B Node B Synchronisation for 1.28Mcps TDD

Cell synchronisation burst timing is the time of start (defined by the first detected path in time) of the cell sync burst of a neighbouring cell. Type 1 is used for the initial phase of Node B synchronisation. Type 2 is used for the steady-state phase of Node B synchronisation. Both have different range.

The reference point for the cell sync burst timing measurement shall be the Rx antenna connector.

9.2.1.11B.1 Cell Synchronisation burst timing Type1 and Type 2

Table 9.xx1

<u>Parameter</u>	<u>Unit</u>	Accuracy [chip]	Conditions
	1.	F. / 0.127 C. 1. d. / 1.	
Cell Synchronisation burst timing	<u>chip</u>	[+/-0.125 for both type 1] and type 2	
		and type 21	

9.2.1.11B.2 Range/mapping Type 1

The reporting range for Cell Synchronisation burst timing type 1 is from -65536 to +65536 chips with 1/4 chip resolution.

In table 9.xx the mapping of measured quantity is defined for burst type 1.

Table 9.xx2

Reported value	Measured quantity value	<u>Unit</u>
Burst TIME TYPE1 0000000	-65536 ≤ burst timing Type 1< -65535.75	<u>chip</u>
Burst_TIMETYPE1_0000001	<u>-65535.75</u> ≤ burst timing Type 1< -65535.5	<u>chip</u>
Burst_TIMETYPE1_0000002	-65535.5 ≤ burst timing Type 1< -65535.25	<u>chip</u>
<u></u>	<u></u>	<u></u>
Burst_TIMETYPE1_0524285	65535.25 ≤ burst timing Type 1< 65535.5	<u>chip</u>
Burst TIME TYPE1 0524286	<u>65535.5 ≤ burst timing Type 1< 65535.75</u>	<u>chip</u>
Burst TIME TYPE1 0524287	65535.75 ≤ burst timing Type 1< 65536	<u>chip</u>

9.2.1.11B.3 Range/mapping Type 2

The reporting range for Cell Synchronisation burst timing type 2 is from -8 to +8 chips with 1/8 chip resolution. In table 9.xx3 the mapping of measured quantity is defined for burst type 2.

Table 9.xx3

Reported value	Measured quantity value	<u>Unit</u>
Burst TIME TYPE2 0000	-8 ≤ burst timing Type 2< -7.875	<u>chip</u>
Burst TIME TYPE2 0001	$-7.875 \le \text{burst timing Type } 2 < -7.750$	chip
Burst TIME TYPE2 0002	$-7.750 \le \text{burst timing Type } 2 < -7.625$	chip
<u></u>	<u></u>	<u></u>
Burst TIME TYPE2 0125	$7.625 \le \text{burst timing Type } 2 < 7.750$	<u>chip</u>
Burst_TIMETYPE2_0126	$7.750 \le \text{burst timing Type } 2 < 7.875$	<u>chip</u>
Burst TIME TYPE2 0127	$7.875 \le \text{burst timing Type } 2 < 8$	<u>chip</u>

9.2.11B.4 Cell Synchronisation burst SIR Type1 and Type2

Signal to Interference Ratio for the cell sync burst, defined according to TS25.225.

The reference point for the cell synchronisation burst SIR shall be the Rx antenna connector.

Table 9.xx4

<u>Parameter</u>	<u>Unit</u>	Accura	Conditions	
		Normal conditions	Extreme conditions	
Cell Synchronisation burst SIR	<u>dB</u>	±3 dB for both type 1 and 2	Ц	

9.2.1.11B.5 Range/Mapping for Type1 and Type 2

The reporting range for SIR is from 0 ... 30 dB with a resolution of 1dB.

In table 9.xx5 mapping of the measured quantity is defined. Signalling range may be larger than the guaranteed accuracy range.

Table 9.xx5

Reported value	Measured quantity value	<u>Unit</u>
Cell_Sync_Burst_SIR_00	<u>SIR< 0</u>	<u>dB</u>
Cell_Sync_Burst_SIR_01	$0 \le SIR < 1$	<u>dB</u>
Cell_Sync_Burst_SIR_02	$1 \le SIR < 2$	<u>dB</u>
<u></u>	<u></u>	<u></u>
Cell_Sync_Burst_SIR_29	<u>28≤ SIR< 29</u>	<u>dB</u>
Cell Sync Burst SIR 30	29 ≤ SIR< 30	<u>dB</u>
Cell Sync Burst SIR 31	30 ≤ SIR	dB