TSG-RAN Meeting #13 Beijing, China, 18 - 21, September, 2001

TSGRP#13(01) 0597

Title: Agreed CRs to TS 25.425

Source: TSG-RAN WG3

Agenda item: 8.3.3/8.3.4/9.4.3

| RP Tdoc | R3 Tdoc | Spec | CR_Num | Rev | Release | CR_Subject | Ca | tCur_Ve | r New_Ver | Workitem |
|-----------|-----------|--------|--------|-----|---------|--|----|---------|-----------|---------------|
| | | | | | | | | | | |
| RP-010597 | R3-012263 | 25.425 | 030 | | Rel-4 | Correction on RACH data frame in Iur interface | F | 4.0.0 | 4.1.0 | LCRTDD-lublur |

TSGR3#23(01)2263

| ж | 25.425 CR 030 [#] rev - [#] Current version: 4.0.0 [#] | | | | | | | | | |
|---|---|--|--|--|--|--|--|--|--|--|
| For HFI P on u | ing this form see bottom of this page or look at the pop-up text over the \mathfrak{K} symbols | | | | | | | | | |
| | | | | | | | | | | |
| Proposed change affects: # (U)SIM ME/UE Radio Access Network Core Network | | | | | | | | | | |
| Title: ¥ | Correction on RACH data frame in lur interface | | | | | | | | | |
| Source: ೫ | R-WG3 | | | | | | | | | |
| Work item code: Ж | LCR TDD-lublur Date: % Jul 2001 | | | | | | | | | |
| Category: ೫ | F Release: # Rel4 | | | | | | | | | |
| | Use one of the following categories:Use one of the following releases:F (essential correction)2A (corresponds to a correction in an earlier release)R96B (Addition of feature),R97C (Functional modification of feature)R98D (Editorial modification)R99D tetailed explanations of the above categories canREL-4A found in 3GPP TR 21.900.REL-5 | | | | | | | | | |
| Reason for change | It would simplify the implementation if the number of bits of 'Received SYNC UL Timing Deviation' (in the RACH data frame) is changed from 11 to 8, so that the number of octets in the RACH data frame header would be independent of the UTRA mode (FDD; HCR TDD, LCR TDD). | | | | | | | | | |
| Summary of chang | The figure of the RACH data frame is changed. The number of bits of 'Received SYNC UL Timing Deviation' is changed from 11 to 8,and the resolution is changed from 1/8 chips to 1chip. | | | | | | | | | |
| Consequences if not approved: | It would cause some difficulty in implementation. Backward compatibility: This CR is backward compatible to the current R99 version. This CR has isolated impact to the current R99 version, because none of the R99 functional is effected. | | | | | | | | | |
| Clauses affected: | 策 6.2.1, 6.2.5.11A | | | | | | | | | |
| Other specs affected: | % Other core specifications % Test specifications % O&M Specifications % | | | | | | | | | |
| Other comments: | # | | | | | | | | | |

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at: <u>http://www.3gpp.org/3G_Specs/CRs.htm</u>. Below is a brief summary:

1) Fill out the above form. The symbols above marked **#** contain pop-up help information about the field that they are closest to.

- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under <u>ftp://www.3gpp.org/specs/</u> For the latest version, look for the directory name with the latest date e.g. 2000-09 contains the specifications resulting from the September 2000 TSG meetings.
- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

6.2.1 RACH/CPCH[FDD] Channels

RACH/CPCH[FDD] Iur data stream corresponds to the data stream of one specific UE. The used transport bearer for the transport of FACH/RACH or FACH/CPCH[FDD] is bi-directional.

The RACH/CPCH[FDD]/FACH FP does not facilitate multiplexing of data streams from different UEs onto the same data frame, but does allow multiple UEs to share the same transport bearer.

The RACH Data frame structure is defined as common for FDD and TDD with conditional fields, and CPCH[FDD] Data frame structure is defined as common for FDD only.





Figure 9: RACH/CPCH[FDD] Data Frame structure

Propagation delay is a conditional Information Element which is only present when the Cell supporting the RACH/CPCH[FDD] Transport Channel is a FDD Cell.

Rx Timing Deviation is a conditional Information Element which is only present when the Cell supporting the RACH Transport Channel is a 3.8<u>4</u>Mcps TDD Cell.

Received SYNC UL Timing Deviation is a conditional Information Element which is only present when the Cell supporting the RACH Transport Channel is a 1.28Mcps TDD Cell.

/* Partly omitted */

6.2.5.11A [1.28Mcps TDD – Received SYNC UL Timing Deviation]

Description: Measured Received SYNC UL Timing Deviation as a basis for propagation delay.

Value range: {0, ..., +256} chips

Granularity: 1/81 chips.

Field length: <u>11-8</u> bits.