TSGRP#7(00)0076

TSG-RAN Meeting #7 Madrid, Spain, 13 - 15 March 2000

Title: Agreed CRs to TS 25.411

Source: TSG-RAN WG3

Agenda item: 6.4.3

Tdoc_Num	Specification	CR_Num	Revision_Num	CR_Subject	CR_Category	WG_Status	Cur_Ver_Num	New_Ver_Num
R3-000217	25.411	001		Precise wording in section 7.2 with respect to Fractional ATM.	D	agreed	3.1.0	3.2.0

Madrid, Spain, 13 - 15 March 2000

	3G CHANGE REQUEST Please see embedded help file at the bottom of this page for instructions on how to fill in this form correctly.						
	25.411 CR 001 Current Version: 3.1.0						
	3G specification number ↑						
For submission to TSG-RAN #7 for approval list TSG meeting no. here for information to for information to list TSG meeting no. here for information to list the formation th							
Proposed change (at least one should be							
Source:	TSG-RAN WG3 Date: Jan 25, 2000						
Subject: Precise wording in section 7.2 with respect to Fractional ATM.							
3G Work item:							
(only one category Eshall be marked	F Correction A Corresponds to a correction in a 2G specification B Addition of feature C Functional modification of feature D Editorial modification X						
Reason for change:	Wording should be clear.						
Clauses affected: Section 4.2. Changes are shown by revision marks.							
Other specs affected:	Other 3G core specifications Other 2G core specifications MS test specifications BSS test specifications O&M specifications O&M specifications → List of CRs: → List of CRs: → List of CRs: → List of CRs:						
Other comments:							

2 References

[16] ATM Forum AF-PHY-0130.00 (10/99), ATM on Fractional E1/T1"

4.2 Layer 1 Description

Layer 1 reference configuration shall be according to ITU-T Recommendation I.432.1 [6]. The physical layer is divided into:

- Physical Media Dependent (PMD) sublayer
- Transmission Convergence (TC) sublayer defined according to ITU-T Recommendation I.432.1 [6].

The PMD shall comply with at least one of the following standards:

- ETSI STM-4 (622 Mb/s) interface according to I.432.2 [1] with optical S-1.1 interface according to G.957 [5].
- SONET STS-12c (622 Mb/s) interface according to ANSI, T1.105-1995 with optical multimode.
- SONET STS-3c (155 Mb/s) interface according to ANSI, T1.105-1995 with optical multimode.
- ETSI STM-1 (155 Mb/s) interface according to I.432.2 [1] with electrical interface (CMI) to G.703 [3].
- ETSI STM-1 (155 Mb/s) interface according to I.432.2 [1] with optical S-1.1 interface according to G.957 [5].
- ITU STS-1 (51 Mb/s) interface according to ANSI, T1.105-1995 with electrical interface.
- ITU STM-0 (51 Mb/s) interface according to ETSI/TTC with electrical interface.
- ITU STM-0 (51 Mb/s) interface according to ETSI/TTC with optical S-1.1 interface according G.957 [5].
- J2, 6.3 Mb/s interface according to Japanese standard JT-G.703 [3] and JT-G.704 [4] (75 Ohm).

NOTE: J2 requires that the ATM cells be mapped into the physical layer according to HEC based mapping in G.804.

- E2, 8Mb/s according to ETSI/ITU G.703 [3] and G.704 [4] (75 Ohm).
- E3, 34 Mb/s interface according to ETSI/ITU G.751 [13] (75 Ohm).
- T3, 45 Mb/s interface according to ANSI/ITU G.703 [3] and G.704 [4] (75 Ohm).
- E1, 2Mb/s interface balanced 120 Ohm symmetrical according to ETS 300 420, ITU-T G.704
 [4] and TBR 013 (G.703) [3], and AF-PHY-0064.000 [11]
- E1, 2Mb/s according to ETSI/ITU G.703 [3] and G.704 [4] (75 Ohm), and AF-PHY-0064.000 [13].
- J1, 1.5 Mb/s interface according to Jt-431-a (100 Ohm).
- J1, 1.5 Mb/s interface according to JT-G.703 [3] and JT-G.704 [4] (110 Ohm).
- T1, 1.5 Mb/s interface according to AF-PHY-0016.000 [10] and ANSI/ITU G.703 [3] and G.704 [4] (100 Ohm).

Services provided to the upper layer shall be independent from the used underlying technology. The support of intervening transport networks – like PDH or SDH terrestrial links, Point-to-point or Point-to-Multipoint radio links – shall not be prevented.

Fractional use of the physical medium to terminate a multiple of n time slots shall not be prevented, in order to allow coexistence of this interface with other interfaces on the same physical medium. It shall be possible to use n 64 kbit/s time slots within the scope of "ATM on Fractional E1/T1" as specified in [16], and to allow the co-existance of this interface with other interfaces on the same physical medium

3GPP 5

When using E1, T1, or J1, it shall be possible to use inverse multiplexing of ATM (IMA) [14] within suitable subsets of the physical ports on the respective Exchange Termination (ET). The clock stability required shall be according to G.823 [7] or G.824 [8] or G.825 [9] whichever is applicable.

The clock extracted from the I_u may be used as UTRAN reference clock.

Transmission quality control shall be provided according to ITU-T Recommendation G.826 [10].

3GPP 6