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Agreed CRs of category "C" (Modifications) and "F" (Corrections) to TS 25.101 Title:

Source: TSG-RAN WG4

Agenda item: 5.4.3

TSG_DOC SPEC CR RE 3G_P	SPEC	CR	RE	3G_P	SUBJECT	CAT	CAT VERS_CUR VERS_NEW	VERS_NEW
R4-99752	25.101 003	003		R99	Modifications for Receiver Characteristics	Ŀ	3.0.0	3.1.0
R4-99756	25.101 004	004		R99	Corrections to Tx Diversity testing assumptions	LL	3.0.0	3.1.0
R4-99847	25.101	00	2	R99	Correction of UE Measurement Channels Rev.2	LL	3.0.0	3.1.0
R4-99857	25.101 006	900		R99	Corrections to Annex C Down link Physical Channels	LL	3.0.0	3.1.0
R4-99860	25.101 007	007		R99	Proposal for ACLR/ACS specifications for class 3	LL	3.0.0	3.1.0
R4-99929	25.101 009	600		R99	Clarification of Uplink inner loop power control requirements	ပ	3.0.0	3.1.0
R4-99932	25.101 011	011		R99	Power setting of DPCH	ပ	3.0.0	3.1.0
R4-99937	25.101 014	014		R99	Update of ITU Region 2 Specific Specifications and proposed universal channel numbering	с	3.0.0	3.1.0
R4-99966	25.101 015	015		R99	Performance requirements for demodulation of DCH in Site Selection	L	3.0.0	3.1.0
R4-99977	25.101 016	016		R99	Change of propagation conditions	Ŀ	3.0.0	3.1.0
R4-99992	25.101 017	017		R99	CR for minimum requirements for UE power class 1 and 2 in 25.101	ш	3.0.0	3.1.0
R4-99998 25.101 018	25.101	018		R99	Downlink Inner loop power control	с U	3.0.0	3.1.0

TSGRP#6(99)772

3GPP TSG-RAN WG4 meeting #8 Sophia Antipolis, France, 26th -29th of Oct. 1999

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7.3 Reference sensitivity level

The reference sensitivity is the minimum receiver input power measured at the antenna port at which the Bit Error Rate (BER) does not exceed a specific value

7.3.1 Minimum requirement

The BER shall not exceed 0.001 for the parameters specified in Table 12

Table 12: Te	st parameters for refere	nce sensitivity
Parameter	Unit	Level
$\frac{PCCPCH_Ec}{I_{or}}$	dB	-1
DPCH_Ec I _{or}	dB	-7
Î _{or}	dBm/3.84 MHz	-110

Table 12: Test parameters for reference sensitivity

Table 12: Test parameters for reference sensitivity

Parameter	<u>Unit</u>	Level
DPCH_Ec	<u>dBm/3.84 MHz</u>	<u>-117</u>
<u>Î</u> <u>or</u>	<u>dBm/3.84 MHz</u>	<u>-106.7</u>

7.4 Maximum input level

This is defined as the maximum receiver input power at the UE antenna port, which does not degrade the specified BER performance.

7.4.1 Minimum requirement

The BER shall not exceed 0.001 for the parameters specified in Table 13

Parameter	Unit	Level
PCCPCH_Ec I _{or}	dB	_10
DPCH _ Ec I _{or}	dB	-19
OCNS_Ec I _{or}	dB	- 0.52
er.	dBm/3.84 MHz	-25
<u>T</u>	able 13: Maximum input	level
Parameter	<u>Unit</u>	Level
DPCH _ Ec I _{or}	dB	<u>-19</u>
2 <u>or</u>	<u>dBm/3.84 MHz</u>	<u>-25</u>

Note

1. Since the spreading factor is large (10log(SF)=21dB), the majority of the total input signal consists of the OCNS interference.

7.5 Adjacent Channel Selectivity (ACS)

Adjacent Channel Selectivity (ACS) is a measure of a receiver's ability to receive a W-CDMA signal at its assigned channel frequency in the presence of an adjacent channel signal at a given frequency offset from the center frequency of the assigned channel. ACS is the ratio of the receive filter attenuation on the assigned channel frequency to the receive filter attenuation on the adjacent channel(s).

7.5.1 Minimum requirement

The ACS shall be better than the value indicated in Table 14a for the test parameters specified in Table 14b where the BER shall not exceed 0.001

Power Class	Unit	ACS
4	dB	33

Table 14a: Adjacent Channel Selectivity

Table 14b: Test parameters for Adjacent Channel Selectivity

Parameter	Unit	Level
$\frac{PCCPCH_Ec}{I_{or}}$	dB	-0.46
$\frac{DPCH_Ec}{I_{or}}$	dB	-10
Î _{or}	dBm/3.84 MHz	-93
I _{oac}	dBm/3.84 MHz	-52
F _{uw} (modulated)	MHz	+5 or 5

Table 14b: Test parameters for Adjacent Channel Selectivity

Parameter	<u>Unit</u>	Level
DPCH Ec	<u>dBm/3.84 MHz</u>	<u>-103</u>
$\hat{\underline{\mathbf{I}}}_{\underline{\mathrm{or}}}$	<u>dBm/3.84 MHz</u>	<u>-92.7</u>
<u>I_{oac}</u>	<u>dBm/3.84 MHz</u>	<u>-52</u>
$\underline{F_{uw}}$ (modulated)	MHz	<u>+5 or -5</u>

7.6 Blocking characteristics

The blocking characteristic is a measure of the receiver's ability to receive a wanted signal at its assigned channel frequency in the presence of an unwanted interferer on frequencies other than those of the spurious response or the adjacent channels, without this unwanted input signal causing a degradation of the performance of the receiver beyond a specified limit. The blocking performance shall apply at all frequencies except those at which a spurious response occur.

7.6.1 Minimum requirement

The BER shall not exceed 0.001 for the parameters specified in Table 15 and Table 16. For Table 16 up to (24) exceptions are allowed for spurious response frequencies in each assigned frequency channel when measured using a 1MHz step size

	Table 13. In-band blocking						
Parameter	Unit	Offset	Offset				
PCCPCH _ Ec I _{or}	dB	-1	-1				
DPCH_Ec I _{or}	d₿	-7	-7				
Î _{or}	dBm/3.84 MHz	-107	-107				
I _{blocking} (modulated)	dBm/3.84 MHz	-56	-44				
Blocking offset	MHz	10< f fo <15	<u>f_fo ≥15</u>				

Table 15: In-band blocking

Table 16: Out of band blocking

Parameter	Unit	Band 1	Band 2	Band 3
PCCPCH_Ec I _{or}	dB	-1	-1	-1
DPCH_Ec I _{or}	dB	-7	-7	-7
Î _{or}	dBm/3.84 MHz	- 107	- 107	- 107
I _{blocking} (CW)	dBm	-44	-30	-15
Blocking offset	MHz	2050<f <2095<="" del=""> 2185<f <2230<="" del=""></f></f>	2025 <f <2050<br="">2230 <f <2255<="" td=""><td>1< f <2025 2255<f<12750< del=""></f<12750<></td></f></f>	1< f <2025 2255<f<12750< del=""></f<12750<>

Table 15: In-band blocking

Parameter	<u>Unit</u>	<u>Offset</u>	<u>Offset</u>
DPCH Ec	<u>dBm/3.84 MHz</u>	<u>-114</u>	<u>-114</u>
<u>Î</u> <u>or</u>	<u>dBm/3.84 MHz</u>	<u>-103.7</u>	<u>-103.7</u>
<u>I_{blocking} (modulated)</u>	<u>dBm/3.84 MHz</u>	<u>-56</u>	<u>-44</u>
Blocking offset	MHz	<u>10< f-fo <15</u>	<u> f-fo ≥15</u>

Table 16: Out of band blocking

Parameter	<u>Unit</u>	<u>Band 1</u>	Band 2	Band 3
DPCH_Ec	<u>dBm/3.84 MHz</u> <u>-114</u>		<u>-114</u>	<u>-114</u>
<u>Î</u> <u>or</u>	<u>dBm/3.84 MHz</u>	<u>-103.7</u>	<u>-103.7</u>	<u>-103.7</u>
<u>Iblocking</u> (CW)	<u>DBm</u>	<u>-44</u>	<u>-30</u>	<u>-15</u>
Blocking offset	MHz	<u>2050<f <2095<="" u=""></f></u>	<u>2025 <f <2050<="" u=""></f></u>	<u>1< f <2025</u>
Diocking offset		<u>2185<f <2230<="" u=""></f></u>	<u>2230 <f <2255<="" u=""></f></u>	<u>2255<f<12750< u=""></f<12750<></u>

Note

1. On frequency regions 2095 <f< 2110 MHz and 2170<f< 2185 MHz, the appropriate in-band blocking or adjacent channel selectivity in section 7.5.1 shall be applied.

7.7 Spurious response

Spurious response is a measure of the receiver's ability to receive a wanted signal on its assigned channel frequency without exceeding a given degradation due to the presence of an unwanted CW interfering signal at any other frequency at which a response is obtained i.e. for which the blocking limit is not met.

7.7.1 Minimum requirement

The BER shall not exceed 0.001 for the parameters specified in Table 17

Parameter	Unit	Level	
$\frac{PCCPCH_Ec}{I_{or}}$	d₿	-+	
DPCH_Ec I _{or}	dB	-7	
Î _{or}	dBm/3.84 MHz	-107	
I _{blocking (} CW)	dBm	-44	
Few	MHz	Spurious response frequencie	

Table 17: Spurious Response

Table 17: Spurious Response

Parameter	Unit	Level
DPCH_Ec	<u>dBm/3.84 MHz</u>	<u>-114</u>
<u>Î</u> <u>or</u>	<u>dBm/3.84 MHz</u>	<u>-103.7</u>
<u>I_{blocking (}CW)</u>	dBm	<u>-44</u>
fcw	MHz	Spurious response frequencies

7.8 Intermodulation characteristics

Third and higher order mixing of the two interfering RF signals can produce an interfering signal in the band of the desired channel. Intermodulation response rejection is a measure of the capability of the receiver to receiver a wanted signal on its assigned channel frequency in the presence of two or more interfering signals which have a specific frequency relationship to the wanted signal.

7.8.1 Minimum requirement

The BER shall not exceed 0.001 for the parameters specified in Table 18.

Table 18: Receive intermodulation characteristics

Parameter	Unit	Level
PCCPCH_Ec I _{or}	dB	+
DPCH_Ec I _{or}	dB	-7
Î _{or}	dBm/3.84 MHz	-107
I _{ouw1}	dBm	-46
I _{ouw2}	dBm/3.84 MHz	- 46

ſ	Fuw1 (CW)	MHz	10
Ī	Fuw2 (Modulated)	MHz	20

Table 18: Receive intermodulation characteristics

Parameter	Unit	Level
DPCH Ec	<u>dBm/3.84 MHz</u>	<u>-114</u>
<u>Î</u> <u>or</u>	<u>dBm/3.84 MHz</u>	<u>-103.7</u>
<u>I_{ouw1}</u>	<u>dBm</u>	<u>-46</u>
<u>I_{ouw2}</u>	<u>dBm/3.84 MHz</u>	<u>-46</u>
Fuw1 (CW)	MHz	<u>10</u>
Fuw2 (Modulated)	MHz	<u>20</u>

C.3. During connection

Table C.3 and C4 describes the downlink Physical Channels that are transmitted during a connection i.e., when measurements are done. The offset between DPCH and SCH should be zero chips at base station meaning that SCH is overlapping with the first symbols in DPCH in the beginning of DPCH slot structure.

C.3.1 Measurement of Receiver Characteristics

This section is applicable for measurements on Receiver Characteristics (section 7), except paragraph 7.4, Maximum input level..

Table C.3. Downlink Physical Channels transmitted during a connection. ¹			
Physical Channel	Power level		
<u>CPICH</u>	$\frac{\text{CPICH Ec/DPCH Ec} = 7 \text{ dB}}{100000000000000000000000000000000000$		
<u>PCCPCH</u>	<u>PCCPCH Ec/DPCH Ec = 5 dB</u>		
<u>SCH</u>	<u>SCH Ec/DPCH Ec = 5 dB</u>		
<u>PICH</u>	<u>PICH Ec/DPCH Ec = 2 dB</u>		
OCNS	Not applicable		
DPCH	The power needed to meet the BER/BLER target		

C.3.2. Measurement of Performance requirements

This section is applicable for measurements on performance requirements (section 8), including paragraph 7.4, Maximum input level.

Table C.43. Downlink Physical	Channels transmitted during a connection. ¹

Physical Channel	Power
СРІСН	$CPICH_Ec/Ior = -10 \text{ dB}$
РССРСН	$PCCPCH_Ec/Ior = -12 dB$
SCH	PCCPCH_Ec/Ior = -12 dB
PICH	$PICH_Ec/Ior = -15 \text{ dB}$
DPCH	The power needed to meet the BER/BLER target
OCNS	Necessary power so that total transmit power spectral density of BS (Ior) adds to one

¹ Power levels are based on the assumption that multipath propagation conditions and noise source representing interference from other cells Ioc are turned on after the call set-up phase.

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6.5.3 Change of TFC

A change of TFC (Transport Format Combination) in uplink means that the power in uplink varies according the change of data rate. DTX, where the DPDCH is turned off, is a special case of variable data rate, which is used to minimize the interference between UE(s) by reducing the UE transmit power when voice, user or control information is not present.

6.5.3 Transmit Discontinuous Transmission (DTX)

DTX is used to minimize the interference between UE(s) by reducing the UE transmit power when voice. user or control information is not present.

6.5.3.1 Minimum requirement

A change of output power is required when the TFC, and thereby the datarate, is changed. The ratio of the amplitude between the DPDCH codes and the DPCCH code will vary. The power step due to a change in TFC shall be calculated in the UE so that the power transmitted on the DPCCH shall be kept constant. The power step shall then be rounded to the closest integer dB value. The accuracy of the power step, given the step size is specified in Table 5.

Table 5: Transmitter power step tolerance					
Power control step size (Up or down)	Transmitter power step tolerance				
<u>ΔΡ [dB]</u>	[<u>dB</u>]				
<u>1</u>	<u>+/- 0.5 dB</u>				
2	<u>+/- 1.0 dB</u>				
3	<u>+/- 1.5 dB</u>				
<u>4≤∆P≤10</u>	<u>+/- 2 dB</u>				
<u>11≤ΔP≤15</u>	<u>+/- 3 dB</u>				
$\underline{16 \leq \Delta P \leq 20}$	<u>+/- 4 dB</u>				
<u>21≤∆P</u>	<u>+/- 6 dB</u>				

Table 5. Transmitter newer stan telerance

The transmit power levels versus time should meet the mask specified in figure 1. When power increases the power step should be performed before the frame boundary, when power decreases the power step should be performed after the frame boundary.

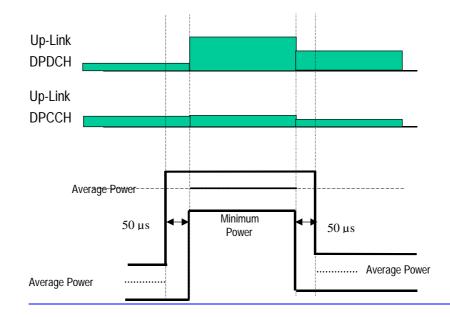


Figure 1 Transmit template during TFC Change.

The transmit DTX template is specified when Uplink Dedicated Physical Data Channel (DPDCH) is turned OFF and when the DPDCH is turned ON. With reference to the template specified when the DPDCH is turned OFF (a) and when the DPDCH is turned ON (b)

Note

1.Px [dB] and Py [dB] is the average power of 1 slot excluding the 50µs transient period 2.Pt [dB] is the average power during the period of the 50µs transient 3.Py – Px should be within ±2dB of the theoretical power change 4.Pt should be between Px and Py

5.* Theoretical power change is specified by the Gain factor β (see 25.213v x.y.z section 4.2.1)

(a)DTX template when DPDCH is turned OFF

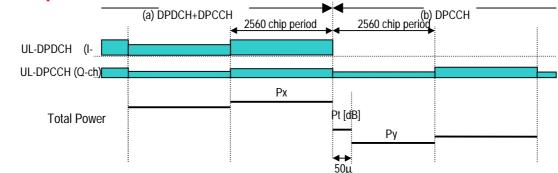


Figure 2a; Uplink Transmit DTX (DPDCH is turned OFF)

Table 6a: the values of Gain Factor β and theoretical power change

	(a) DPDCH+DPCCH	(b) DPCCH		
DPDCH Gain	1	θ		
DPCCH Gain	0.5	0.5		
Theoretical power change	-7 dB			

(b) DTX template when DPDCH is turned ON

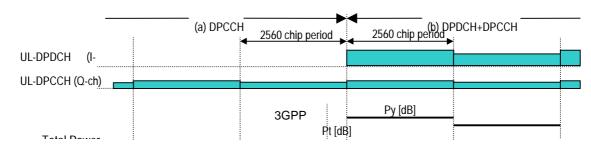


Figure 2b; Uplink Transmit DTX (DPDCH is turned ON)

Table 6b: the values of Gain Factor β and theoretical power change

	(a) DPCCH	(b) DPDCH+DPCCH
DPDCH Gain	θ	1
DPCCH Gain	0.5	0.5
Theoretical power change	7-	d₿

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Annex A (normative): Measurement channels

A.1 General

The measurement channels in this annex are defined to derive the requirements in section 6,7 and 8. The measurement channels represent example configurations of radio access bearers for different data rates.

The measurement channel for 12.2 kbps shall be supported by any UE both in up- and downlink. Support for other measurement channels is depending on the UE Radio Access capabilities.

A.2 UL reference measurement channel

A.2.1 UL reference measurement channel (12.2 kbps)

The parameters for the 12.2 kbps UL reference measurement channel are specified in Table A.1 and Table A.2. The channel coding for information is shown in figure A.1

Parameter	Unit	Level
Information bit rate	kbps	12.2
DPDCH	kbps	60
DPCCH	kbps	15
DPCCH/DPDCH	dB	-6
Power control	-	Off
TFCI	-	On
Repetition	%	23

Table A.1: UL reference measurement channel physical parameters (12.2 kbps)

Table A.2: UL reference measurement channel, transport channel parameters (12.2 kbps)

Parameters	DCCH	DTCH
Transport Channel Number	1 (TBD by WG2)	2 (TBD by WG2)
Transport Block Size	96	244
Transport Block Set Size	96	244
Transmission Time Interval	40 ms	20 ms
Type of Error Protection	Convolution Coding	Convolution Coding
Coding Rate	1/3	1/3
Static Rate Matching parameter	1.0	1.0
Size of CRC	16	16
Position of TrCH in radio frame	Fixed	fixed

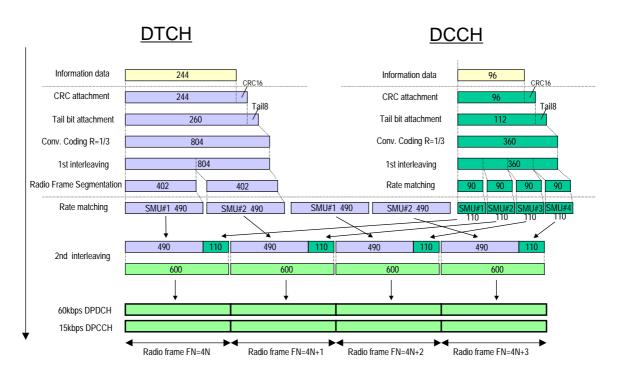


Figure A.1(Informative): Channel coding of UL reference measurement channel (12.2 kbps)

A.2.2 UL reference measurement channel (64 kbps)

The parameters for the 64 kbps UL reference measurement channel are specified in Table A.3 and Table A.4. The channel coding for information is shown in figure A.2. This measurement channel is not currently used in TS25.101 but can be used for future requirements.

Parameter	Unit	Level
Information bit rate	kbps	64
DPDCH	kbps	240
DPCCH	kbps	15
DPCCH/DPDCH	dB	-9
Power control	-	Off
TFCI	-	On
Repetition	%	18

 Table A.3: UL reference measurement channel (64 kbps)

Table A.4: UL reference measurement channel, transport channel parameters (64kbps)

Parameter	DCCH	DTCH
Transport Channel Number	1 (TBD by WG2)	2 (TBD by WG2)
Transport Block Size	96	1280
Transport Block Set Size	96	1280
Transmission Time Interval	40 ms	20 ms

Type of Error Protection	Convolution Coding	Turbo Coding
Coding Rate	1/3	1/3
Static Rate Matching parameter	1.0	1.0
Size of CRC	16	16
Position of TrCH in radio frame	Fixed	Fixed

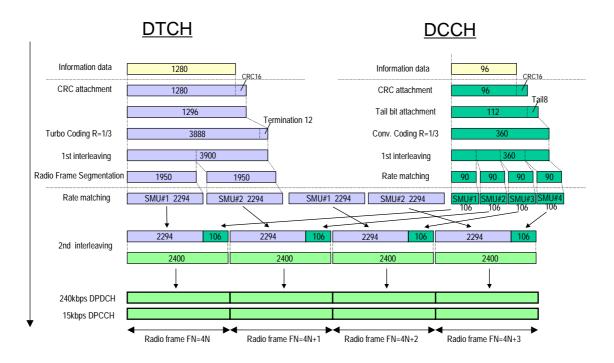


Figure A.2 (Informative): Channel coding of UL reference measurement channel (64 kbps)

A.2.3 UL reference measurement channel (144 kbps)

The parameters for the 144 kbps UL reference measurement channel are specified in Table A.5 and Table A.6. The channel coding for information is shown in Figure A.3. This measurement channel is not currently used in TS25.101 but can be used for future requirements.

Parameter	Unit	Level
Information bit rate	kbps	144
DPDCH	kbps	480
DPCCH	kbps	15
DPCCH/DPDCH	dB	-12
Power control	-	Off
TFCI	-	On
Repetition	%	8

Table A.5: UL reference measurement channel (144 kbps)

 Table A.6: UL reference measurement channel, transport channel parameters (144kbps)

		· • ·
Parameters	DCCH	DTCH
Transport Channel Number	1 (TBD by WG2)	2 (TBD by WG2)
Transport Block Size	96	2880
Transport Block Set Size	96	2880
Transmission Time Interval	40 ms	20 ms
Type of Error Protection	Convolution Coding	Turbo Coding
Coding Rate	1/3	1/3
Static Rate Matching parameter	1.0	1.0
Size of CRC	16	16
Position of TrCH in radio frame	Fixed	Fixed

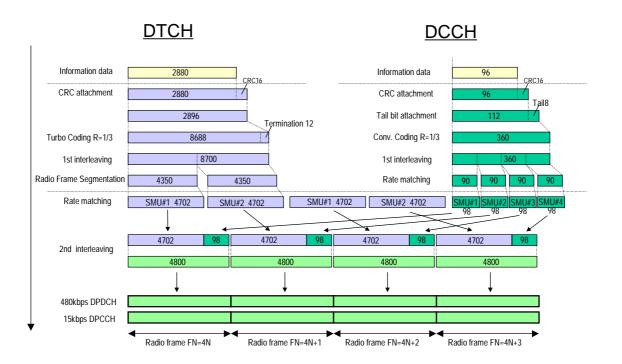


Figure A.3(Informative): Channel coding of UL reference measurement channel (144 kbps)

A.2.4 UL reference measurement channel (384 kbps)

The parameters for the 384 kbps UL reference measurement channel are specified in Table A.7 and Table A.8 The channel coding for information is shown in Figure A.4. This measurement channel is not currently used in TS25.101 but can be used for future requirements.

Parameter	Unit	Level	
Information bit rate	kbps	384	
DPDCH	kbps	960	
DPCCH	kbps	15	
DPCCH/DPDCH	dB	-12	
Power control	-	Off	
TFCI	-	On	
Puncturing	%	18	

Table A.7: UL reference measurement channel (384 kbps)

Table A.8: UL reference measurement channe	. transport channel parameters (384 kbps)	
Tuble 11.01 CE refer chee meusurement chumie	, d'ansport channel parameters (sot hops)	

Parameter	DCCH	DTCH
Transport Channel Number	1 (TBD by WG2)	2 (TBD by WG2)
Transport Block Size	96	7680<u>3840</u>
Transport Block Set Size	96	7680
Transmission Time Interval	40 ms	20 ms
Type of Error Protection	Convolution Coding	Turbo Coding
Coding Rate	1/3	1/3
Static Rate Matching parameter	1.0	1.0
Size of CRC	16	16
Position of TrCH in radio frame	Fixed	Fixed

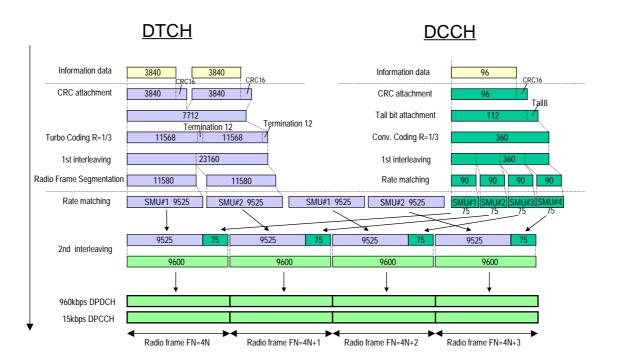


Figure A.4<u>(Informative)</u>: Channel coding of UL reference measurement channel (384 kbps)

A.3 DL reference measurement channel

A.3.1 DL reference measurement channel (12.2 kbps)

The parameters for the 12.2 Kbps DL reference measurement channel are specified in Table A. 9 and Table A.10. The channel coding is shown for information in figure A.5

 Table A.9: DL reference measurement channel physical parameters (12.2 kbps)

	1 0 1	· · ·
Parameter	Unit	Level
Information bit rate	kbps	12.2
DPCH	ksps	30
Power control	_	Off
TFCI	-	On
Puncturing	%	14.5

Table A.10: DL reference measurement channel, transport channel parameters (12.2 kbps)

Parameter	DCCH	DTCH
Transport Channel Number	1 (TBD by WG2)	2 (TBD by WG2)
Transport Block Size	96	244
Transport Block Set Size	96	244
Transmission Time Interval	40 ms	20 ms
Type of Error Protection	Convolution Coding	Convolution Coding
Coding Rate	1/3	1/3
Static Rate Matching parameter	1.0	1.0
Size of CRC	16	16
Position of TrCH in radio frame	fixed	fixed

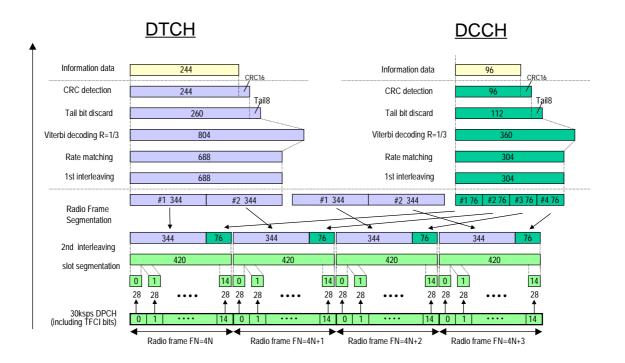


Figure A.5<u>(Informative)</u>: Channel coding of DL reference measurement channel (12.2 kbps)

A.3.2 DL reference measurement channel (64 kbps)

The parameters for the DL reference measurement channel for 64 kbps are specified in Table A.11 and Table A.12. The channel coding is shown for information in Figure A.6

Parameter	Unit	Level
Information bit rate	kbps	64
DPCH	ksps	120
Power control	-	Off
TFCI	-	On
Repetition	%	2.9

 Table A.11: DL reference measurement channel physical parameters (64 kbps)

 Table A.12: DL reference measurement channel, transport channel parameters (64 kbps)

Parameter	DCCH	DTCH
Transport Channel Number	1 (TBD by WG2)	2 (TBD by WG2)
Transport Block Size	96	1280
Transport Block Set Size	96	1280
Transmission Time Interval	40 ms	20 ms
Type of Error Protection	Convolution Coding	Turbo Coding
Coding Rate	1/3	1/3
Static Rate Matching parameter	1.0	1.0
Size of CRC	16	16
Position of TrCH in radio frame	fixed	fixed

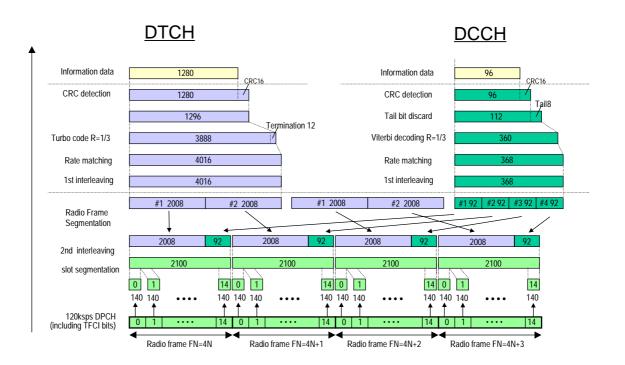


Figure A.6<u>(Informative)</u>: Channel coding of DL reference measurement channel (64 kbps)

A.3.3 DL reference measurement channel (144 kbps)

The parameters for the DL measurement channel for 144 kbps are specified in Table A.13 and Table A.14. The channel coding is shown for information in Figure A.7

Parameter	Unit	Level
Information bit rate	kbps	144
DPCH	ksps	240
Power control	-	Off
TFCI	-	On
Puncturing	%	2.7

 Table A.13: DL reference measurement channel physical parameters (144 kbps)

 Table A.14: DL reference measurement channel, transport channel parameters (144 kbps)

Parameter	DCCH	DTCH
Transport Channel Number	1 (TBD by WG2)	2 (TBD by WG2)
Transport Block Size	96	2880
Transport Block Set Size	96	2880
Transmission Time Interval	40 ms	20 ms
Type of Error Protection	Convolution Coding	Turbo Coding
Coding Rate	1/3	1/3
Static Rate Matching parameter	1.0	1.0
Size of CRC	16	16
Position of TrCH in radio frame	fixed	fixed

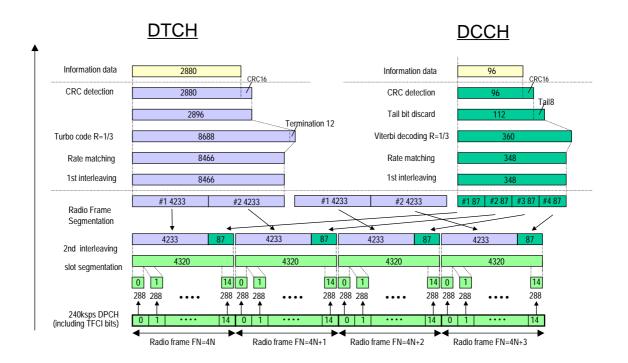


Figure A.7<u>(Informative)</u>: Channel coding of DL reference measurement channel (144 kbps)

A.3.4 DL reference measurement channel (384 kbps)

The parameters for the DL measurement channel for 384 kbps are specified in Table A.15 and Table A.16. The channel coding is shown for information in Figure A.8

Parameter	Unit	Level
Information bit rate	kbps	384
DPCH	ksps	480
Power control		Off
TFCI		On
Puncturing	%	22

 Table A.15: DL reference measurement channel, physical parameters (384 kbps)

Table A.16: DL reference measurement channel, transport channel parameters (384 kbps)

Parameter	DCCH	DTCH
Transport Channel Number	1 (TBD by WG2)	2 (TBD by WG2)
Transport Block Size	96	7680<u>3840</u>
Transport Block Set Size	96	7680
Transmission Time Interval	40 ms	20 ms
Type of Error Protection	Convolution Coding	Turbo Coding
Coding Rate	1/3	1/3
Static Rate Matching parameter	1.0	1.0
Size of CRC	16	16
Position of TrCH in radio frame	fixed	fixed

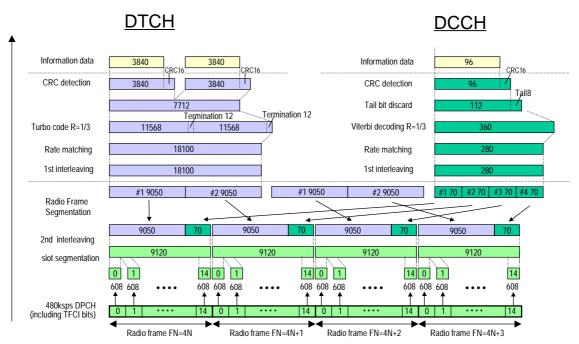


Figure A.8<u>(Informative)</u>: Channel coding of DL reference measurement channel (384 kbps)

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Annex C (normative): Downlink Physical Channels

C.1 General

This Normative annex specifies the downlink physical channels that are needed for setting a connection and channels that are needed during a connection.

C.2 Connection Set-up

Table C.2 describes the downlink Physical Channels that are required for connection set up.

Physical Channel
СРІСН
РССРСН
SCH
SCCPCH
PICH
AICH
DPCH

C.3. During connection

Table C.3 The following clauses describes the downlink Physical Channels that are transmitted during a connection i.e., when measurements are done. For these measurement the offset between DPCH and SCH shall should be zero chips at base station meaning that SCH is overlapping with the first symbols in DPCH in the beginning of DPCH slot structure.

C.3.1 Measurement of Rx Characteristics

Table C.3.1 is applicable for measurements on the Receiver Characteristics (Section 7) with the exception of clause 7.4 (Maximum input level)

Tube electric boundary in the second competition		
Physical Channel	Power	
<u>CPICH</u>	$\frac{\text{CPICH Ec} / \text{DPCH Ec}}{\text{Ec}} = 7 \text{ dB}$	
<u>PCCPCH</u>	$\frac{PCCPCH Ec / DPCH Ec = 5 dB}{PCCPCH Ec = 5 dB}$	
<u>SCH</u>	$\frac{\text{SCH Ec} / \text{DPCH Ec}}{\text{Ec}} = 5 \text{ dB}$	
PICH	$\frac{\text{PICH Ec} / \text{DPCH Ec}}{\text{Ec}} = 2 \text{ dB}$	
<u>DPCH</u>	Test dependent power The power needed to meet the BER/BLER target	

Table C.3.1. Downlink Physical Channels transmitted during a connection.

C.3.2 Measurement of Performance requirements

<u>Table C.3.2 is applicable for measurements on the Performance requirements(Section 8), including clause 7.4</u> (Maximum input level)

Physical Channel	Power	Note
СРІСН	$CPICH_Ec/Ior _= -10 \text{ dB}$	
РССРСН	PCCPCH_Ec/Ior= -12 dB	
SCH	PCCPCH_SCH_Ec/Ior = -12 dB	<u>This power shall be divided equally</u> <u>between Primary and Secondary</u> <u>Synchronous channels</u>
PICH	PICH_Ec/Ior = -15 dB	
DPCH	Test dependent power The power needed to meet the BER/BLER target	
OCNS	Necessary power so that total transmit power spectral density of BS (Ior) adds to one	

Table C.3.2 Downlink Physical Channels transmitted during a connection.¹

C.3.3 Connection with open-loop transmit diversity mode

Table C.3.3 is applicable for measurements for clause 8.6.1(Demodulation of DCH in open loop transmit diversity mode

Table C3.3. Downlink Physical Channels transmitted during a connection.¹

Physical Channel	Power	Note

¹ Power levels are based on the assumption that multipath propagation conditions and noise source representing interference from other cells Ioc are turned on after the call set-up phase.

CPICH (antenna 1) CPICH (antenna 2)	$\frac{\text{CPICH Ec1/Ior}}{\text{CPICH Ec2/Ior}} = -13 \text{ dB}$	$1. \underline{Total \ CPICH \ Ec/lor} = -10 \ dB$
PCCPCH (antenna 1)	<u>PCCPCH Ec1/Ior = -15 dB</u>	1. <u>STTD applied</u>
PCCPCH (antenna 2)	<u>PCCPCH Ec2/Ior = -15 dB</u>	2. <u>Total PCCPCH Ec/Ior = $-12 dB$</u>
<u>SCH (antenna 1 / 2)</u>	$\underline{SCH \ Ec/Ior} = -12 \ dB$	 <u>TSTD applied.</u> <u>This power shall be divided</u> <u>equally between Primary and</u> <u>Secondary Synchronous</u> <u>channels</u>
PICH (antenna 1)	$\underline{PICH_Ec1/Ior} = -18 \text{ dB}$	1. <u>STTD applied</u>
PICH (antenna 2)	$\underline{PICH_Ec2/Ior} = -18 \text{ dB}$	2. <u>Total PICH Ec/Ior = -15 dB</u>
DPCH	Test dependent power Total power from both antennas	STTD applied 2. Total power from both antennas
<u>OCNS</u>	<u>Necessary power so that total</u> <u>transmit power spectral</u> <u>density of BS (Ior) adds to one</u>	1. <u>This power shall be divided</u> equally between antennas

C.3.4 Connection with closed loop transmit diversity mode

Table C.3.4 is applicable for measurements for clause 8.6.2(Demodulation of DCH in closed loop transmit diversity mode)

Physical Channel	Power	Note				
CPICH (antenna 1)	$\underline{\text{CPICH}_\text{Ec1/Ior}} = -13 \text{ dB}$	1. <u>Total CPICH Ec/Ior = -10</u>				
CPICH (antenna 2)	$\underline{\text{CPICH Ec2/Ior}} = -13 \text{ dB}$	<u>dB</u>				
PCCPCH (antenna 1)	$\underline{PCCPCH \ Ec1/Ior} = -15 \ dB$	1. <u>STTD applied</u>				
PCCPCH (antenna 2)	$\underline{PCCPCH_Ec2/Ior} = -15 \text{ dB}$	1. <u>STTD applied, total</u> <u>PCCPCH Ec/Ior = -12 dB</u>				
SCH (antenna 1 / 2)	$\underline{\text{SCH Ec/Ior}} = -12 \text{ dB}$	1. <u>TSTD applied</u>				
PICH (antenna 1)	$\underline{\text{PICH Ec1/Ior}} = -18 \text{ dB}$	1. <u>STTD applied</u>				
PICH (antenna 2)	$\underline{PICH}\underline{Ec2/Ior} = -18 \text{ dB}$	2. <u>STTD applied, total</u> <u>PICH_Ec/Ior = -15 dB</u>				
DPCH	Test dependent power Total power from both antennas	1. Total power from both antennas				
<u>OCNS</u>	<u>Necessary power so that total</u> <u>transmit power spectral density</u> <u>of BS (Ior) adds to one</u>	1. This power shall be divided equally between antennas				

Table C.3.4. Downlink Physical Channels transmitted during a connection.¹

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6.6.2.2 Adjacent Channel Leakage power Ratio (ACLR)

Adjacent Channel Leakage power Ratio (ACLR) is the ratio of the transmitted power to the power measured after a receiver filter in the adjacent channel(s). Both the transmitted power and the received power are measured with a filter that has a Root-Raised Cosine (RRC) filter response with roll-off $\alpha = 0.22$ and a bandwidth equal to the chip rate.

6.6.2.2.1 Minimum requirement

The ACLR shall be better than the value specified in Table 8

Power Class	UE channel	ACLR limit
<u>3</u>	<u>+ 5 MHz or – 5 MHz</u>	<u>33 dB or –50 dBm which ever is</u>
		higher
<u>3</u>	<u>+ 10 MHz or -10 MHz</u>	43 dB or –50 dBm which ever is
		higher
4	+ 5 MHz or - 5 MHz	33 dB or -50 dBm which ever is
		higher
4	+ 10 MHz or -10 MHz	43 dB or -50 dBm which ever is
		higher

Table	8:UE	ACLR
-------	------	------

Note

- 1. The ACLR due to switching transients shall not exceed the limits in Table 8.
- 2. The ACLR requirements reflect what can be achieved with present state of the art technology.
- 3. Requirement on the UE shall be reconsidered when the state of the art technology progresses.

7.5 Adjacent Channel Selectivity (ACS)

Adjacent Channel Selectivity (ACS) is a measure of a receiver's ability to receive a W-CDMA signal at its assigned channel frequency in the presence of an adjacent channel signal at a given frequency offset from the center frequency of the assigned channel. ACS is the ratio of the receive filter attenuation on the assigned channel frequency to the receive filter attenuation on the adjacent channel(s).

7.5.1 Minimum requirement

The ACS shall be better than the value indicated in Table 14a for the test parameters specified in Table 14b where the BER shall not exceed 0.001

Table 14a: Aujacent Channel Selectivity				
Power Class	Unit	ACS		
<u>3</u>	dB	<u>33</u>		
4	dB	33		

Table 14a: Adjacent Channel Selectivity

Parameter	Unit	Level
$\frac{PCCPCH_Ec}{I_{or}}$	dB	-0.46
$\frac{DPCH_Ec}{I_{or}}$	dB	-10
Î _{or}	dBm/3.84 MHz	-93
I _{oac}	dBm/3.84 MHz	-52
F _{uw} (modulated)	MHz	+5 or -5

Table 14b: Test parameters for Adjacent Channel Selectivity

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6.4.1 Open loop power control

Open loop power control is the ability of the UE transmitter to sets its output power to a specific value. The UE open loop power control tolerance is given in Table 4

Table 4	I: Open I	oop power	control
			0.475

Normal conditions	$\pm 9 \text{ dB}$
Extreme conditions	± 12 dB

6.4.2 Inner loop power control in the uplink

Inner loop power control in the uplink is the ability of the UE transmitter to adjust its output power in accordance with <u>one or more the TPC symbols commands</u> received in the downlink.

6.4.2.1 Power control steps

The power control step is the minimum step-change in the <u>ULUE</u>-_ transmitter output power in response to a power control commandsingle TPC command, TPC cmd, derived at the UE.

6.4.2.1.1 Minimum requirement

The UE transmitter shall have the capability of setting changing the inner loop output power with a step sizes of 1, 2 and 3 dB according to the value of Δ_{TPC} or $\Delta_{\text{RP-TPC}}$, in the slot immediately after the TPC_cmd can be derived.

- (a) The tolerance of the transmitter output power step due to inner loop power control shall be within the range shown in Table 5.
- (b) The tolerance of the transmitter average output power step due to inner loop power control shall be within the range shown in Table 6.

TPC cmd Power control Transmitter power control tolerancerange commands in the forward Transmitter power control tolerancerange						
links	1 dB ste	ep size	2 dB ste	p size	3 dB step size	
	Lower	Upper	Lower	Upper	Lower	Upper
<u>+1</u> Up	+0.5 dB	+1.5 dB	+1 dB	+3 dB	+1.5 dB	+4.5 dB
<u>0</u>	<u>-0.5 dB</u>	<u>+0.5 dB</u>	<u>-0.5 dB</u>	<u>+0.5 dB</u>	<u>-0.5 dB</u>	<u>+0.5 dB</u>
<u>-1 Down</u>	-0.5 dB	-1.5 dB	-1 dB	-3 dB	-1.5 dB	-4.5 dB

Table 5: Transmitter power control tolerance range

Table 6: Transmitter average power control tolerancerange

TPC cmd Power control commands in the forward links	Transmitter power control tolerance-range after 10 equal commands <u>TPC_cmd (up or down)</u>						
	1 dB ste	ep size	2 dB ste	p size	3 dB step size		
	Lower	Upper	Lower	Upper	Lower	Upper	
<u>+1</u> Up	+8 dB	+12 dB	+16 dB	+24 dB	+24 dB	+36 dB	
<u>0</u>	<u>-2 dB</u>	<u>+ 2 dB</u>	<u>-2 dB</u>	<u>+ 2 dB</u>	<u>-2 dB</u>	<u>+ 2 dB</u>	
- <u>1</u> Down	-8 dB	-12 dB	-16 dB	-24 dB	-24 dB	-36 dB	

6.4.3 Minimum transmit output power

The minimum controlled output power of the UE is when the power control setting is set to a minimum value. This is when both the inner loop and open loop power control indicate a minimum transmit output power is required.

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6.5.3 Change of TFC

A change of TFC (Transport Format Combination) in uplink means that the power in uplink varies according the change of data rate. DTX, where the DPDCH is turned off, is a special case of variable data rate, which is used to minimize the interference between UE(s) by reducing the UE transmit power when voice, user or control information is not present.

6.5.3 Transmit Discontinuous Transmission (DTX)

DTX is used to minimize the interference between UE(s) by reducing the UE transmit power when voice, user or control information is not present.

6.5.3.1 Minimum requirement

A change of output power is required when the TFC, and thereby the datarate, is changed. The ratio of the amplitude between the DPDCH codes and the DPCCH code will vary. The power step due to a change in TFC shall be calculated in the UE so that the power transmitted on the DPCCH shall follow the inner loop power control. The power step shall then be rounded to the closest integer dB value. The accuracy of the power step, given the step size is specified in Table 5.

Table 5: Transmitter power step tolerance						
Power control step size (Up or down)	Transmitter power step tolerance					
<u>ΔΡ [dB]</u>	[<u>dB</u>]					
<u>1</u>	<u>+/- 0.5 dB</u>					
2	<u>+/- 1.0 dB</u>					
3	<u>+/- 1.5 dB</u>					
<u>4≤∆P≤10</u>	<u>+/- 2 dB</u>					
<u>11≤ΔP≤15</u>	<u>+/- 3 dB</u>					
$\underline{16 \leq \Delta P \leq 20}$	<u>+/- 4 dB</u>					
<u>21≤∆P</u>	<u>+/- 6 dB</u>					

Table 5. Transmitter newer stan telerance

The transmit power levels versus time should meet the mask specified in figure 1. When power increases the power step shall be performed before the frame boundary, when power decreases the power step shall be performed after the frame boundary.

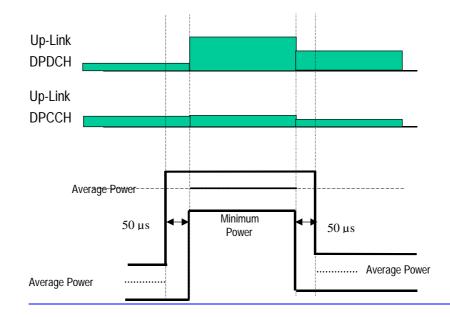


Figure 1 Transmit template during TFC Change.

The transmit DTX template is specified when Uplink Dedicated Physical Data Channel (DPDCH) is turned OFF and when the DPDCH is turned ON. With reference to the template specified when the DPDCH is turned OFF (a) and when the DPDCH is turned ON (b)

Note

1.Px [dB] and Py [dB] is the average power of 1 slot excluding the 50µs transient period 2.Pt [dB] is the average power during the period of the 50µs transient 3.Py – Px should be within ±2dB of the theoretical power change 4.Pt should be between Px and Py

5.* Theoretical power change is specified by the Gain factor β (see 25.213v x.y.z section 4.2.1)

(a)DTX template when DPDCH is turned OFF

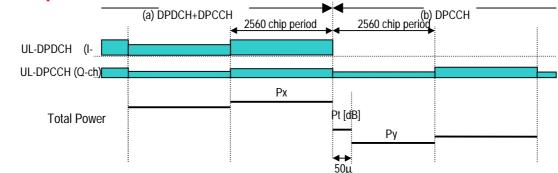


Figure 2a; Uplink Transmit DTX (DPDCH is turned OFF)

Table 6a: the values of Gain Factor β and theoretical power change

	(a) DPDCH+DPCCH	(b) DPCCH
DPDCH Gain	1	θ
DPCCH Gain	0.5	0.5
Theoretical power change	-7 d	₿

(b) DTX template when DPDCH is turned ON

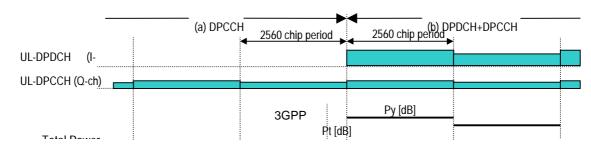


Figure 2b; Uplink Transmit DTX (DPDCH is turned ON)

Table 6b: the values of Gain Factor β and theoretical power change

	(a) DPCCH	(b) DPDCH+DPCCH
DPDCH Gain	θ	1
DPCCH Gain	0.5	0.5
Theoretical power change	7-	d₿

6.5.4 Power setting in uplink compressed mode

Compressed mode in uplink means that the power in uplink is changed.

6.5.4.1 Minimum requirement

A change of output power is required during uplink compressed frames since the transmission of data is performed in a shorter interval. The ratio of the amplitude between the DPDCH codes and the DPCCH code will also vary. The power step due to compressed mode shall be calculated in the UE so that the energy transmitted on the pilot bits during each transmitted slot shall follow the inner loop power control. Thereby the power step during the transmitted part of a compressed frame shall be such that the power on the DPCCH follows the inner loop power control with an additional power offset during a compressed frame of $N_{pilot,N}$ is the number of pilot bits per slot when in compressed mode, and $N_{pilot,N}$ is the number of pilot bits per slot in normal mode.

The power step shall then be rounded to the closest integer dB value. The accuracy of the power step, given the step size is specified in Table 5 in paragraph 6.5.3.1.

<u>The transmit power levels versus time shall meet the mask specified in figure 1. When power increases the power step shall be performed before the actual slot boundary, when power decreases the power step shall be performed after the actual slot boundary.</u>

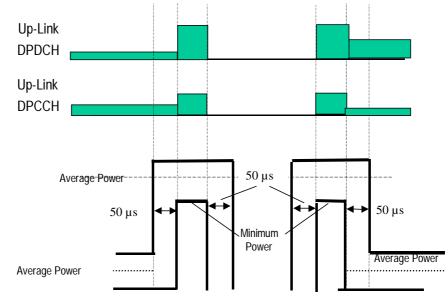


Figure 2 Transmit template during Compressed mode .

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5.2 Frequency bands

UTRA/FDD is designed to operate in either of the following paired bands;

(a) 1920 – 1980MHz:	Up-link (Mobile transmit, base receive)
2110 – 2170MHz:	Down-link (Base transmit, mobile receive)
(b)* 1850 – 1910MHz:	Up-link (Mobile transmit, base receive)
1930 – 1990MHz:	Down-link (Base transmit, mobile receive)

Note: Appropriate adjustment is required for the parameters in the specified band

* Used in Region 2

Additional allocations in ITU region 2 are FFS

Deployment in other frequency bands is not precluded.

5.3 TX–RX frequency separation

- (a) The minimum transmit to receive frequency separation is 134.8 MHz and the maximum value is 245.2 MHz-when operating in the paired band defined in sub-clause 5.2 (a). All and all UE(s) shall support a TX –RX frequency separation of 190 MHz when operating in the paired band defined in sub clause 5.2(a).
- (b) When operating in the paired band defined in sub-clause 5.2(b), all UE(s) shall support a TX RX frequency separation of 80 MHz.
- (c) UTRA/FDD can support both fixed and variable transmit to receive frequency separation.
- (d) The use of other transmit to receive frequency separations in <u>existing or</u> other frequency bands shall not be precluded-

5.4 Channel arrangement

5.4.1 Channel spacing

The nominal channel spacing is 5 MHz, but this can be adjusted to optimize performance in a particular deployment scenario.

5.4.2 Channel raster

The channel raster is 200 kHz, which means that the center frequency must be an integer multiple of 200 kHz.

5.4.3 Channel number

I

The carrier frequency is designated by the UTRA Absolute Radio Frequency Channel Number (UARFCN). The value of the UARFCN in the IMT2000 band is defined as follows;

	Uplink	$N_u = 5 * (F_{uplink} - 1885.2 - MHz)$	$\frac{0.0 _{1885.2-\text{MHz}} \leq F_{\text{uplink}} \leq \underline{3276.6 \text{ MHz}} \frac{2024.8}{2024.8}$				
1			where F_{uplink} is the uplink frequency in MHz				
	Downlink	$N_d = 5 * (F_{downlink} - 2075.2)$ MHz)	$\frac{0.0}{2110.2} \text{-MHz} \le F_{\text{uplinkdownlink}} \le 3276.6 \text{ MHz} \frac{2199.8}{2199.8}$				
1			where F_{downlink} is the downlink frequency in MHz				

Table 1: UTRA Absolute Radio Frequency Channel Number

7.6 Blocking characteristics

The blocking characteristic is a measure of the receiver's ability to receive a wanted signal at its assigned channel frequency in the presence of an unwanted interferer on frequencies other than those of the spurious response or the adjacent channels, without this unwanted input signal causing a degradation of the performance of the receiver beyond a specified limit. The blocking performance shall apply at all frequencies except those at which a spurious response occur.

7.6.1 Minimum requirement

The BER shall not exceed 0.001 for the parameters specified in Table 15 and Table 16. For Table 16 up to (24) exceptions are allowed for spurious response frequencies in each assigned frequency channel when measured using a 1MHz step size

Table 15: In-band blocking								
Parameter	Unit	Offset	Offset					
$\frac{PCCPCH_Ec}{I_{or}}$	dB	-1	-1					
DPCH_Ec I _{or}	dB	-7	-7					
Î _{or}	dBm/3.84 MHz	-107	-107					
$I_{blocking}$ (modulated)	dBm/3.84 MHz	-56	-44					
Blocking offset	MHz	10< f-fo <15	f-fo ≥15					

Table 15: In-band blocking

Parameter	Unit	Band 1	Band 2	Band 3
PCCPCH_Ec I _{or}	dB	-1	-1	-1
DPCH_Ec I _{or}	dB	-7	-7	-7
Î _{or}	dBm/3.84 MHz	-107	-107	-107
I _{blocking} (CW)	dBm	-44	-30	-15
Blocking offset <u>Fuw</u> <u>For operation in</u> <u>frequency bands as</u> <u>defined in sub-</u> <u>clause 5.2(a)</u>	MHz	2050 <f <2095<br="">2185<f <2230<="" td=""><td>2025 <f <2050<br="">2230 <f <2255<="" td=""><td>1< f <2025 2255<f<12750< td=""></f<12750<></td></f></f></td></f></f>	2025 <f <2050<br="">2230 <f <2255<="" td=""><td>1< f <2025 2255<f<12750< td=""></f<12750<></td></f></f>	1< f <2025 2255 <f<12750< td=""></f<12750<>
<u>Euw</u> <u>For operation in</u> <u>frequency bands as</u> <u>defined in sub-</u> <u>clause 5.2(b)</u>	<u>MHz</u>	<u>1870 <f<1915< u=""> 2005 <f<2050< td=""><td><u>1845 <f 1870<="" <="" u=""> 2050 < f <2075</f></u></td><td><u>1 < f < 1845</u> 2075 <f 12750<="" <="" td=""></f></td></f<2050<></f<1915<></u>	<u>1845 <f 1870<="" <="" u=""> 2050 < f <2075</f></u>	<u>1 < f < 1845</u> 2075 <f 12750<="" <="" td=""></f>

Note:

On frequency regions For operation in bands referenced in 5.2(a), from 2095 <f< 2110 MHz and 2170<f< 2185 MHz, the appropriate in-band blocking or adjacent channel selectivity in section 7.5.1 shall be applied.

2. For operation in bands referenced in 5.2(b), from 1915 <f< 1930 MHz and 1990 <f< 2005 MHz, the appropriate in-band blocking or adjacent channel selectivity in section 7.5.1 shall be applied.

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1. Text proposal for chapter 8.6.3

8.6.3 Demodulation of DCH in Site Selection Diversity Transmission mode

<Editor's note: This section may be moved to TS25.103.>

The bit error characteristics of UE receiver is determined in Site Selection Diversity Transmission (SSDT) mode. Two BS emulators are required for this performance test. The delay profiles of signals received from different base stations are assumed to be the same but time shifted by 10 chip periods (2604 ns).

8.6.3.1 Minimum Requirements

For the parameters specified in Table 39, the BLER shall not exceed the piece-wise linear BLER curve specified by the points in Table 41

Parameter	Unit	Test 1	Test 2	Test 3	Test 4
$\frac{CPICH_E_c}{I_{or}} $ (for Cell 1)	dB	-10	-1 <u>3</u> 0	-10 +delta	-10 +delta
$\frac{CPICH_{-E_c}}{I_{or}} $ (for Cell 2)	dB	-10 +delta	-10 +delta	-10	-1 <u>3</u> 0
$\frac{\underline{DPCH}_{-}E_{c1}}{I_{or}} / \frac{\underline{DPCH}_{-}E_{c2}}{I_{or}}$ (Cell 1)*	dB	<u>0</u>	<u>-3</u>	<u>0</u>	<u>+3</u>
$\frac{DPCH_E_c}{I_{or}}$	dB				
\hat{I}_{or1}/I_{oc} and \hat{I}_{or2}/I_{oc}	dB	<u>9</u>	<u>6</u>	<u>9</u>	<u>9</u>
\hat{I}_{or2}/I_{oc}	<u>dB</u>	<u>9</u>	<u>9</u>	<u>9</u>	<u>6</u>
I _{oc}	dBm/3.84 MHz		<u>-(</u>	<u>50</u>	
Information Data Rate	kbps	12.2	12.2	12.2	12.2
$\frac{DCH}{E_b}/N_t$	dB				
Number of FBI bits assigned to "S" Field		<u>1</u>	<u>1</u>	2	2
Code word Set		Long	Long	<u>Short</u>	<u>Short</u>

Table 39: DCH parameters in multi-path propagation conditions during SSDT mode (Propagation condition: Case 1)

*Note: DPCH_Ec/Ior value applies whenever DPDCH in the cell is transmitted.

Where the 4 test modes are defined as follows: Table 40: Test mode parameter

	Table 40: Test mode parameter						
Test Mode	Delta (dB)	Number of FBI bits	Cell ID				
		assigned to "S" Field	Code word Set				
Test 1	0	+	Long				
Test 2	3	4	Long				
Test 3	0	2	Short				

Test 4	3	2	Short
	•	~	Diloit

Table 41: DCH requirements in multi-path propagation conditions during SSDT Mode

Test Number	$\frac{\frac{DPCH_E_c}{I_{or}}}{\frac{DCH_E_b}{N_t}}$	BLER
1		<u>10⁻¹</u>
		10^{-2}
2		10^{-1}
		10^{-2}
3		10^{-1}
		10^{-2}
4		10^{-1}
		10^{-2}

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Annex B (normative): Propagation conditions

B.1 General

B.2 Propagation Conditions

B.2.1 Static propagation condition

The propagation for the static performance measurement is an Additive White Gaussian Noise (AWGN) environment. No fading and multi-paths exist for this propagation model.

B.2.2 Multi-path fading propagation conditions

Table B2 shows propagation conditions that are used for the performance measurements in multi-path fading environment. All taps have classical Doppler spectrum.

Case 1, sp	eed 3km/h	Case 2, s	peed 3 km/h	Case 3, 120 km/h		
Relative Delay [ns]	Average Power [dB]	Relative Delay [ns]	Average Power [dB]	Relative Delay [ns]	Average Power [dB]	
0	0	0	0	0	0	
976	-10	976	0	260	-3	
		<u>12000</u> 20000	0	521	-6	
				781	-9	

Table B2: Propagation Conditions for Multi path Fading Environments

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4 General

4.1 Measurement uncertainty

The requirements given in these specifications are absolute. Compliance with these requirements is determined by comparing the measured values with the specified limits, without making allowance for measurement uncertainty.

4.2 Power Classes

For the UE power classes 1 and 2, a number of RF parameter are not specified. It is intended that these are part of a later release.

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8.8 Inner loop power control in downlink

Performance of the inner loop power control in downlink is determined by the Block Error Rate (BLER) values and by the measured average transmitted DPCH_Ec/Ior value.

8.8.1 Inner loop power control in the downlink

8.8.1.1 Minimum requirements

For the parameters specified in Table 44, the BLER and DPCH_Ecd/Ior value shall not exceed the values specified in Table 45.

Note

- 1. Power control is ON during the test.
- 2. Power control step size is 1 dB.

Parameter	Unit	Test 1	Test 2	
\hat{I}_{or}/I_{oc}	dB	9	-1	
I _{oc}	dBm/3.84 MHz	-60	-60	
Information Data Rate	kbps	12.2	12.2	
TFCI	-	on	on	
Propagation Conditions		TBD	TBD	
SIR target		<u>FFS</u>	<u>FFS</u>	
DCH E_b/N_t	dB			

Table 44: Test parameters for downlink inner loop power control

Table 45: Requirements in downlink inner loop power control

Parameter	Unit	Test 1	Test 2	
$\frac{DPCH_E_c}{I_{or}}$	dB	<u>FFS</u>	<u>FFS</u>	
Target QualityBLER on_DTCH		FFS <u>0.01</u>	FFS <u>0.01</u>	
$\frac{DPCH_E_c}{I_{or}}$	%			