25th July 2007

Letter of support for Alan Taylor
3GPP CT5 JWG position of vice-chair

Dear Colleagues,

A*e*PONA supports and recommends the nomination of Alan Taylor for the position of vice-chair for the 3GPP CT5 joint working group (JWG). A*e*PONA is an Individual Member of 3GPP via the ETSI Organisational Partner and has continuously shown strong commitment to the success of 3GPP. Alan has extensive experience in the telecommunications industry in the areas of mobile, fixed line and IP networks, and in areas such as circuit switched, packet switched and multi-media systems. Alan is the A*e*PONA product engineering manager with responsibility for delivering Parlay and Parlay-X solutions, and has become an active participant in the CT5 joint working group. Alan intends to use his experience and skills to contribute fully to achieving the objectives of the CT5 JWG.

As a leading supplier of Parlay and Parlay-X solutions A*e*PONA is fully committed to the work of the CT5 JWG, and its continued work on technical specifications. In recommending the nomination of Alan for vice-chair A*e*PONA is showing its support by committing to continued active contribution to the JWG. In doing so A*e*PONA ensures that Alan has the full support of the management team to fulfil the responsibilities associated with this role. A*e*PONA is confident that Alan is fully qualified and committed to the task to fulfil the role of 3GPP CT5 JWG vice-chair.

Please do not hesitate to contact me of Alan directly for further information relavnt to this nomination.

Kind regards,

Liam McQuillan

Chief Operating Officer

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Alan Taylor

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Experience:

Apion Ltd moving to AePONA Ltd (Apr 1998 to present)

Current position: Parlay/Parlay-X Product Engineering Manager

Initially attached to the wireless data unit as technical architect and project manager for an Over The Air Provisioning (OTAP) system based on SS7 ANSI for the North American market. Project manager for various professional service solutions including HP Opencall IN applications, mobile pre-paid roaming, SMS legal intercept, etc.

Appointed engineering manager for the wireless data unit, and then the Parlay product engineering unit. This role currently involves the following main responsibilities (list not exhaustive):

- Ensuring that all aspects of the product are fully compliant with OSA/Parlay and Parlay-X specifications, and any relevant standards based specifications for network integration, e.g. SS7 ETSI and ANSI, SMPP, MM7, SIP, etc.
- Contributing to the standardisation process.
- Setting and implementing the strategy for the engineering unit.
- Ensuring adequate resource availability for projects, including support to project and programme managers.
- Establishing and maintaining appropriate channels of communication, internal and external.
- Coaching employees and setting development objectives.
- Identifying training needs and facilitating training.
- Providing engineering consultancy for customer accounts.
- Quality improvement initiatives, including improving software development processes.

BT plc, Belfast Engineering Centre (Jan '93 to Apr 1998)

Position: Senior Software Engineer and Analyst

Responsibilities:
- Design and development of a new pricing and billing system, for flexible off-switch-pricing. When the system was introduced the major benefits were to facilitate pricing of services on time rather than per unit, and to allow for complex price offerings including customer promotions. The system remains in commission for fixed line call and data services.
- Systems administration for BT software engineering centre. Responsible for all aspects of the group network, and servers.
- Oracle DBA for engineering centre systems.

Short Brothers, Defence Systems Division (March 1990 to Jan 1993)

Position: Senior electronics engineer

Development of embedded software for built in testing of fire and control systems for Starstreak close air defence.

Northern Telecom (Sept 1987 to March 1990)

Position: Senior software and electronics engineer

Main responsibilities:
- Development of system used in reliability (MTBF) calculation for hardware assemblies.
- Development of electronic circuits/modules for 34 Mbit/s optical fibre transceiver.
- Design of a fibre optic transmission network management system.

Qualifications:

Dublin University, Trinity College (Graduated 1987)

Electronic Engineering BAI Honours
Pure Mathematics BA

Member Institute Engineers Ireland (MIEI)