

**3GPP TSG\_CN#7**  
**ETSI SMG3 Plenary Meeting #7,**  
**Madrid, Spain**  
**13<sup>th</sup> – 15<sup>th</sup> March 2000**

**NP-000068**

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**Agenda item:** 5.2.3  
**Source:** TSG\_N WG2  
**Title:** CRs to 3G Work Item GSM/UMTS interworking

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**Introduction:**

This document contains “1” CR on **Work Item GSM/UMTS interworking**, that has been agreed by **TSG\_N WG2**, and is forwarded to **TSG\_N Plenary meeting #7** for approval.

TDoc	SPEC	CR	REV	CAT	Rel	Old vers	New vers	SUBJECT
N2B000418	23.003	018		F	R99	3.3.0		Coding of a deleted P-TMSI signature

<b>CHANGE REQUEST</b>		<i>Please see embedded help file at the bottom of this page for instructions on how to fill in this form correctly.</i>	
<b>23.003 CR 018</b>		Current Version: <b>3.3.0</b>	
GSM (AA.BB) or 3G (AA.BBB) specification number ↑		↑ CR number as allocated by MCC support team	
For submission to: <b>TSG CN#7</b>	for approval <input checked="" type="checkbox"/>	strategic <input type="checkbox"/>	(for SMG use only)
<i>list expected approval meeting # here</i>	for information <input type="checkbox"/>	non-strategic <input checked="" type="checkbox"/>	

Form: CR cover sheet, version 2 for 3GPP and SMG    The latest version of this form is available

**Proposed change affects:**    (U)SIM     ME     UTRAN / Radio     Core Network   
*(at least one should be marked with an X)*

**Source:**    N2    **Date:**    2000-02-20

**Subject:**    Coding of a deleted P-TMSI signature

**Work item:**    GSM/UMTS interworking

<b>Category:</b>	F Correction <input checked="" type="checkbox"/>	<b>Release:</b>	Phase 2 <input type="checkbox"/>
	A Corresponds to a correction in an earlier release <input type="checkbox"/>		Release 96 <input type="checkbox"/>
<i>(only one category shall be marked with an X)</i>	B Addition of feature <input type="checkbox"/>		Release 97 <input type="checkbox"/>
	C Functional modification of feature <input type="checkbox"/>		Release 98 <input type="checkbox"/>
	D Editorial modification <input type="checkbox"/>		Release 99 <input checked="" type="checkbox"/>
			Release 00 <input type="checkbox"/>

**Reason for change:**    This CR proposes to clarify that the network shall reserve the hexadecimal value: FFFFFFFF and not assign this value in the P-TMSI signature to the MS, as the MS uses this value in order indicate on the SIM that no valid P-TMSI signature is available.

**Clauses affected:**    2.7 (new section)

<b>Other specs affected:</b>	Other 3G core specifications <input type="checkbox"/>	→	List of CRs:
	Other GSM core specifications <input type="checkbox"/>	→	List of CRs:
	MS test specifications <input type="checkbox"/>	→	List of CRs:
	BSS test specifications <input type="checkbox"/>	→	List of CRs:
	O&M specifications <input type="checkbox"/>	→	List of CRs:

**Other comments:**

## **2.4 Structure of TMSI**

Since the TMSI has only local significance (i.e. within a VLR and the area controlled by a VLR, or within an SGSN and the area controlled by an SGSN), the structure and coding of it can be chosen by agreement between operator and manufacturer in order to meet local needs.

The TMSI consists of 4 octets. It can be coded using a full hexadecimal representation.

In order to avoid double allocation of TMSIs after a restart of an allocating node, some part of the TMSI may be related to the time when it was allocated or contain a bit field which is changed when the allocating node has recovered from the restart.

In areas where both MSC-based services and SGSN-based services are provided, some discrimination is needed between the allocation of TMSIs for MSC-based services and the allocation of TMSIs for SGSN-based services. The discrimination shall be done on the 2 most significant bits, with values 00, 01, and 10 being used by the VLR, and 11 being used by the SGSN.

The TMSI shall only be allocated in ciphered form. See also GSM 03.20.

The network shall not allocate a TMSI with all 32 bits equal to 1 (this is because the TMSI must be stored in the SIM, and the SIM uses 4 octets with all bits equal to 1 for indicating that no valid TMSI is available).

To allow for eventual modifications of the management of the TMSI code space management, MSs shall not check if an allocated TMSI belongs to the range allocated to the allocating node. MSs shall use an allocated TMSI according to the specifications, whatever its value.

## **2.5 Structure of LMSI**

The LMSI consists of 4 octets and may be allocated by the VLR.

## **2.6 Structure of TLLI**

A TLLI is built by the MS or by the SGSN either on the basis of the P-TMSI (local or foreign TLLI), or directly (random or auxiliary TLLI), according to the following rules.

The TLLI consists of 32 bits, numbered from 0 to 31 by order of significance, with bit 0 being the LSB.

A local TLLI is built by a MS which has a valid P-TMSI as follows:

- bits 31 down to 30 are set to 1; and

- bits 29 down to 0 are set equal to bits 29 to 0 of the P-TMSI.

A foreign TLLI is built by a MS which has a valid P-TMSI as follows:

- bit 31 is set to 1 and bit 30 is set to 0; and

- bits 29 down to 0 are set equal to bits 29 to 0 of the P-TMSI.

A random TLLI is built by an MS as follows:

- bit 31 is set to 0;

- bits 30 down to 27 are set to 1; and

- bits 0 to 26 are chosen randomly.

An auxiliary TLLI is built by the SGSN as follows:

- bit 31 is set to 0;

- bits 30 down to 28 are set to 1;

- bit 27 is set to 0; and

- bits 0 to 26 can be assigned independently.

Other types of TLLI may be introduced in the future.

The structure of the TLLI is then summarised by the following table:

**Table A: TLLI structure**

31	30	29	28	27	26 to 0	Type of TLLI
1	1	T	T	T	T	Local TLLI
1	0	T	T	T	T	Foreign TLLI
0	1	1	1	1	R	Random TLLI
0	1	1	1	0	A	Auxiliary TLLI
0	1	1	0	X	X	Reserved
0	1	0	X	X	X	Reserved
0	0	X	X	X	X	Reserved

'T', 'R', 'A' and 'X' indicate bits which can take any value for the type of TLLI. More precisely, 'T' indicates bits derived from a P-TMSI, 'R' indicates bits chosen randomly, 'A' indicates bits chosen by the SGSN and 'X' bits in reserved ranges.

## [2.7 Structure of P-TMSI Signature](#)

[The P-TMSI Signature consists of 3 octets and may be allocated by the SGSN.](#)

[The network shall not allocate a P-TMSI Signature with all 24 bits equal to 1 \(this is because the P-TMSI Signature must be stored in the SIM, and the SIM uses 3 octets with all bits equal to 1 for indicating that no valid P-TMSI signature is available.](#)